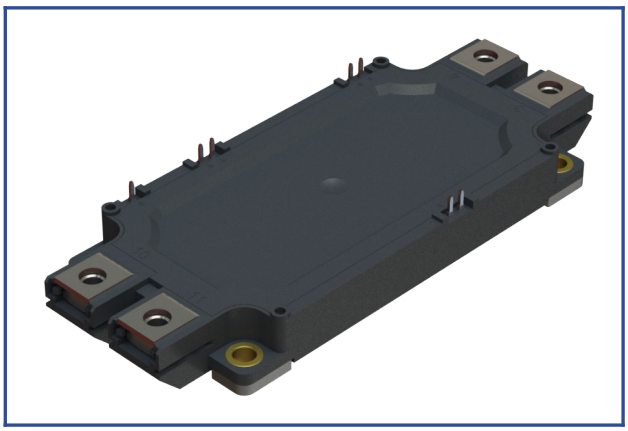
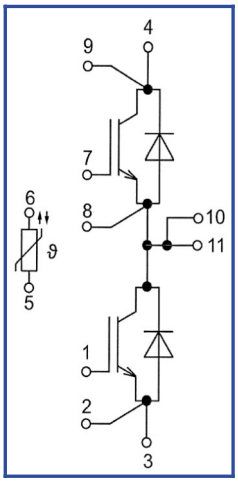
Low Inductance IGBT Module with 17 mm Height Housing

1700 V 600 A





Chip features

- IGBT chip
  - low $V_{CE(sat)}$ value
  - 10 µs short circuit duration at 150°C
  - square RBSOA of 2xI_C
- FRD chip
  - fast and soft reverse recovery
  - low voltage drop

Design features

- copper baseplate
- Al_2O_3 DBC substrate
- ultrasonic welded power terminals
- improved thermal cycling
- RoHS compliant
- low inductance value

Typical application

- AC motor drives
- solar inverters
- air conditioning
- high power converters and UPS
- Inverters for wind energy converters

Maximum rated values

Definition	Symbol	Conditions	Value	Unit
IGBT				
Collector-Emitter voltage	V_{CES}	V_{GE} = 0.	1700	V
Maximum allowable collector current (continuous)	$I_{C\ 25}$	$T_{vj\ (max)}$ = 175°C; T_c = 25°C.	689	A
	$I_{C\ 80}$	$T_{vj\ (max)}$ = 175°C; T_c = 80°C.	531	A
Repetitive peak collector current*1	I_{CRM}	I_{CRM} = 3 x $I_{C\ nom}$; t_p = 1 ms.	1800	A
Short-circuit duration	t_{psc}	T_{vj} = 25°C; V_{GE} = ±15 V; V_{CE} = 1000 V; $R_{G\ on}$ = $R_{G\ off}$ = 1.5 Ω.	10	µs
		T_{vj} = 150°C; V_{GE}= ±15 V; V_{CE}= 1000 V; $R_{G\ on}$ = $R_{G\ off}$ = 1.5 Ω.	10	
Gate-Emitter voltage	V_{GES}		±20	V
Junction operating temperature	$T_{vj\ (op)}$		-40…+150	°C
Inverse diode				
Repetitive peak reverse voltage	V_{RRM}	V_{GE} = 0 V.	1700	V
Maximum allowable forward current (continuous)	$I_{F\ 25}$	$T_{vj\ (max)}$ = 175°C; T_c = 25°C.	604	A
	$I_{F\ 80}$	$T_{vj\ (max)}$ = 175°C; T_c = 80°C.	459	A
Repetitive peak forward current*1	I_{FRM}	I_{FRM} = 3 x $I_{F\ nom}$; t_p = 1 ms.	1800	A
Junction operating temperature	$T_{vj\ (op)}$		-40…+150	°C
Module				
Storage temperature	T_{stg}		-55…+50	°C
Isolation voltage	V_{isol}	AC sin 50 Hz; t = 1 min.	4000	V

*1 Pulse width and repetition rate should be such that device junction temperature does not exceed maximum T_{vj} rating

Characteristics

Definition	Symbol	Conditions		Value min.	Value typ.	Value max.	Unit.
IGBT							
Collector-Emitter saturation voltage	V_{CEsat}	V_{GE} = +15 V; I_C = 600 A; t_u = 1000 µs.	T_{vj} = 25°C	-	-	2.70	V
			T_{vj} = 150°C	-	-	3.60	V
Gate-Emitter threshold voltage	$V_{GE(th)}$	I_C = 24 mA; V_{CE} = V_{GE}; T_{vj} = 25°C; t_u = 2 ms.		4.15	5.90	8.10	V
Collector-Emitter cut-off current	I_{CES}	V_{CE} = 1700 V; t_u = 50 ms; V_{GE} = 0.	T_{vj} = 25°C	-	11.00	300	µA
			T_{vj} = 150°C	-	1.50	5.00	mA
Gate-Emitter leakage current	I_{GES}	V_{CE} = 0; V_{GE} = ±20 V; T_{vj} = 25°C; t_u = 30 ms.		-	15.00	200	nA
Input capacitance	C_{ies}	V_{CE} = 25 V; V_{GE} = 0 V; f = 1 MHz; T_{vj} = 25°C.		-	237.60	-	nF
Reverse transfer capacitance	C_{res}			-	1.17	-	nF
Total gate charge	Q_G	I_C = 600 A; V_{CE} = 920 V; V_{GE} = -8...+15 V.		-	-	3800	nC
Internal gate resistance	R_{Gint}	T_{vj} = 25°C.		-	1.60	-	Ω
Turn-on delay time	$t_{d(on)}$	V_{CE} = 920 V; V_{GE} = ±15 V; $I_{C\,max}$ = 600 A; R_G = 1.5 Ω; L_s = 56 nH.	T_{vj} = 25°C	-	-	376	ns
			T_{vj} = 150°C	-	-	429	
Rise time	t_{ri}		T_{vj} = 25°C	-	-	108	ns
			T_{vj} = 150°C	-	-	139	
Turn-on energy	E_{on}		T_{vj} = 25°C	-	-	178	mJ
			T_{vj} = 150°C	-	-	305	
Turn-off delay time	$t_{d(off)}$		T_{vj} = 25°C	-	-	690	ns
			T_{vj} = 150°C	-	-	780	
Fall time	t_{fi}		T_{vj} = 25°C	-	-	605	ns
			T_{vj} = 150°C	-	-	915	
Turn-off energy	E_{off}		T_{vj} = 25°C	-	-	185	mJ
			T_{vj} = 150°C	-	-	258	
Collector-emitter threshold voltage	V_{CE0}	V_{GE} = +15 V; T_{vj} = 150°C; I_{CE1} = 150 A; I_{CE2} = 600 A; t_u = 1000 µs.		-	-	0.83	V
On-State slope resistance (IGBT)	r_{CE0}			-	-	4.14	mΩ
Thermal resistance junction to case	$R_{th(j-c)}$	DC; I_{test} = 1.5 A; V_{GE} = +15 V.		-	-	0.059	K/W
Inverse diode							
Forward voltage drop	V_F	I_F = 600 A; V_{GE} = 0; t_u = 1000 µs.	T_{vj} = 25°C	-	-	2.70	V
			T_{vj} = 150°C	-	-	3.20	V
Reverse recovery time	t_{rr}	V_{CE} = 920 V; V_{GE} = ±15 V; $I_{C\,max}$ = 600 A; R_G = 1.5 Ω; L_s = 56 nH.	T_{vj} = 25°C	-	-	427	ns
			T_{vj} = 150°C	-	-	1010	ns
Peak reverse current	I_{RM}		T_{vj} = 25°C	-	-	595	A
			T_{vj} = 150°C	-	-	540	A
Recovered charge	Q_r		T_{vj} = 25°C	-	-	116	µC
			T_{vj} = 150°C	-	-	231	µC
Reverse recovery energy	E_{rec}		T_{vj} = 25°C	-	-	83.5	mJ
			T_{vj} = 150°C	-	-	156	mJ
Threshold voltage	$V_{(T0)}$	T_{vj} = 150°C; V_{GE} = 0; I_{CE1} = 150 A; I_{CE2} = 600 A; t_u = 1000 µs		-	-	0.88	V
Forward slope resistance	r_T			-	-	3.38	mΩ
Thermal resistance junction to case	$R_{th(JC-D)}$	DC; I_{test} = 1.5 A; V_{GE} = 0 V.		-	-	0.085	K/W

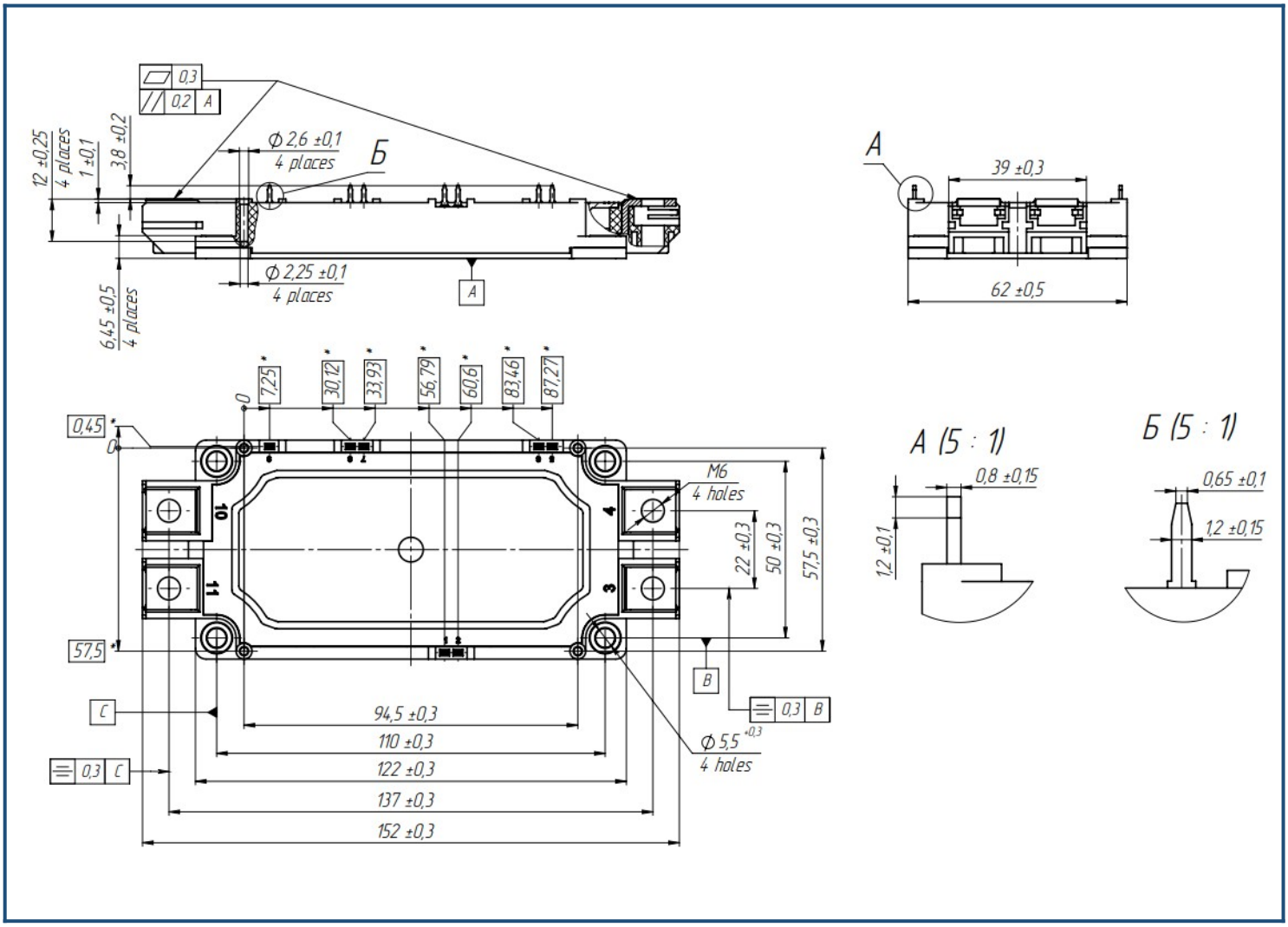
Module							
Pin resistance	R_{Pxy}	T_{vj} = 25°C.	$R_{P10/11-3}$	-	0.95	1.00	mΩ
			$R_{P10/11-4}$	-	0.68	1.00	
Parasitic inductance between terminals	L_{Pce}			-	22.00	-	nH
Thermistor resistance	R_t	T_{vj} = 25°C		4850	-	6225	Ω
		T_{vj} = 150°C		160	-	195	
Coefficient of temperature sensitivity	$B_{25/50}$	$R_2 = R_{25} \exp [B_{25/50} (1/T_2 - 1/T_1)]$, T_1 = 298,15 K		-	3375	-	K
Thermal resistance case to heatsink	R_{thCH}	per module		-	0.009	0.014	K/W
Mounting torque for screws to heatsink	M_s	to heatsink M5		3	-	6	N*m
Mounting torque for terminal screws	M_t	to terminals M6		3	-	6	N*m
Weight	W			-	-	360	g

" - " — data will be refined as additional tests are conducted and statistics are collected.

Notes:

- Insulating material operating temperature 125°C max;
- Case temperature 125°C max;
- The recommended operating junction temperature $T_{vj\ op}$ = -40...+150°C.

Overall dimensions: Package type – D2



Part numbering guide

MID2	-	HB	17	SM	-	600	N	
MID2								IGBT module package type: D2
		HB						2 switches as Half-Bridge
			17					Voltage rating (V_{CES}/100)
				SM				IGBT+FRD chipset modification
						600		Current Rating
							N	Climatic version: normal climate