



High power cycling capability
Low on-state and switching losses
Designed for traction and industrial applications

Phase Control Thyristor Type T123-500-8

Mean on-state current				I_{TAV}	500 A			
Repetitive peak off-state voltage				V_{DRM}	100...800 V			
Repetitive peak reverse voltage				V_{RRM}				
Turn-off time				t_q	80, 100, 125, 160, 200, 250, 320, 400, 500 μ s			
V_{DRM}, V_{RRM}, V	100	200	300	400	500	600	700	800
Voltage code	1	2	3	4	5	6	7	8
$T_j, ^\circ\text{C}$	-60...+150							

MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions	
ON-STATE					
I_{TAV}	Maximum allowable mean on-state current	A	500 591	$T_c=98\text{ }^{\circ}\text{C}$, Double side cooled $T_c=85\text{ }^{\circ}\text{C}$, Double side cooled 180° half-sine wave; 50 Hz	
I_{TRMS}	RMS on-state current	A	785	$T_c=98\text{ }^{\circ}\text{C}$, Double side cooled 180° half-sine wave; 50 Hz	
I_{TSM}	Surge on-state current	kA	6.5 7.5	$T_j=T_{j\max}$ $T_j=25\text{ }^{\circ}\text{C}$	180° half-sine wave; $t_p=10\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
			7.0 8.0	$T_j=T_{j\max}$ $T_j=25\text{ }^{\circ}\text{C}$	180° half-sine wave; $t_p=8.3\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
I^2t	Safety factor	$A^2s\cdot 10^3$	210 280	$T_j=T_{j\max}$ $T_j=25\text{ }^{\circ}\text{C}$	180° half-sine wave; $t_p=10\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
			200 260	$T_j=T_{j\max}$ $T_j=25\text{ }^{\circ}\text{C}$	180° half-sine wave; $t_p=8.3\text{ ms}$; single pulse; $V_D=V_R=0\text{ V}$; Gate pulse: $I_G=2\text{ A}$; $t_{GP}=50\text{ }\mu\text{s}$; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
BLOCKING					
V_{DRM}, V_{RRM}	Repetitive peak off-state and Repetitive peak reverse voltages	V	100...800	$T_{j\min}<T_j<T_{j\max}$; 180° half-sine wave; 50 Hz; Gate open	
V_{DSM}, V_{RSM}	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	200...900	$T_{j\min}<T_j<T_{j\max}$; 180° half-sine wave; single pulse; Gate open	
V_D, V_R	Direct off-state and Direct reverse voltages	V	$0.6\cdot V_{DRM}$ $0.6\cdot V_{RRM}$	$T_j=T_{j\max}$; Gate open	

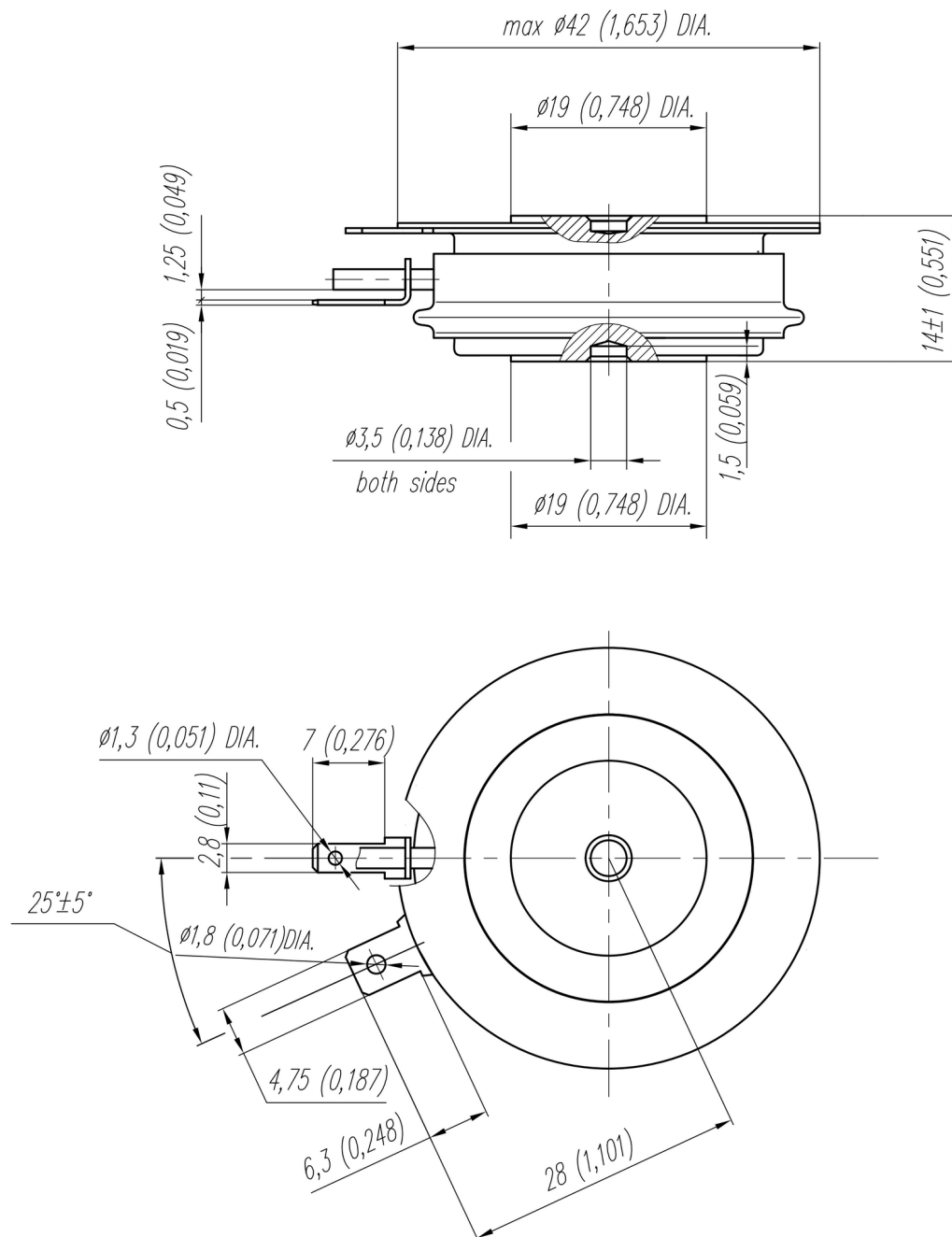
TRIGGERING				
I_{FGM}	Peak forward gate current	A	5	$T_j = T_{j\max}$
V_{RGM}	Peak reverse gate voltage	V	5	
P_G	Gate power dissipation	W	3	$T_j = T_{j\max}$ for DC gate current
SWITCHING				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive ($f=1$ Hz)	A/ μ s	200	$T_j = T_{j\max}$; $V_D = 0.67 \cdot V_{DRM}$; $I_{TM} = 1040$ A; Gate pulse: $I_G = 2$ A; $t_{GP} = 50$ μ s; $di_G/dt \geq 2$ A/ μ s
THERMAL				
T_{stg}	Storage temperature	$^{\circ}$ C	-60...+50	
T_j	Operating junction temperature	$^{\circ}$ C	-60...+150	
MECHANICAL				
F	Mounting force	kN	5.0...7.0	
a	Acceleration	m/s ²	50	Device clamped

CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
ON-STATE					
V _{TM}	Peak on-state voltage, max	V	1.60	T _j =25 °C; I _{TM} =1570 A	
V _{T(TO)}	On-state threshold voltage, max	V	0.910	T _j =T _{j max} ;	
r _T	On-state slope resistance, max	mΩ	0.455	0.5 π I _{TAV} < I _T < 1.5 π I _{TAV}	
I _L	Latching current, max	mA	500	T _j =25 °C; V _D =12 V; Gate pulse: I _G =2 A; t _{GP} =50 μs; di _G /dt≥1 A/μs	
I _H	Holding current, max	mA	250	T _j =25 °C; V _D =12 V; Gate open	
BLOCKING					
I _{DRM} , I _{RRM}	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	50	T _j =T _{j max} ; V _D =V _{DRM} ; V _R =V _{RRM}	
(dv _D /dt) _{crit}	Critical rate of rise of off-state voltage ¹⁾ , min	V/μs	200, 320, 500, 1000, 1600, 2000, 2500	T _j =T _{j max} ; V _D =0.67·V _{DRM} ; Gate open	
TRIGGERING					
V _{GT}	Gate trigger direct voltage, max	V	3.00 2.50 1.50	T _j = T _{j min} T _j =25 °C T _j = T _{j max}	V _D =12 V; I _D =3 A; Direct gate current
I _{GT}	Gate trigger direct current, max	mA	500 300 150	T _j = T _{j min} T _j = 25 °C T _j = T _{j max}	
V _{GD}	Gate non-trigger direct voltage, min	V	0.55	T _j =T _{j max} ; V _D =0.67·V _{DRM} ;	
I _{GD}	Gate non-trigger direct current, min	mA	45.00	Direct gate current	
SWITCHING					
t _{gd}	Delay time, max	μs	0.55	T _j =25 °C; V _D =600 V; I _{TM} =I _{TAV} ; di/dt=200 A/μs;	
t _{gt}	Turn-on time, max	μs	2.00	Gate pulse: I _G =2 A; V _G =20 V; t _{GP} =50 μs; di _G /dt=2 A/μs	
t _q	Turn-off time ²⁾ , max	μs	80, 100, 125, 160, 200, 250, 320, 400, 500	dv _D /dt=50 V/μs; T _j =T _{j max} ; I _{TM} = I _{TAV} ; di _R /dt=-10 A/μs; V _R =100V; V _D =0.67·V _{DRM}	
Q _{rr}	Recovered charge, max	μC	750	T _j =T _{j max} ; I _{TM} =500 A;	
t _{rr}	Reverse recovery time, max	μs	17	di _R /dt=-10 A/μs;	
I _{rr}	Reverse recovery current, max	A	88	V _R =100 V	

THERMAL					
R _{thjc}	Thermal resistance, junction to case, max	°C/W	0.070	Direct current	Double side cooled
R _{thjc-A}			0.154		Anode side cooled
R _{thjc-K}			0.126		Cathode side cooled
R _{thck}	Thermal resistance, case to heatsink, max	°C/W	0.010	Direct current	
MECHANICAL					
m	Weight, max	g	65		
D _s	Surface creepage distance	mm (inch)	7.94 (0.313)		
D _a	Air strike distance	mm (inch)	5.00 (0.197)		

PART NUMBERING GUIDE							NOTES
T	123	500	8	A2	E2	N	1) Critical rate of rise of off-state voltage
1	2	3	4	5	6	7	
1. Phase Control Thyristor							2) Turn-off time (dv _D /dt=50 V/μs)
2. Design version							
3. Mean on-state current, A							
4. Voltage code							
5. Critical rate of rise of off-state voltage, V/μs							
6. Turn-off time (dv _D /dt=50 V/μs)							
7. Ambient conditions: N – normal; T – tropical							



All dimensions in millimeters (inches)

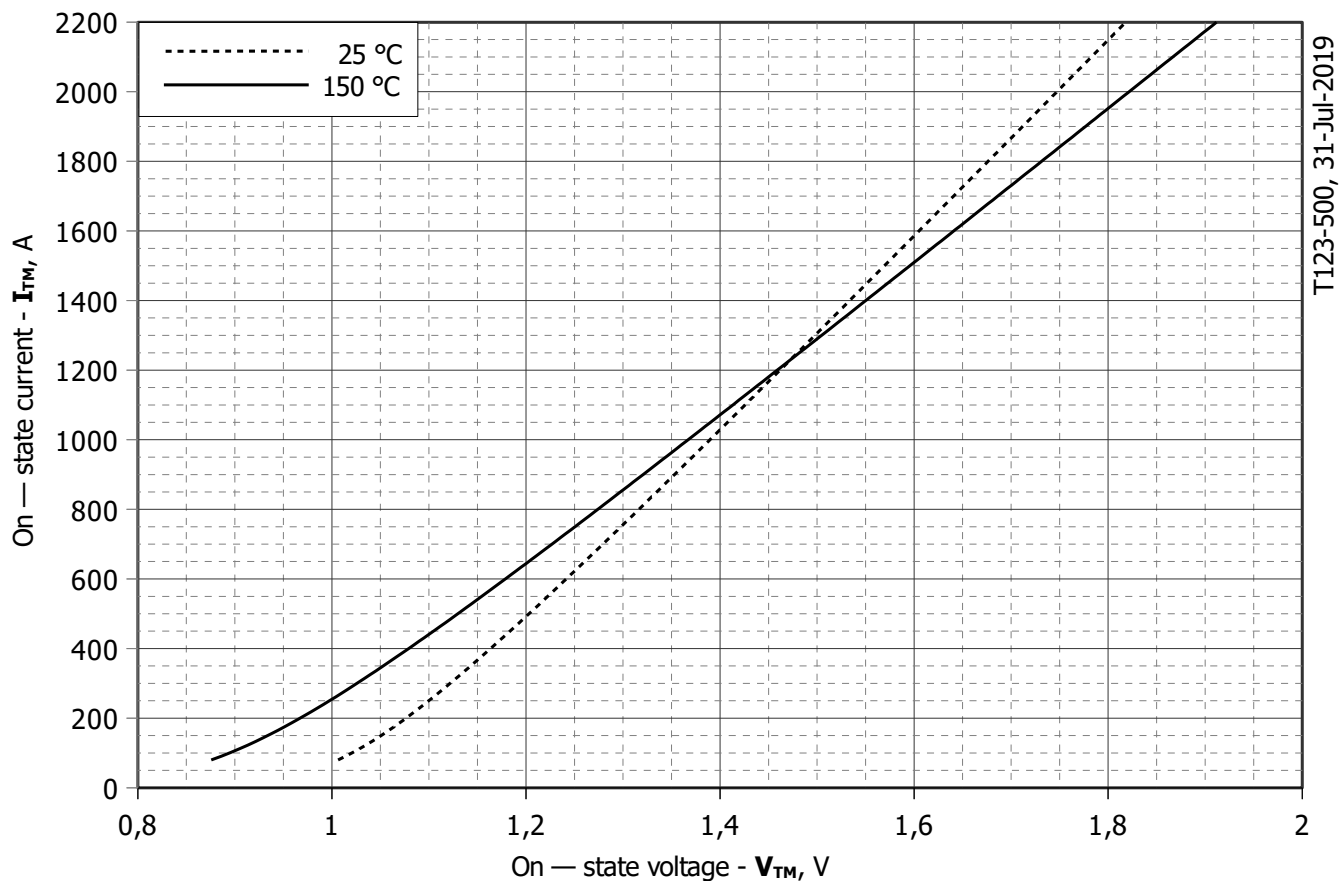


Fig 1 – On-state characteristics of Limit device

Analytical function for On-state characteristic:

$$V_T = A + B \cdot i_T + C \cdot \ln(i_T + 1) + D \cdot \sqrt{i_T}$$

	Coefficients for max curves	
	$T_j = 25^\circ\text{C}$	$T_j = T_{j\max}$
A	0.80092000	0.59468000
B	0.00036627	0.00046907
C	0.04641100	0.06461400
D	-0.00310810	-0.00452510

On-state characteristic model (see Fig. 1)

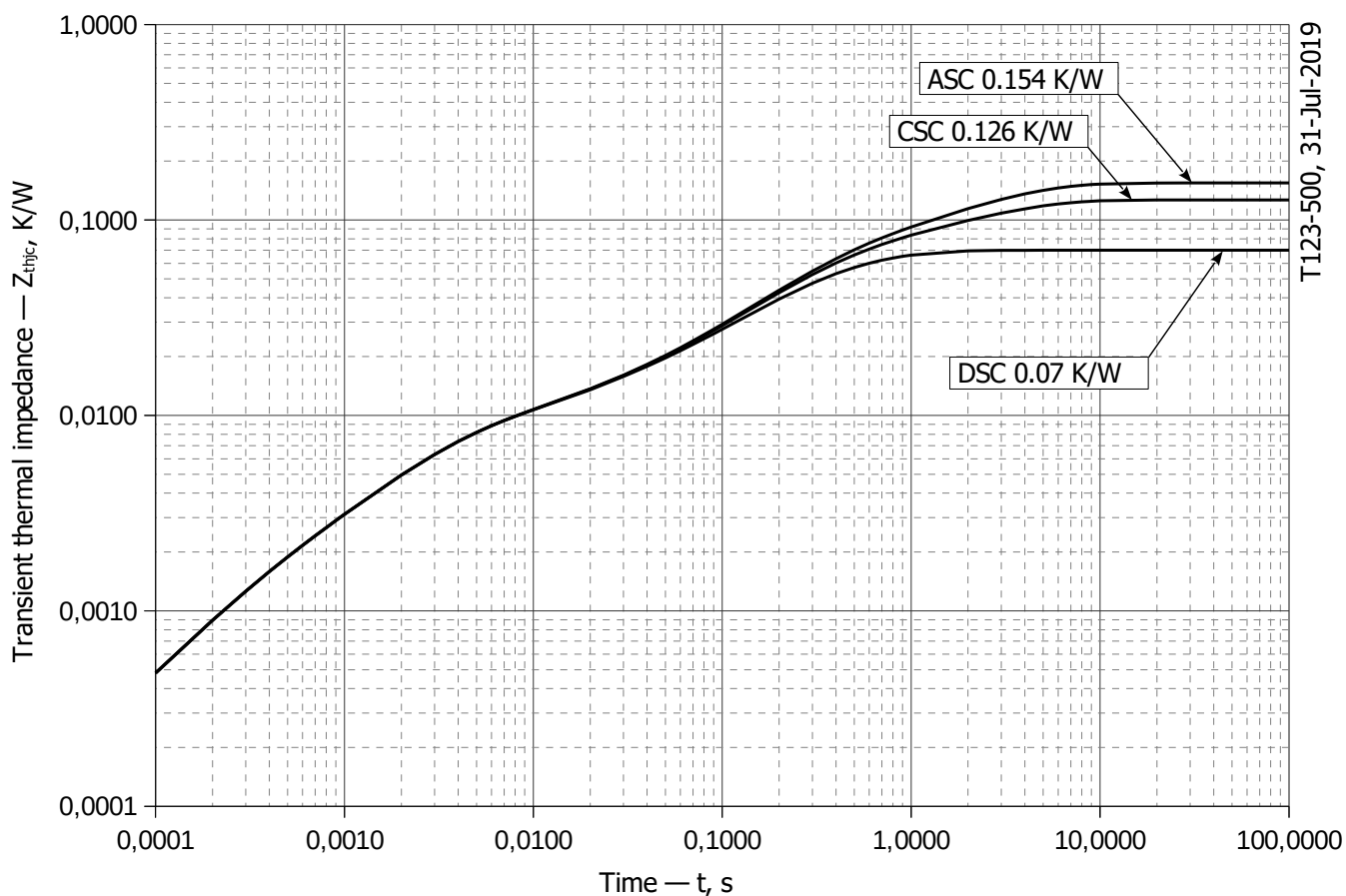


Fig 2 – Transient thermal impedance Z_{thjc} vs. time t

Analytical function for Transient thermal impedance junction to case Z_{thjc} for DC:

$$Z_{thjc} = \sum_{i=1}^n R_i \left(1 - e^{-\frac{t}{\tau_i}} \right)$$

Where $i = 1$ to n , n is the number of terms in the series.

t = Duration of heating pulse in seconds.

Z_{thjc} = Thermal resistance at time t .

R_i = Amplitude of p_{th} term.

τ_i = Time constant of r_{th} term.

DC Double side cooled

i	1	2	3	4	5	6
$R_i, K/W$	0.03233	0.02226	0.005231	0.002739	0.006738	0.0006988
τ_i, s	0.2392	0.533	0.1478	0.01499	0.002749	0.0002969

DC Anode side cooled

i	1	2	3	4	5	6
$R_i, K/W$	0.08459	0.02327	0.002598	0.006598	0.0006736	0.03694
τ_i, s	2.653	0.5669	0.01311	0.00269	0.0002871	0.2416

DC Cathode side cooled

i	1	2	3	4	5	6
$R_i, K/W$	0.05654	0.03706	0.002638	0.006637	0.0006786	0.02303
τ_i, s	2.653	0.2338	0.01361	0.002704	0.000289	0.5476

Transient thermal impedance junction to case Z_{thjc} model (see Fig. 2)

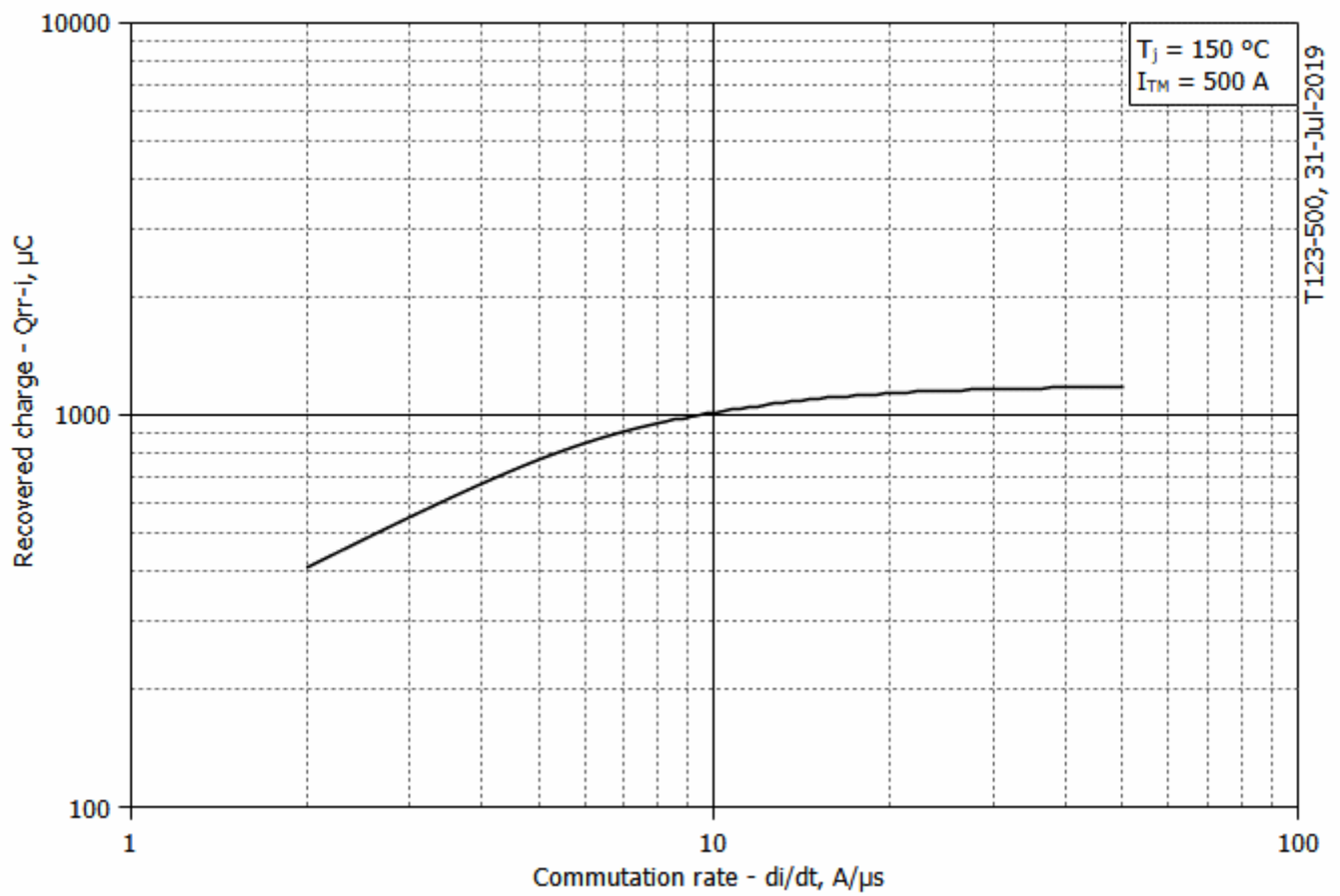


Fig 3 – Maximum recovered charge Q_{rr-i} (integral) vs. commutation rate di_R/dt

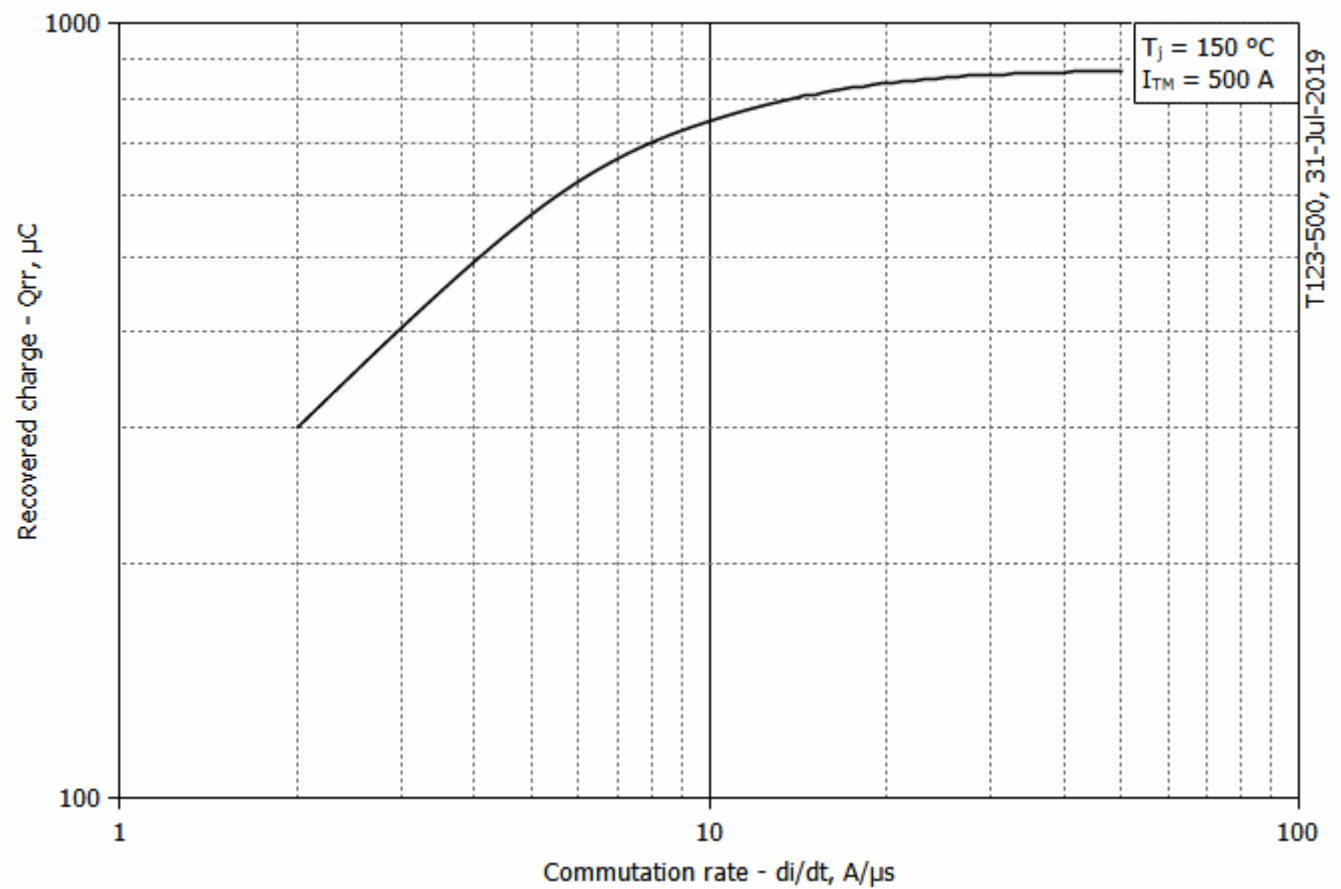


Fig 4 – Maximum recovered charge Q_{rr} vs. commutation rate di_R/dt (25% chord)

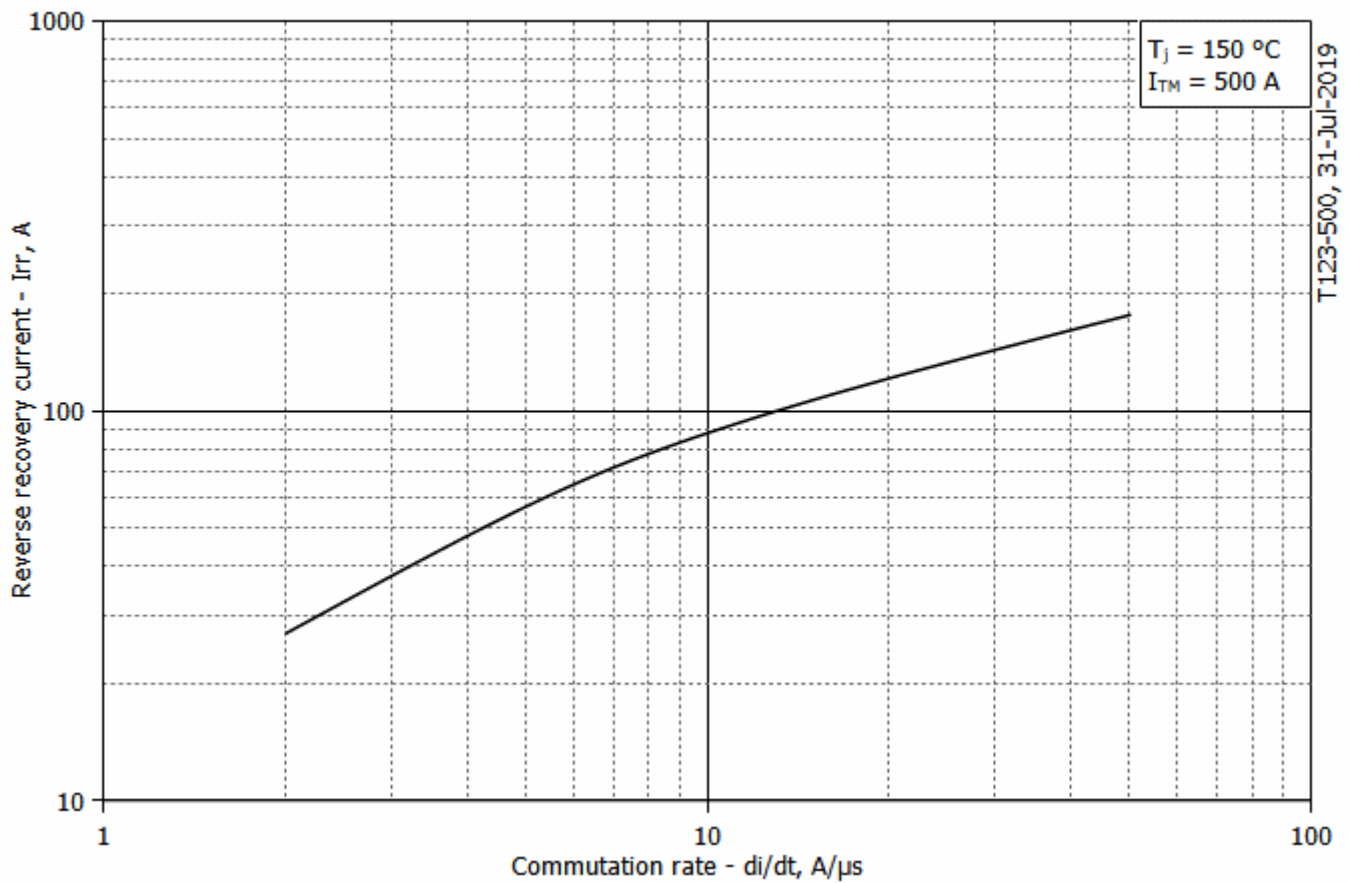


Fig 5 – Maximum reverse recovery current I_{rr} vs. commutation rate di_R/dt

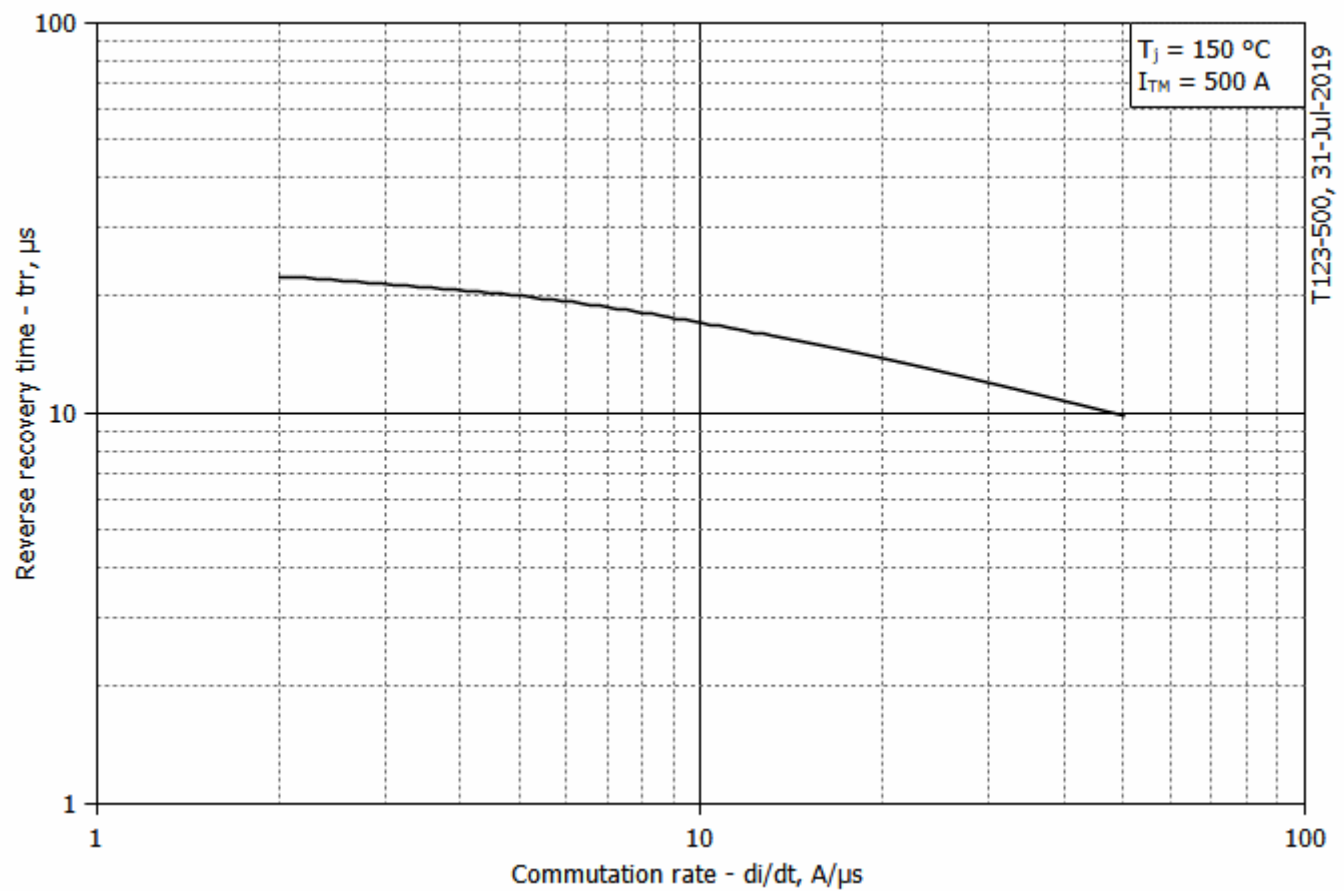
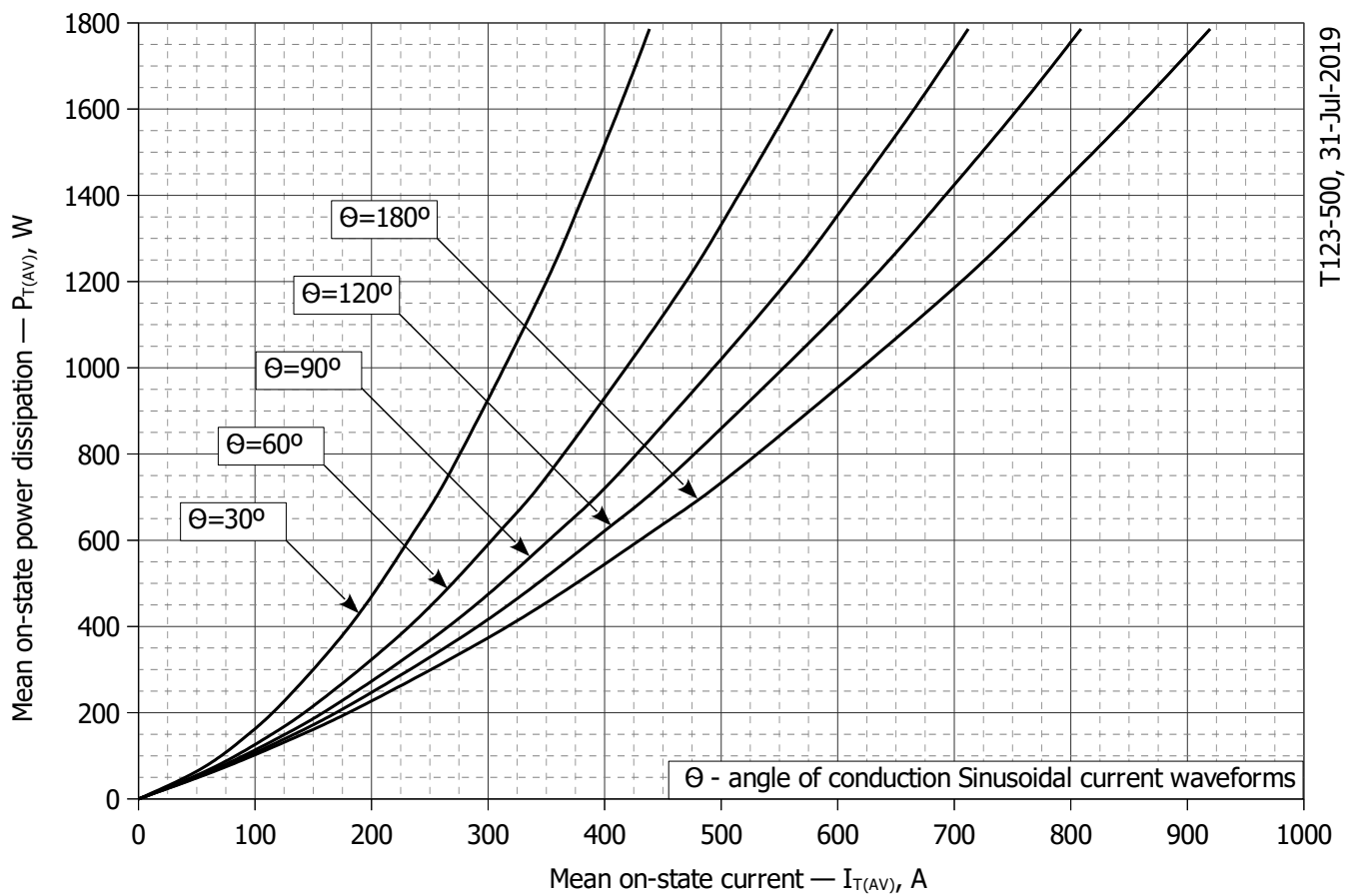
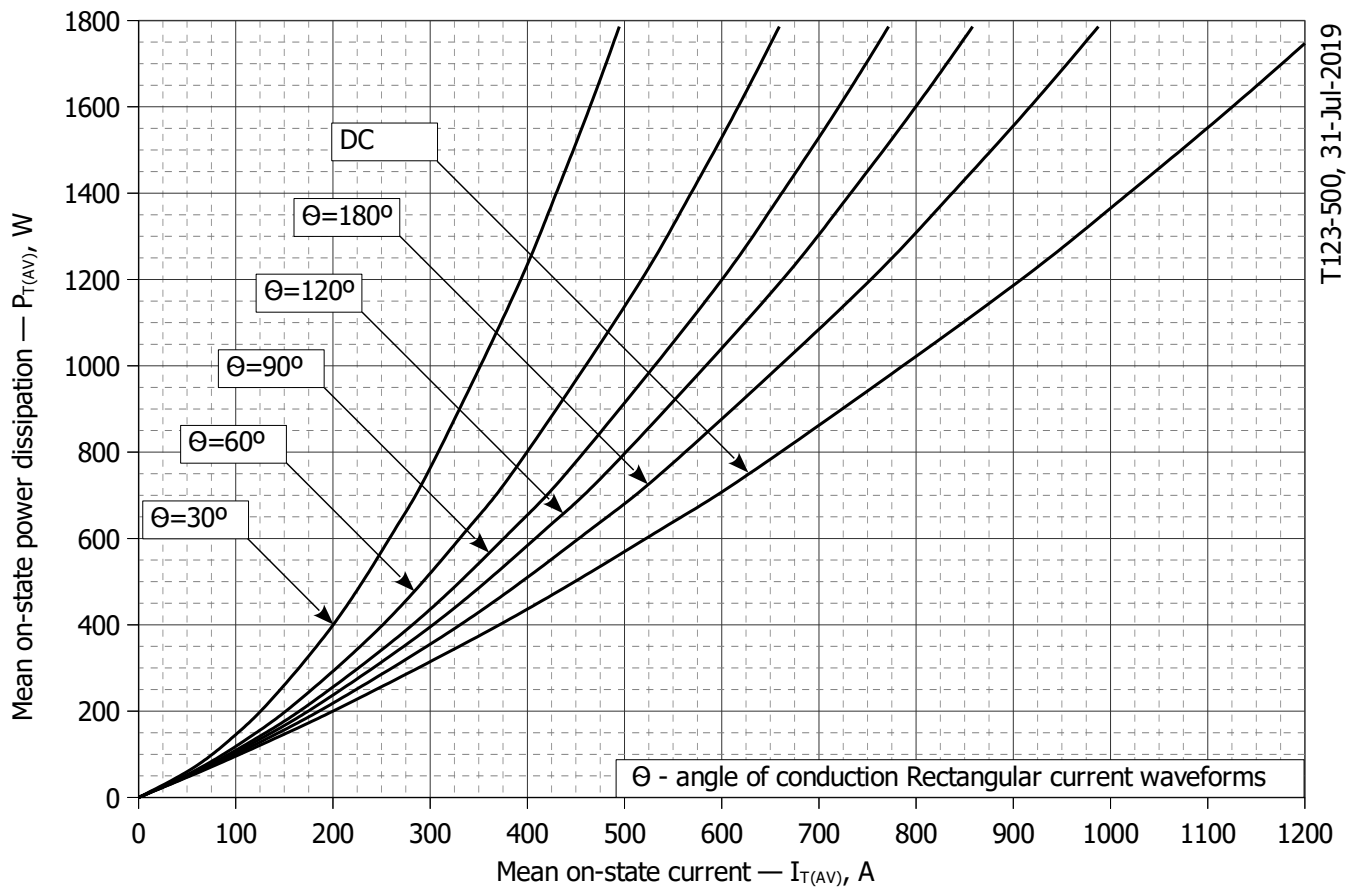


Fig 6 – Maximum recovery time t_{rr} vs. commutation rate di_R/dt (25% chord)



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Fig. 7 - Mean on-state power dissipation P_{TAV} vs. mean on-state current I_{TAV} for sinusoidal current waveforms at different conduction angles ($f=50\text{Hz}$, DSC)



T123-500, 31-Jul-2019

Fig. 8 - Mean on-state power dissipation P_{TAV} vs. mean on-state current I_{TAV} for rectangular current waveforms at different conduction angles and for DC ($f=50\text{Hz}$, DSC)

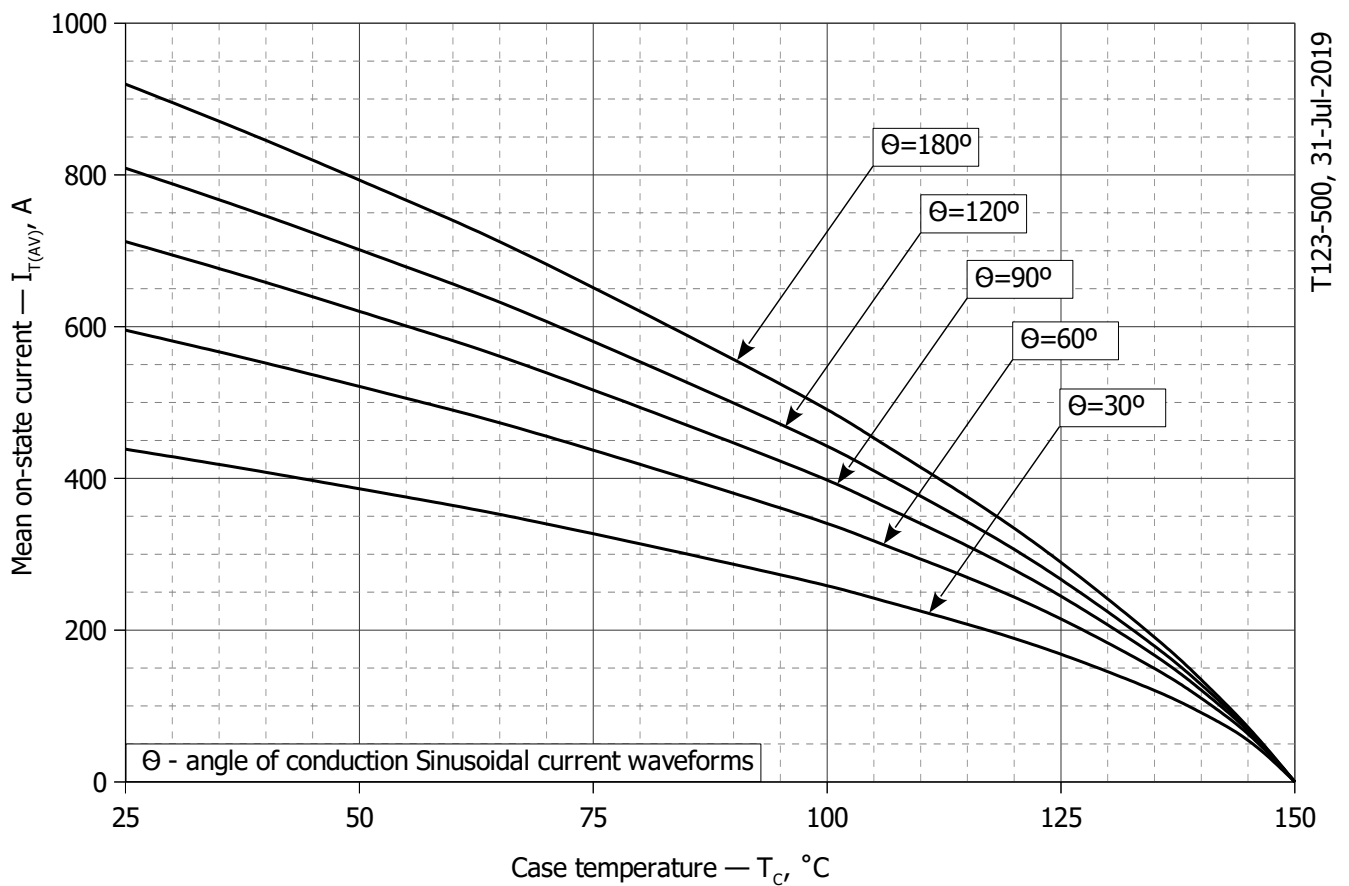


Fig. 9 – Mean on-state current I_{TAV} vs. case temperature T_C for sinusoidal current waveforms at different conduction angles ($f=50\text{Hz}$, DSC)

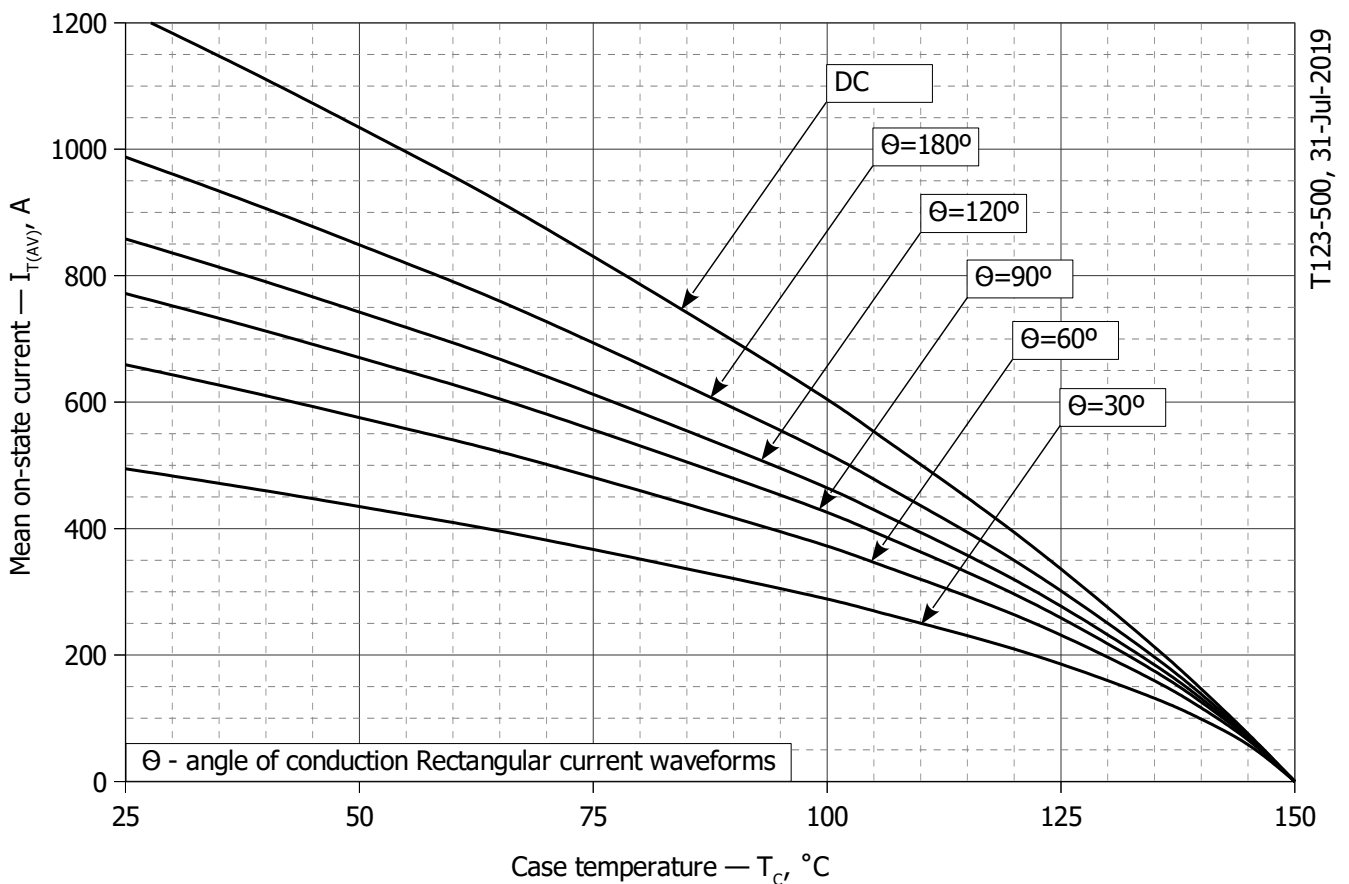


Fig. 10 - Mean on-state current I_{TAV} vs. case temperature T_C for rectangular current waveforms at different conduction angles and for DC ($f=50\text{Hz}$, DSC)

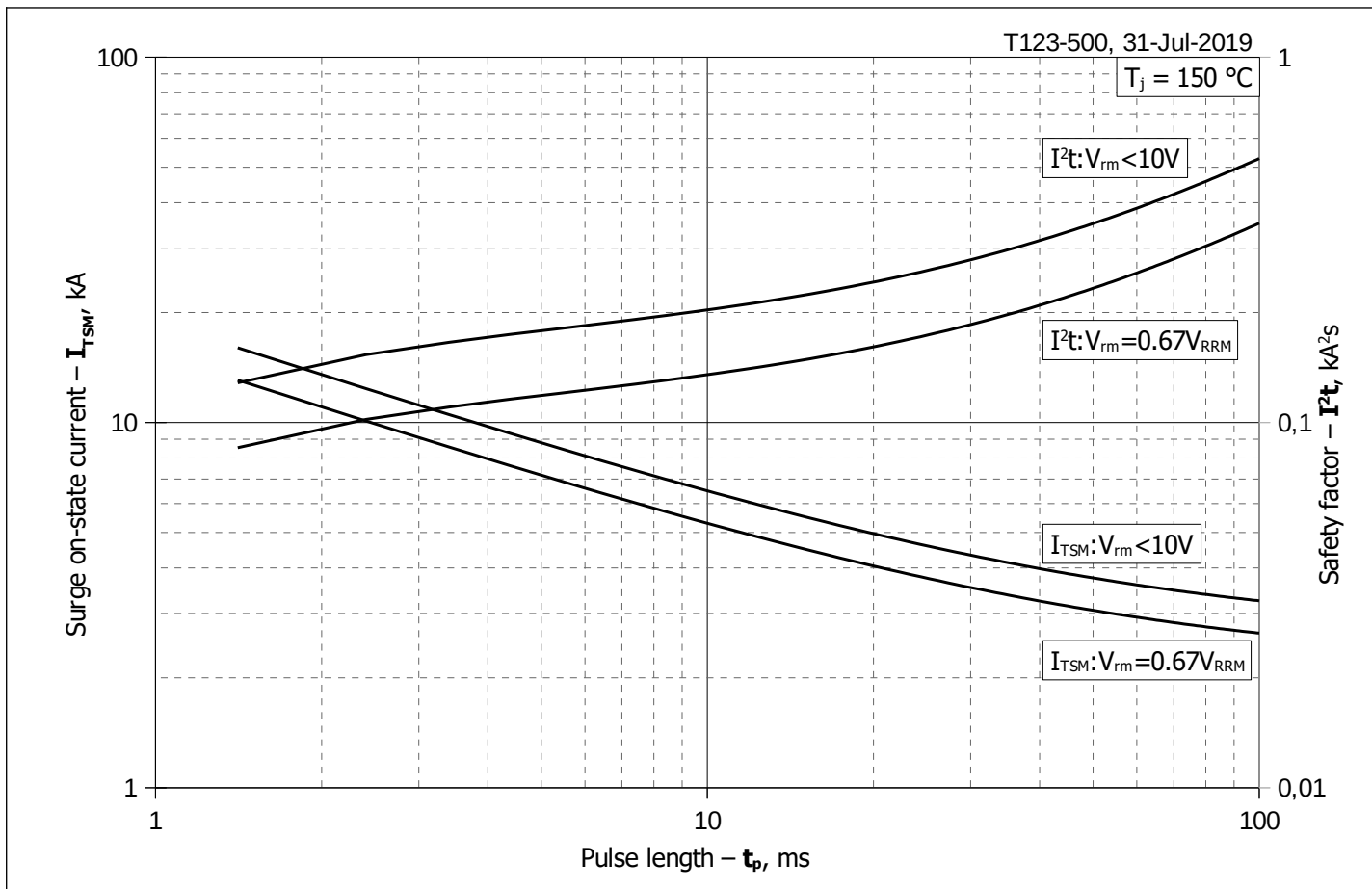


Fig. 11 – Maximum surge on-state current I_{TSM} and safety factor I^2t vs. pulse length t_p

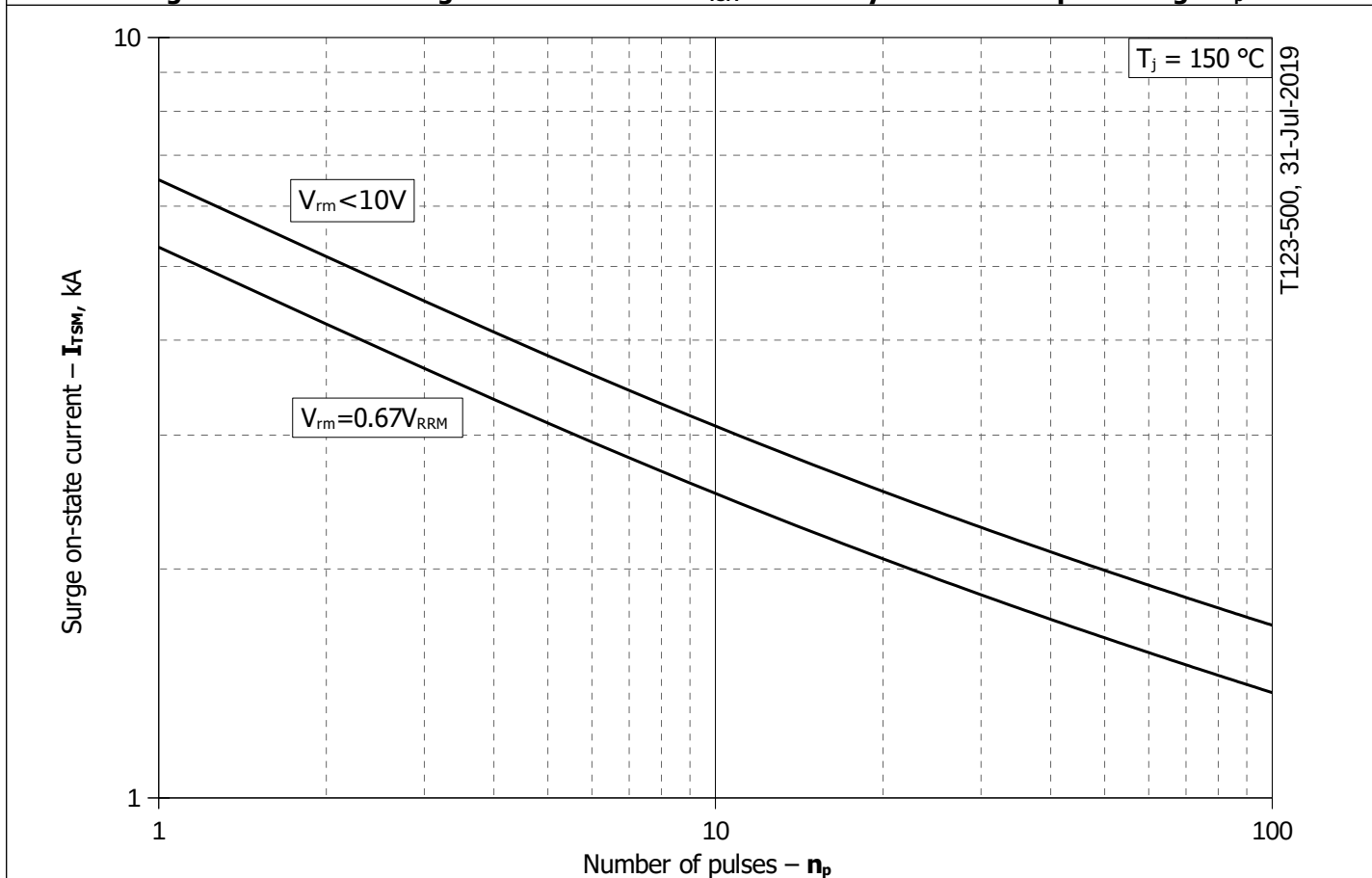


Fig. 12 - Maximum surge on-state current I_{TSM} vs. number of pulses n_p