**Features**

Interdigitated amplifying gates
Fast turn-on and high di/dt
Low switching losses

Typical Applications

Inductive heating
Electronic welders
Self-commutated inverters

Part No. H89KKM-KT78c(d)T

$I_{T(AV)}$	2200A
V_{DRM}, V_{RRM}	4500V 4800V
	5000V 5200V
t_q	50~150μs

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	$T_j(^{\circ}C)$	VALUE			UNIT
				Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled, $T_c=70^{\circ}C$	125			2200	A
V_{DRM} V_{RRM}	Repetitive peak off-state voltage Repetitive peak reverse voltage	$t_p=10ms$	125	4500		5200	V
I_{DRM} I_{RRM}	Repetitive peak current	at V_{DRM} at V_{RRM}	125			250	mA
I_{TSM}	Surge on-state current	10ms half sine wave	125			30	kA
I^2t	I^2t for fusing coordination	$V_R=0.6V_{RRM}$				4500	$A^2s \cdot 10^3$
V_{TO}	Threshold voltage		125			1.67	V
r_T	On-state slope resistance					0.33	mΩ
V_{TM}	Peak on-state voltage	$I_{TM}=2200A, F=70kHz$	25			3.20	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	$V_{DM}=67\%V_{DRM}$, to 4000A Gate pulse $t_r \leq 0.5\mu s$ $I_{GM}=1.5A$	125			300	A/μs
Q_{rr}	Recovery charge	$I_{TM}=2000A, t_p=4000\mu s$, $di/dt=-20A/\mu s, V_R=50V$	125		5000		μC
t_q	Circuit commutated turn-off time	$I_{TM}=2000A, t_p=4000\mu s, V_R=50V$ $dv/dt=30V/\mu s, di/dt=-20A/\mu s$	125	50		150	μs
I_{GT}	Gate trigger current	$V_A=12V, I_A=1A$	25	50		250	mA
V_{GT}	Gate trigger voltage			0.9		2.5	V
I_H	Holding current			40		1000	mA
V_{GD}	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$	125	0.25			V
$R_{th(j-c)}$	Thermal resistance Junction to case	At 180° sine double side cooled Clamping force 70kN				0.007	$^{\circ}C/W$
$R_{th(c-h)}$	Thermal resistance case to heat sink					0.002	
F_m	Mounting force			63		84	kN
T_{vj}	Junction temperature			-40		125	$^{\circ}C$
T_{stg}	Stored temperature			-40		140	$^{\circ}C$
W_t	Weight				1390/ 1920		g
Outline	KT78cT/KT78dT						

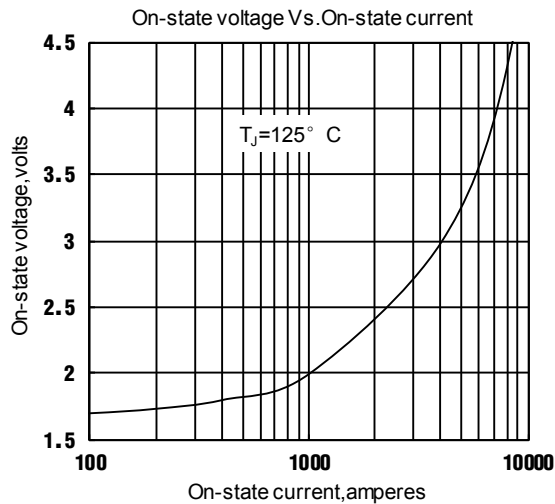


Fig.1

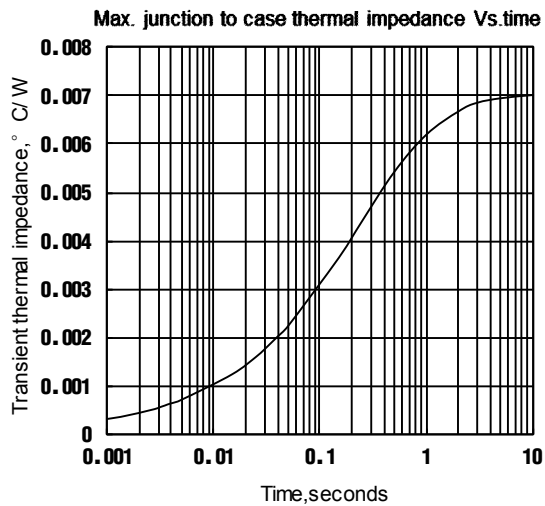


Fig.2

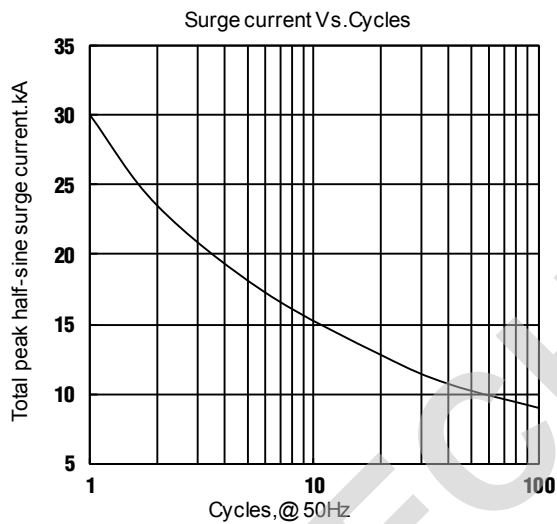


Fig.3

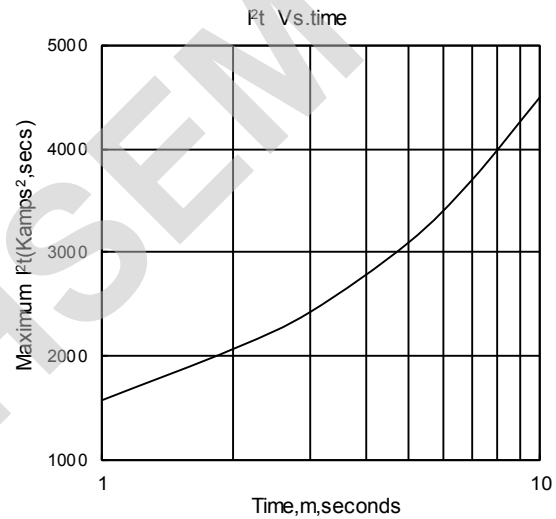


Fig.4

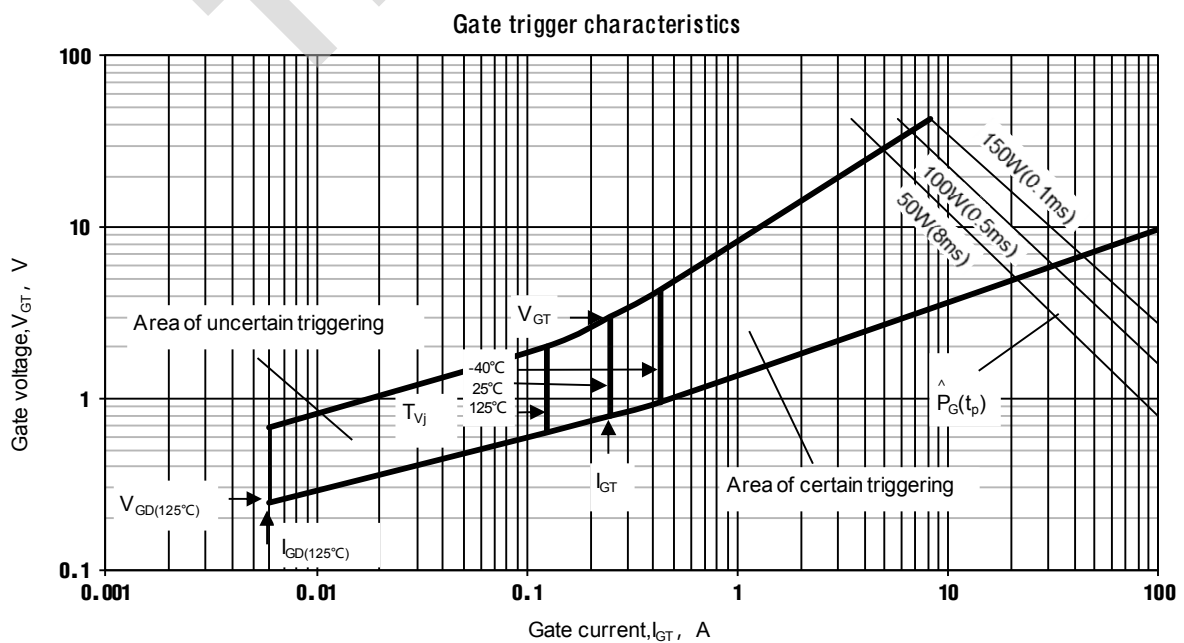
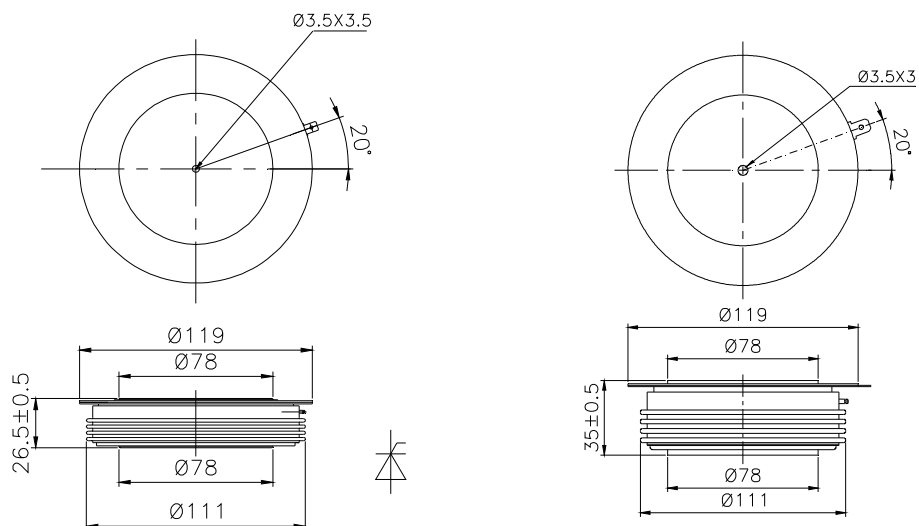


Fig.5

Outline:



TECHSEM reserves the right to change specifications without notice.