

**Features**

- Interdigitated amplifying gates
- Fast turn-on and high di/dt
- Low switching losses

Typical Applications

- Inductive heating
- Electronic welders
- Self-commutated inverters

Part No. Y50KKG-KT50cT

$I_{T(AV)}$	1100A
V_{DRM}, V_{RRM}	2000V 2200V
	2500V
t_q	20~75μs

SYMBOL	CHARACTERISTIC	TEST CONDITIONS		$T_J(^{\circ}C)$	VALUE			UNIT
					Min	Type	Max	
$I_{T(AV)}$	Mean on-state current	180° half sine wave 50Hz Double side cooled.	$T_C=55^{\circ}C$	125			1100	A
V_{DRM}/V_{RRM}	Repetitive peak off-state voltage	$t_p=10ms$		125	2000		2500	V
I_{DRM}/I_{RRM}	Repetitive peak current	at V_{DRM}/V_{RRM}		125			80	mA
I_{TSM}	Surge on-state current	10ms half sine wave		125			16	kA
I^2t	I^2t for fusing coordination	$V_R=0.6V_{RRM}$					1280	10^3A^2s
V_{TO}	Threshold voltage			125			1.60	V
r_T	On-state slope resistance						0.45	mΩ
V_{TM}	Peak on-state voltage	$I_{TM}=3000A, F=24kHz$		25			3.15	V
dv/dt	Critical rate of rise of off-state voltage	$V_{DM}=0.67V_{DRM}$		125			1000	V/μs
di/dt	Critical rate of rise of on-state current (Non-repetitive)	$V_{DM}=67\%V_{DRM}$ to 1600A, Gate pulse $t_r \leq 0.5\mu s, I_{GM}=1.5A$ Single pulse		125			1500	A/μs
Q_{rr}	Recovery charge	$I_{TM}=1000A, t_p=4000\mu s,$ $di/dt=-20A/\mu s, V_R=100V$		125		750		μC
t_q	Circuit commutated turn-off time	$I_{TM}=1000A, t_p=4000\mu s, V_R=100V$ $dv/dt=30V/\mu s, di/dt=-20A/\mu s$		100	20		75	μs
I_{GT}	Gate trigger current	$V_A=12V, I_A=1A$		25	40		300	mA
V_{GT}	Gate trigger voltage				0.9		3.0	V
I_H	Holding current				20		500	mA
I_L	Latching current						500	mA
V_{GD}	Non-trigger gate voltage	$V_{DM}=67\%V_{DRM}$		125			0.3	V
$R_{th(j-c)}$	Thermal resistance Junction to case	double side cooled Clamping force 24kN					0.020	$^{\circ}C/W$
$R_{th(c-h)}$	Thermal resistance case to heat sink						0.005	
F_m	Mounting force				19		26	kN
T_{vj}	Junction temperature				-40		125	$^{\circ}C$
T_{stg}	Stored temperature				-40		140	$^{\circ}C$
W_t	Weight					440		g
Outline	KT50cT							

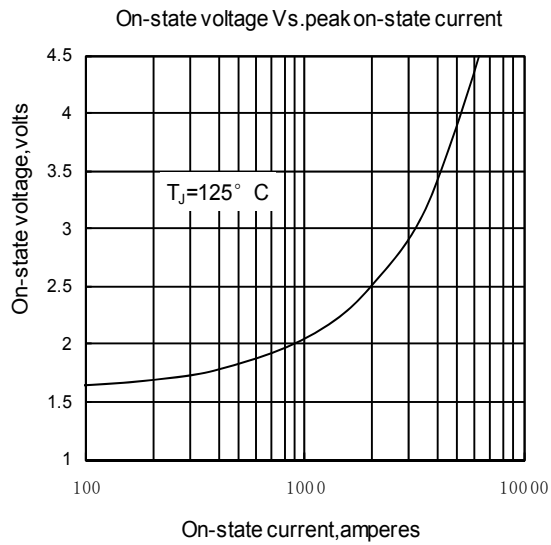


Fig1

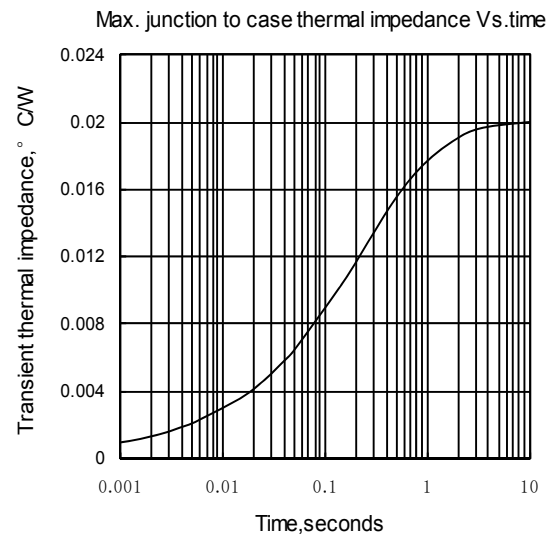


Fig2

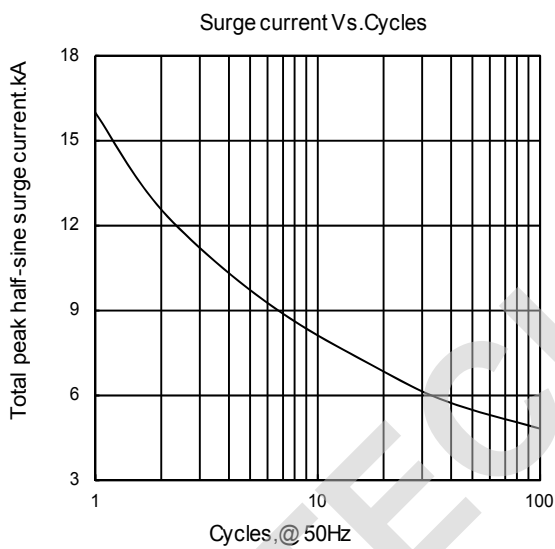


Fig3

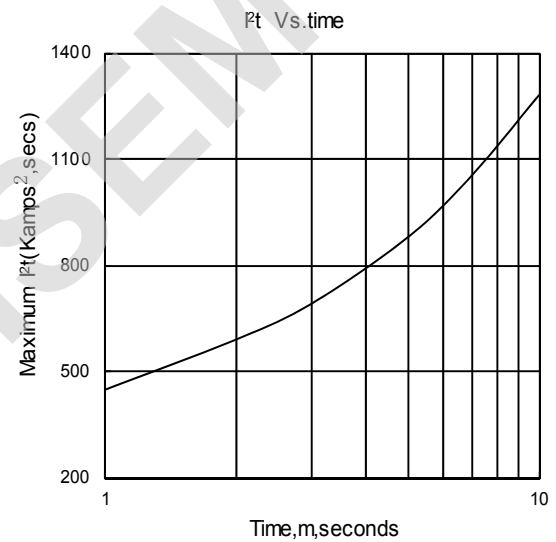


Fig4

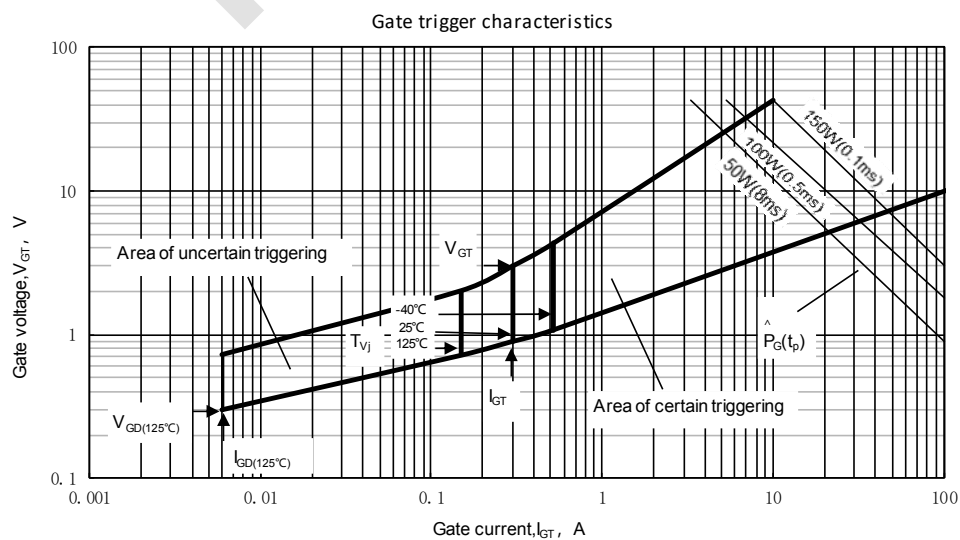
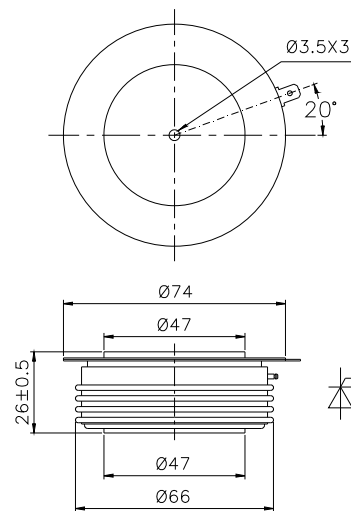


Fig.5

Outline:



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