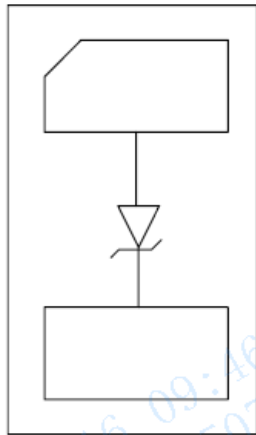


1-Line, Uni-directional, Transient Voltage Suppressor



DFN1610-2L

Features

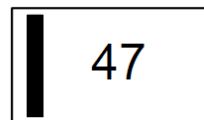
- Stand-off voltage: 3.3 V Max
- Transient protection for each line according to
IEC61000-4-2(ESD): $\pm 30\text{kV}$ (contact/air)
IEC61000-4-5(surge): 170A (8/20 μs)
- Low leakage current
- Ultra-low capacitance: $C_J = 300\text{ pF}$ typ
- Low clamping voltage
- RoHS Compliant

Applications

- Mobile Phones
- Battery Protection
- Power Line Protection
- Vbat pin for Mobile Devices
- Hand Held Portable Applications

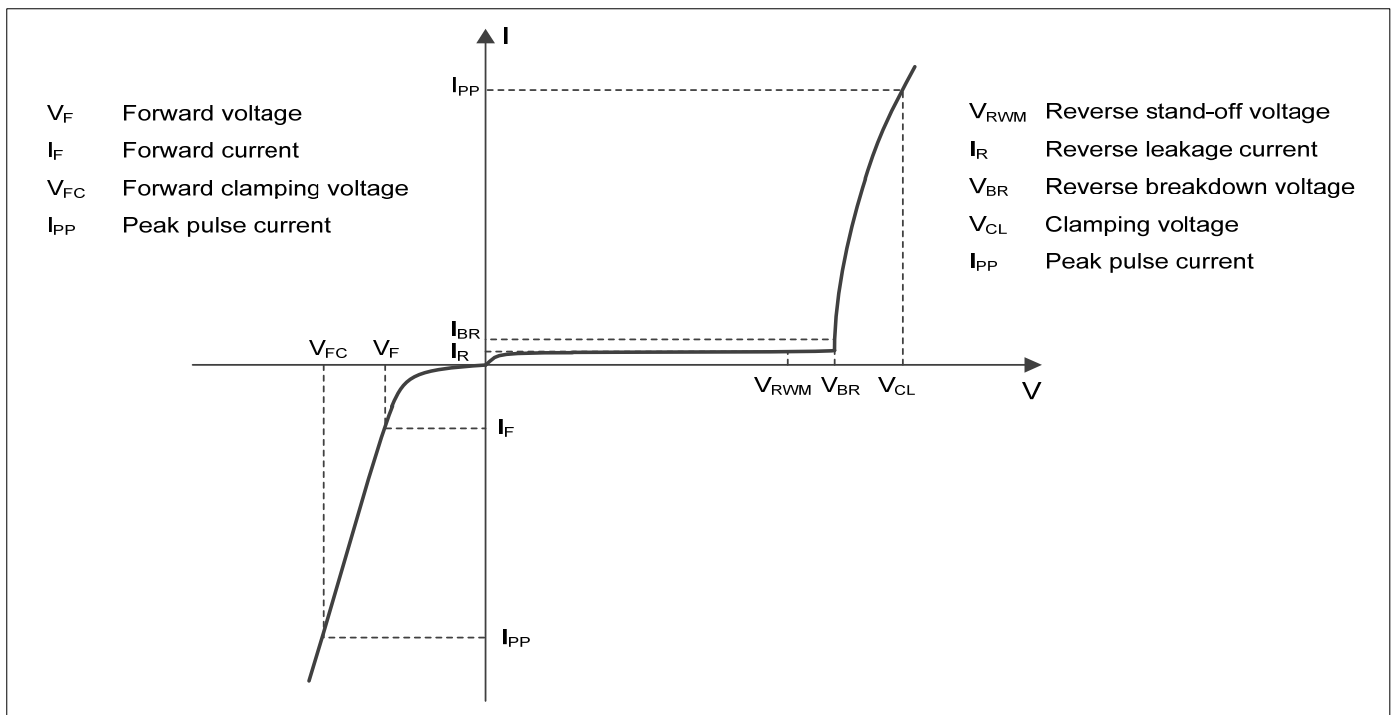
Mechanical Data

- Package: DFN1610-2L
- Case Material: "Green" Molding Compound
- Marking Information: See Below



47 = Device Marking Code
Bar denotes Cathode

■Definitions of electrical characteristics





ESD3V3P6A

■Maximum Ratings

PARAMETER	SYMBOL	LIMITS	UNIT
Peak pulse power ($t_p = 8/20\mu s$), any I/O pin to ground	P_{pk}	2550	W
Peak pulse current ($t_p = 8/20\mu s$), any I/O pin to ground	I_{PP}	170	A
ESD according to IEC61000-4-2 air discharge	V_{ESD}	± 30	KV
ESD according to IEC61000-4-2 contact discharge		± 30	
Junction temperature	T_J	-55~125	°C
Storage temperature	T_{STG}	-55~150	°C

■Electrical Characteristics ($T_a=25^\circ C$ Unless otherwise specified)

PARAMETER	Symbol	UNIT	Conditions	Min	Typ	Max
Reverse maximum working voltage	V_{RWM}	V				3.3
Reverse breakdown voltage	$V_{(BR)}$	V	$I_T = 1mA$	5		
Reverse leakage current	I_R	μA	$V_{RWM} = 3.3V$			200
Clamping voltage	V_{CL}	V	$I_{PP} = 10A, t_p = 8/20\mu s$			7
		V	$I_{PP} = 170A, t_p = 8/20\mu s$			15
Junction capacitance	C_J	pF	$V_R = 0V, f = 1MHz$		300	500

■Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(mg)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
ESD3V3P6A	F1	Approximate 2.8	3000	30000	120000	7 reel

■ Characteristics (Typical)

Fig1.8/20μs waveform per IEC61000-4-5

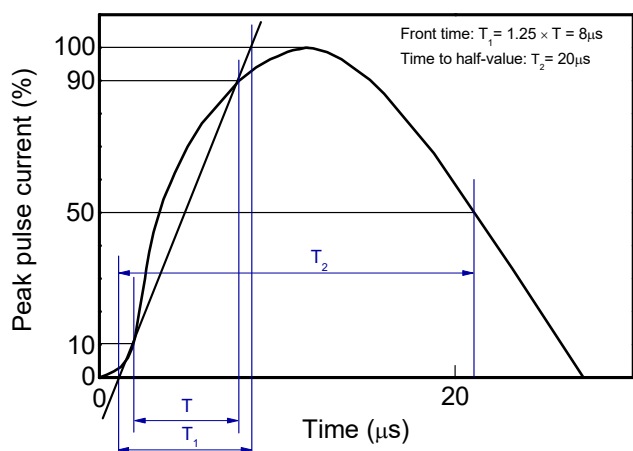


Fig2. Contact discharge current waveform per IEC61000-4-2

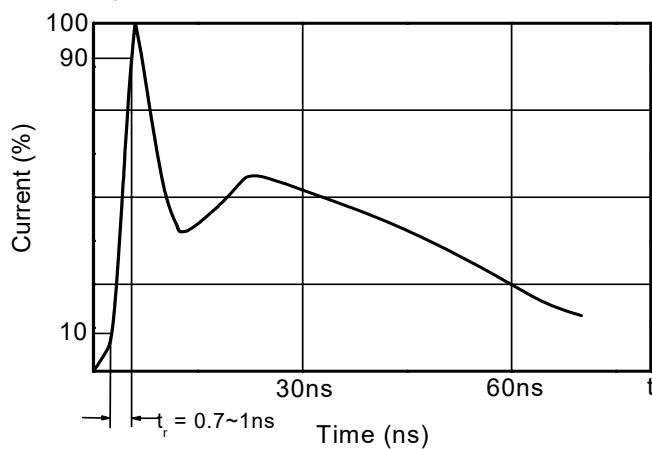


Fig3. Clamping voltage vs. Peak pulse current

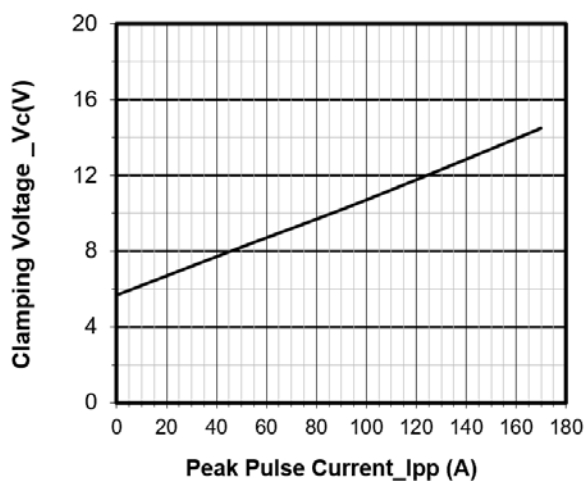


Fig4.Capacitance vs. Reverse voltage

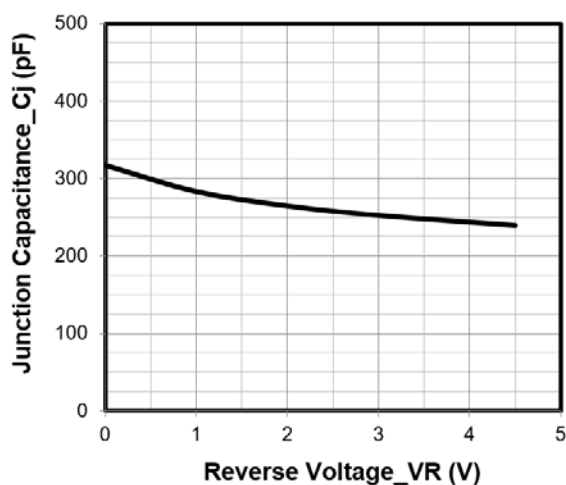


Fig5.Non-repetitive peak pulse power vs. Pulse time

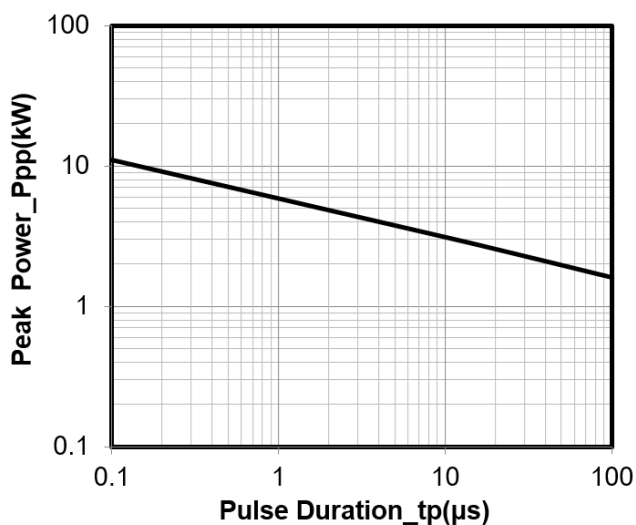


Fig6.Power derating vs. Ambient temperature

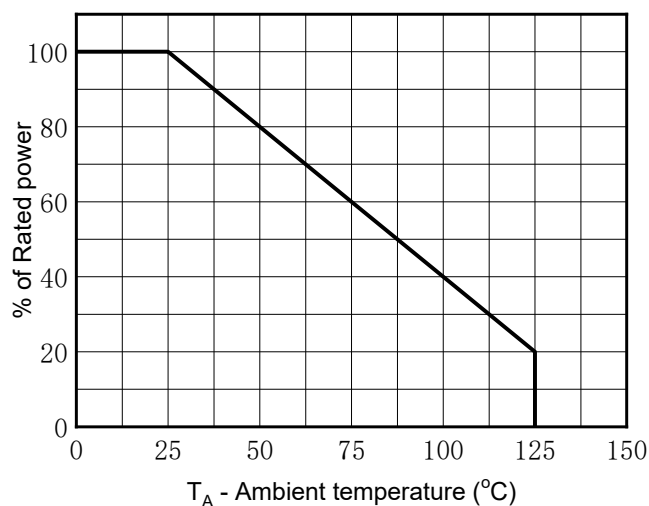
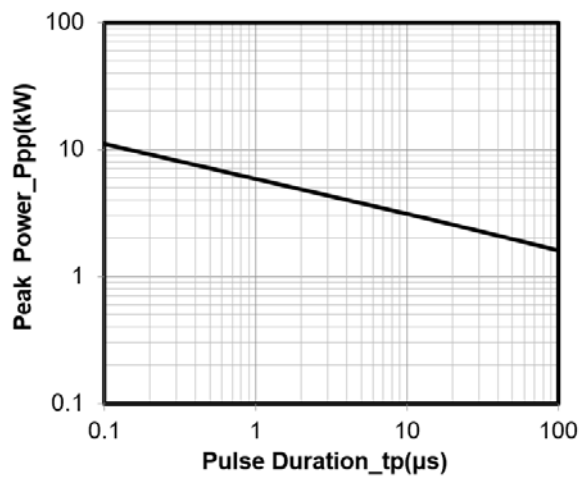
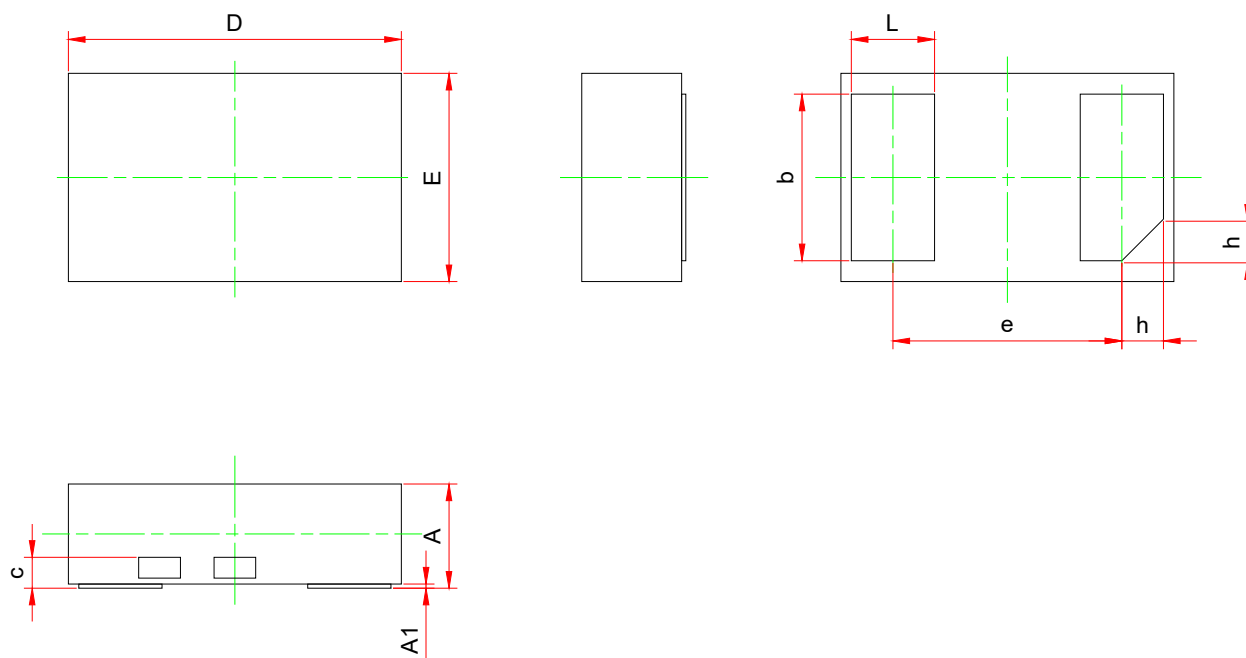


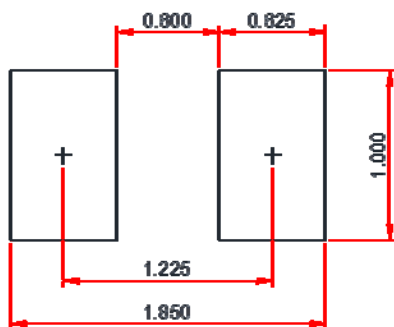
Fig7.TLP Measurement

■ Outline Dimensions



Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
A	0.45	0.50	0.55
A1	0.00	0.02	0.05
c	0.15 Ref.		
b	0.75	0.80	0.85
L	0.35	0.40	0.45
D	1.55	1.60	1.65
E	0.95	1.00	1.05
e	1.10 BSC		
h	0.20 Ref.		

■ Soldering Footprint



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met



Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, function or design or otherwise.

The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with equipment or devices which require high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use of sale.

This publication supersedes & replaces all information previously supplied. For additional information, please visit our website [http:// www.21yangjie.com](http://www.21yangjie.com) , or consult your nearest Yangjie's sales office for further assistance.