KS2460-BIDIRECTIONAL THYRISTOR

2200-2800 V_{DRM}

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HIGH POWER BIDIRECTIONAL THYRISTOR

TS17D

Features：

- **. Amplifying Gate Configuration**
- **. Two thyristors integrated into one wafer**
- **. Blocking capability up to 2800 volts**
- **. High power capability**
- **. Full cold pressing encapsulation**

ELECTRICAL CHARACTERISTICS AND RATINGS

Blocking-Off State

Device No.	V_{DRM} (1)	V_{DSM} (1)
KS2460/22	2200	2300
KS2460/25	2500	2600
KS2460/28	2800	2900

V_{DRM} = Repetitive peak off state voltage
V_{DSM} = Non Repetitive peak reverse voltage(2)

Repetitive peak reverse leakage and off state leakage	I_{DRM}	5 mA 200 mA (3)
Off - state voltage rise rating	dv/dt(4)	2000 V/μs

Notes：
All ratings are specified for Tj=25 °C unless otherwise stated.
(1) All voltage ratings are specified for an applied 50Hz/60Hz sinusoidal waveform over the temperature range-40 °C to +125 °C
(2) 10 msec. Max. Pulse width
(3) Maximum value for Tj=125 °C；5Hz.
(4) Minimum value for linear and exponential waveshape to 67% rated $V_{DRM.}$ Gate open, Tj=125 °C
(5) The value of di/dt is established in accordance with EIA/NIMA Standard JB/T 8950.2-2013.

Conducting-on state

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Average value of on-state current	$I_{T(AV)}$		2460		A	Sinewave,180° conduction,T_c=70°C
RMS value of on-state current	I_{TRMS}		3862		A	Nominal value
Peak one cycle surge (non repetitive) current	I_{TSM}		38000		A	10.0 msec (50Hz), sinusoidal wave-shape, 180° conduction, T_j = 125 °C
I square t	I^2t		$7.22x10^6$		A^2s	10 msec
Latching current	I_L		1000		mA	V_D = 12 V; R_L= 12 ohms
Holding current	I_H		200		mA	V_D=12 V; I = 2.5 A
Peak on-state voltage	V_{TM}		1.47		V	I_{TM}=3000A;Tj =125°C
Threshold Voltage	V_{TO}		0.99		V	Tj=125°C
Slope resistance	r_T		0.16		mΩ	1000A to 3000A
Critical rate of rise of on-state current(5)	di/dt		200		A/μs	Tj=125°C; V_D = 0.67 V_{DRM}; f=50Hz; I_{TM}=5000A
Critical rate of rise of commutating voltage	dv/dt_{com}			500	V/μs	Tj=125°C；VR≤0.67V_{DRM}

Gating

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Peak gate power dissipation	P_{GM}		20		W	
Average gate power dissipation	$P_{G(AV)}$		4		W	
Gate trigger current	I_{GT}	50	200		mA	V_D=12V;R_L=3ohms;T_j=+25°C
Gate trigger voltage	V_{GT}	1.0	2.6		V	V_D=12V;R_L=3ohms;T_j=+25°C
Peak negative voltage	V_{GRM}		5		V	

Dynamic

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Delay time	t_d		3.0		μs	I_{FG}=2.0A; V_D=0.4V_{DRM}; t_r=0.5μs
Turn-off time (V_R=-5V)	t_q			400	μs	I_{TM}=2000A； di/dt=-1.5 A/s; V_R=100 V; dV/dt=30V/μs ; V_D= 67%V_{DRM};T_j=125℃
Reverse recovery charge	Q_{rr}			2300	μC	I_{TM}=2000A；di/dt=-1.5 A/s; V_R=100 V; T_j=125℃

THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

Parameter	Symbol	Min.	Max.	Typ.	Units	Conditions
Operating temperature	T_j	-40	+125		°C	
Storage temperature	T_{stg}	-40	+140		°C	
Thermal resistance- junction to case	$R_{\Theta(j-c)}$		0.0114		°C/W	Double sided cooled
Thermal resistance - case to heatsink	$R_{\Theta(c-s)}$		0.002		°C/W	Double sided cooled
Mounting force	F	81	108	90	kN	
Weight	m			2.9	kg	

* **Mounting surfaces smooth, flat and greased**

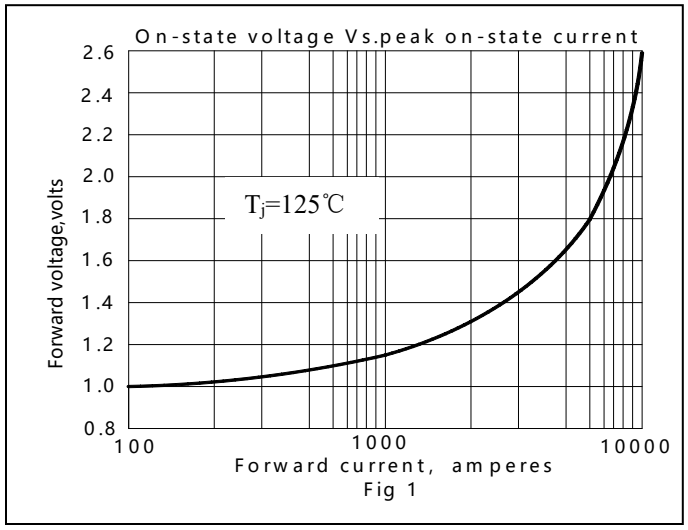


Fig 1

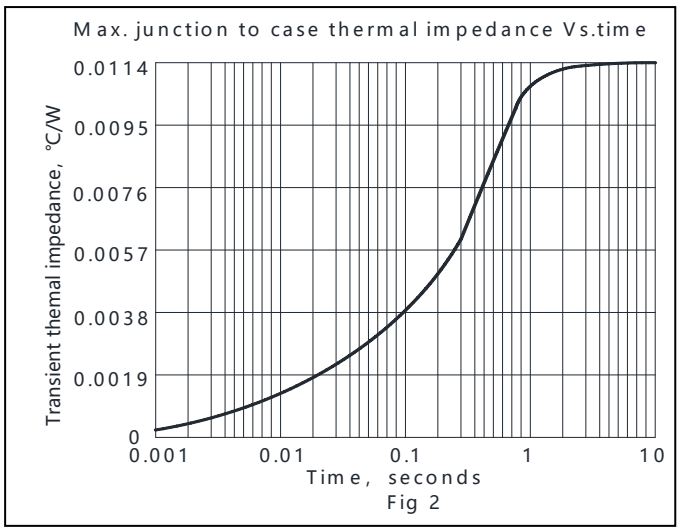


Fig 2

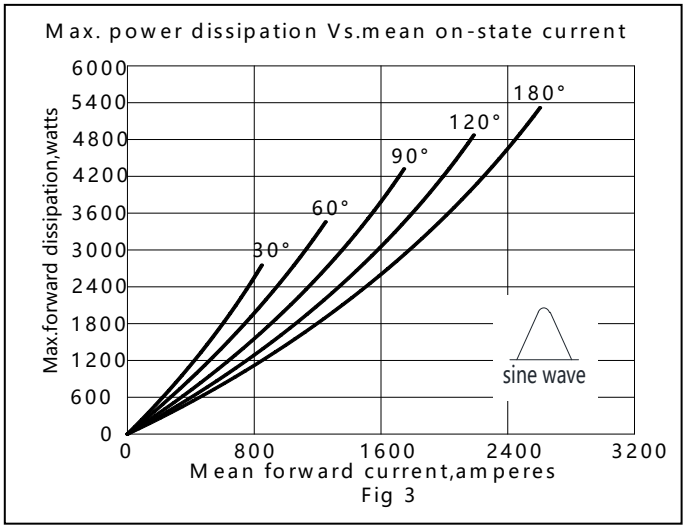


Fig 3

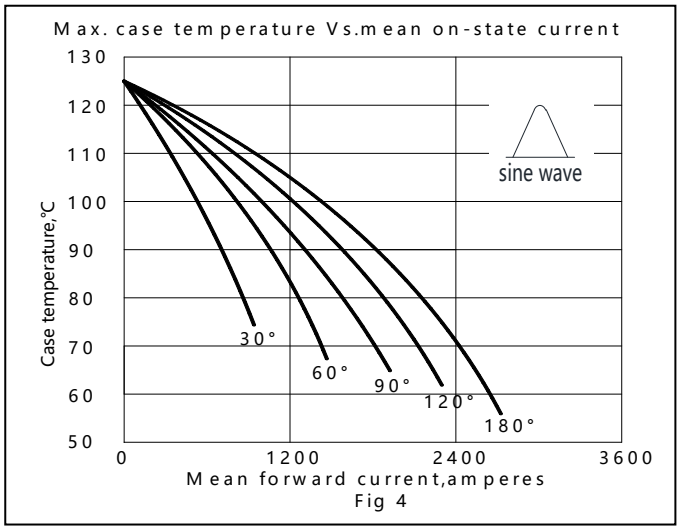


Fig 4

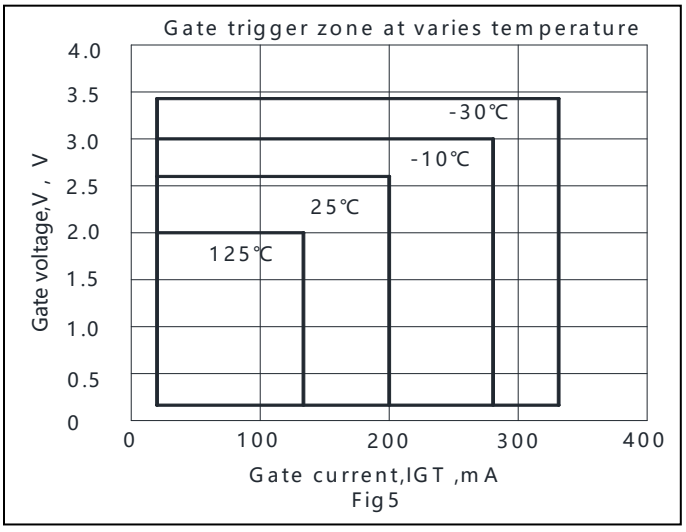


Fig5

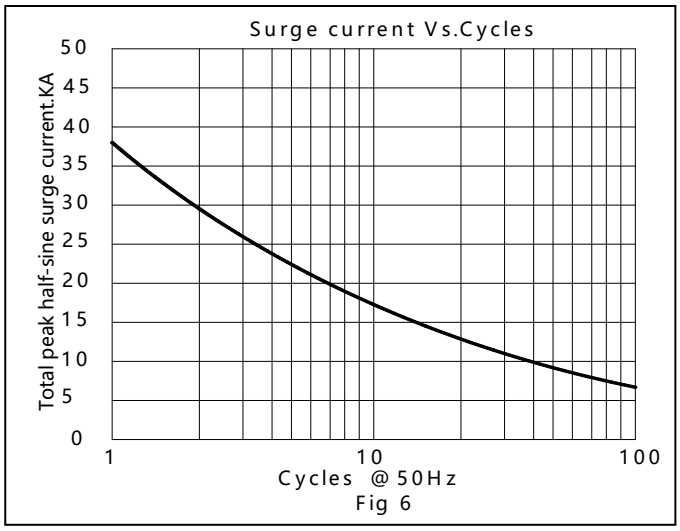
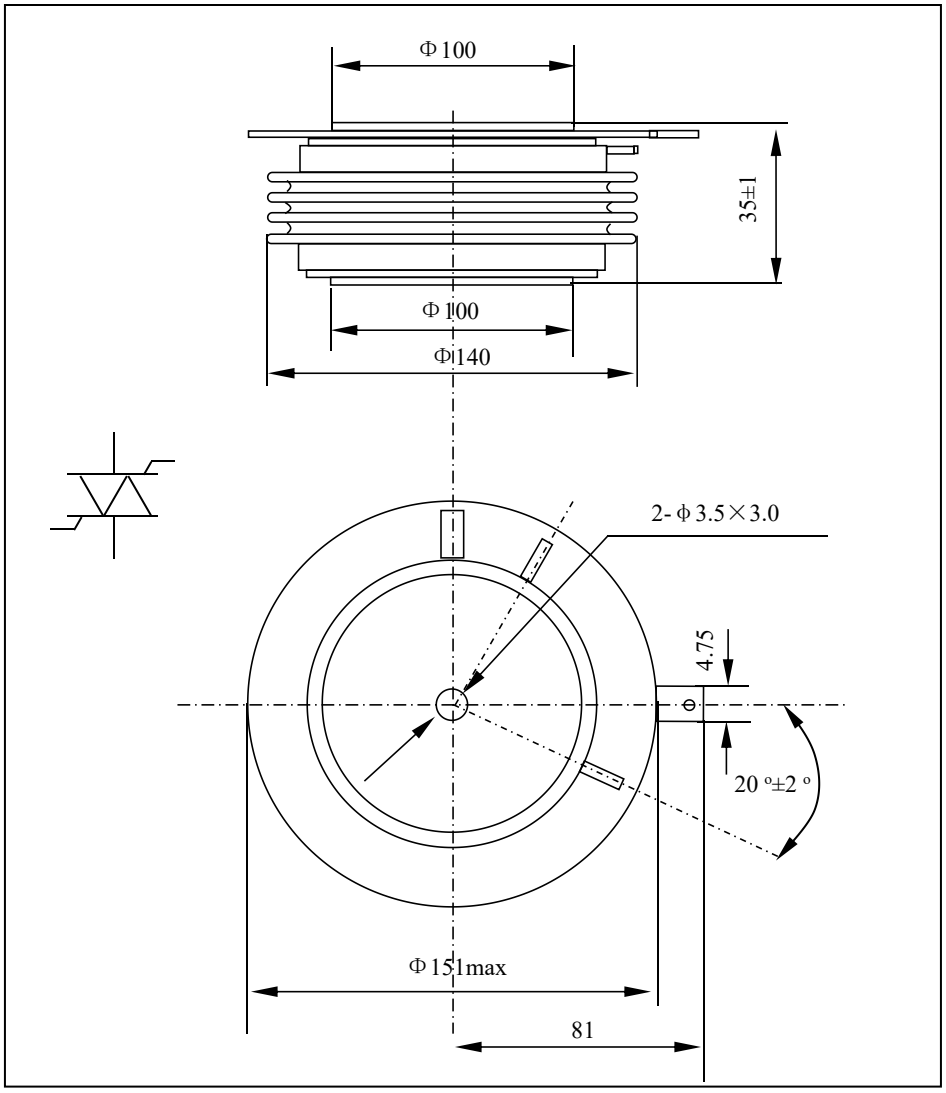


Fig 6


Φ100
35±1
Φ100
Φ140
2-φ3.5×3.0
4.75
20°±2°
Φ151max
81

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