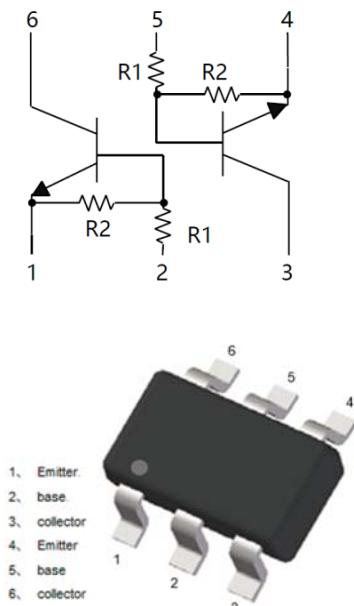


Dual NPN Digital Transistors (Built-in Resistors)



Features

- Moisture sensitivity level 1
- Halogen free and RoHS compliant
- Surface mount package ideally suited for automatic insertion

Application

- Signal amplification
- Switching circuit

Mechanical data

- **Package:** SOT-363S
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102

■ Maximum Ratings ($T_a=25^{\circ}\text{C}$ Unless otherwise specified)

Item	Symbol	Unit	Value
Device marking code			H15
Supply Voltage	V_{CC}	V	50
Input Voltage	V_{IN}	V	-10 to +30
Output current	I_O	mA	100
Power dissipation	P_D	mW	150
Junction temperature	T_J	$^{\circ}\text{C}$	-55 to +150
Storage temperature	T_{STG}	$^{\circ}\text{C}$	-55 to +150



UMH15NS

RoHS
COMPLIANT

■ Electrical Characteristics (T_a=25°C Unless otherwise specified)

Item	Symbol	Unit	Conditions	Min	Typ	Max
Input voltage	V _{I(off)}	V	V _{CC} =5V, I _O =100uA	0.5		
	V _{I(on)}	V	V _O =0.3V, I _O =20mA			3
Output voltage	V _{O(on)}	V	I _O /I _I =10mA/0.5mA			0.3
Input current	I _I	mA	V _I =5V			1.8
Output current	I _{O(off)}	uA	V _{CC} =50V, V _I =0			0.5
DC current gain	G _I		V _O =5V, I _O =10mA	20		
Input resistance	R ₁	kΩ		3.29	4.7	6.11
Resistance ratio	R ₂ /R ₁			0.8	1	1.2
Transition frequency	f _T	MHz	V _{CE} =10V, I _E =5mA, f=100MHz		250	

■ Thermal Characteristics

Parameter	Symbol	Unit	Value
Thermal resistance, junction-to-ambient	R _{θJ-A} ⁽¹⁾	°C/W	834
Thermal resistance, junction-to-case	R _{θJ-C} ⁽¹⁾	°C/W	667

Note:

(1) Device mounted on PCB, single-sided copper, with standard footprint

■ Ordering Information

Preferred P/N	Packing code	Unit weight(g)	Minimum package(pcs)	Inner box quantity(pcs)	Outer carton quantity(pcs)	Delivery mode
UMH15NS	F2	Approximate 0.007	3000	30000	120000	7" reel



■ Characteristics

Fig 1: Input Voltage (On) Characteristics

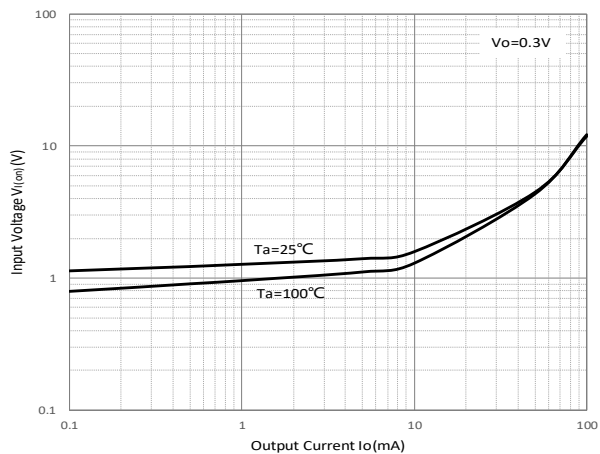


Fig 2: Input Voltage (Off) Characteristic

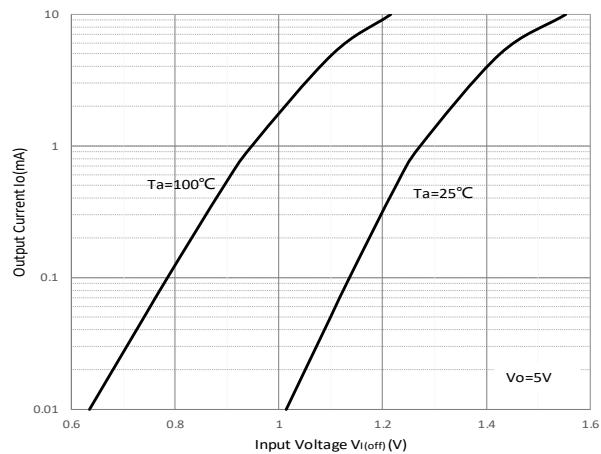


Fig 3: DC Current Gain Characteristics

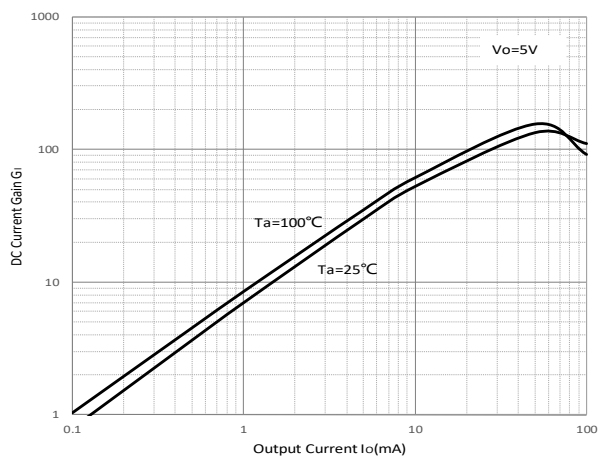


Fig 4: Output Voltage Characteristics

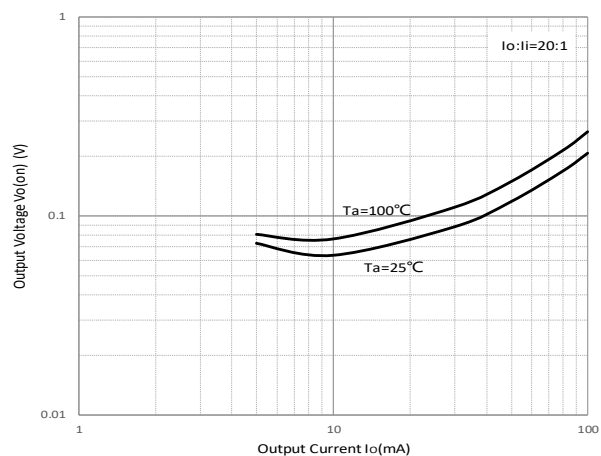
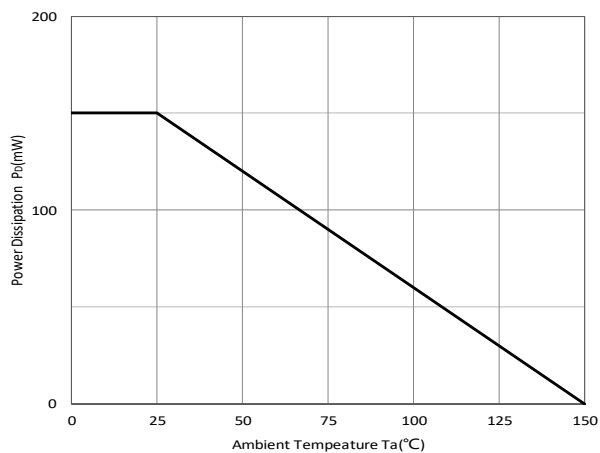
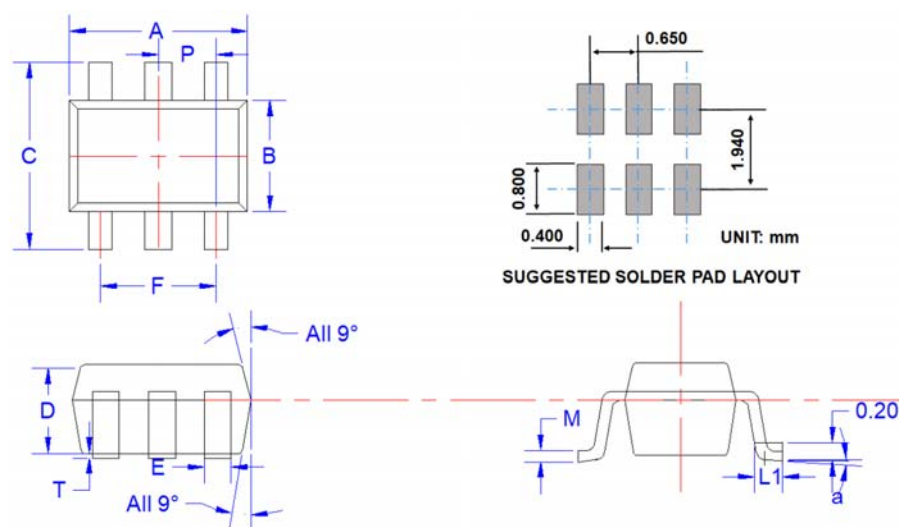


Fig 5: P<sub>D</sub>-T<sub>a</sub> Curve



■SOT-363S Package Outline Dimensions



Symbol	Millimeter		
	Min	Nom	Max
A	1.80	2.00	2.20
a	4°±4°		
B	1.15	1.25	1.35
C	2.00	2.10	2.20
D	0.90	0.95	1.00
E	0.15	0.25	0.35
F	1.30Bsc		
L1	0.20	0.30	0.40
M	0.10	0.15	0.25
P	0.65Bsc		
T	0.00	0.05	0.10

Note:

- All dimensions are in millimeters (mm) unless otherwise specified.
[所有尺寸均以毫米为单位, 除非另有说明]
- General tolerances: ±0.10mm unless otherwise specified.
[通用公差为±0.10mm, 除非另有说明]
- Dimensions and tolerances per ASME Y14.5M-2018.
[尺寸和公差遵循 ASME Y14.5M-2018 标准]
- All dimensions shown are exclusive of burrs and gate residues. Burrs and gate vestiges shall not exceed 0.15 mm in maximum.
[所有尺寸均不包括毛刺和浇口残留。毛刺与浇口残留的尺寸最大不得超过 0.15mm]
- Dimension b does not include dambar protrusion of max 0.100 mm per side.
[尺寸b不包括单边最大0.100 MM的中筋凸出部分]
- Dimensions A and B are the overall extreme outer dimensions of the mold compound. These dimensions exclude mold flash, lead flash, protrusions and burrs but include the maximum allowable mold mismatch.
[A和B是塑封体的外部极限尺寸, 不包括包封溢料、内引线溢料、凸出部分以及胶体毛刺, 但是包含了包封错位的最大尺寸]
- Formed leads shall be planar with respect to one another within a maximum of 0.076 mm relative to the seating plane.
[成型的管脚应为同一平面, 共面性最大为0.1mm]
- ★It is the key size.
[★ 标记为关键尺寸]



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