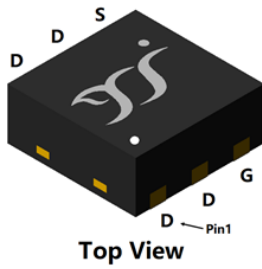
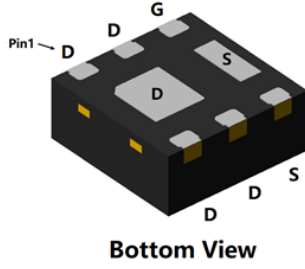




P-Channel Enhancement Mode Field Effect Transistor

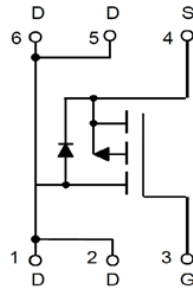


Top View



Bottom View

DFN2020-6L



Product Summary

• V_{DS}	-30V
• I_D	-5.7A
• $R_{DS(ON)}$ (at $V_{GS}=-10V$)	$<105m\Omega$
• $R_{DS(ON)}$ (at $V_{GS}=-4.5V$)	$<174m\Omega$
• ESD Level(HBM)	H0

General Description

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power management
- Portable equipment

Limiting Values

Parameter	Conditions		Symbol	Min	Max	Unit
Drain-Source Voltage			V _{DS}	-	-30	V
Gate-Source Voltage			V _{GS}	-20	20	
Continuous Drain Current (Note 1,2)	Steady-State	T _A =25°C,V _{GS} =-10V	I _D	-	-2.7	A
		T _A =100°C,V _{GS} =-10V		-	-1.7	
Continuous Drain Current (Note 1,3)	Steady-State	T _C =25°C,V _{GS} =-10V,Chip limitation		-	-5.7	
		T _C =100°C,V _{GS} =-10V		-	-3.6	
Pulsed Drain Current	T _C =25°C,t _p ≤10μs		I _{DM}	-	-22.8	
Maximum Body-Diode Continuous Current	T _C =25°C		I _S	-	-4.5	
Total Power Dissipation (Note 1,2)	Steady-State	T _A =25°C	P _D	-	1.21	W
		T _A =100°C		-	0.48	
Total Power Dissipation (Note 1,3)	Steady-State	T _C =25°C		-	5.43	
		T _C =100°C		-	2.17	
Junction and Storage Temperature Range			T _J ,T _{STG}	-55	150	°C

Thermal Resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient (Note 2)	Steady-State	$R_{\theta JA}$	-	103	$^{\circ}C/W$
Thermal Resistance Junction-to-Case	Steady-State	$R_{\theta JC}$	-	23	



YJQT105P03A

■ Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A, T_j=25^\circ C$	-30	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-30V, V_{GS}=0V, T_j=25^\circ C$	-	-	-1	μA
		$V_{DS}=-30V, V_{GS}=0V, T_j=125^\circ C$	-	-	-100	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V, T_j=25^\circ C$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A, T_j=25^\circ C$	-1	-1.5	-2	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-5A, T_j=25^\circ C$	-	80	105	m Ω
		$V_{GS}=-4.5V, I_D=-3A, T_j=25^\circ C$	-	116	174	m Ω
Diode Forward Voltage	V_{SD}	$I_S=-5A, V_{GS}=0V, T_j=25^\circ C$	-	-0.98	-1.2	V
Gate Resistance	R_G	$f=1MHz, T_j=25^\circ C$	-	20	-	Ω
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=-15V, V_{GS}=0V, f=1MHz, T_j=25^\circ C$	-	180	-	pF
Output Capacitance	C_{oss}		-	31	-	
Reverse Transfer Capacitance	C_{rss}		-	24	-	
Total Gate Charge	Q_g	$V_{GS}=-10V, V_{DS}=-15V, I_D=-5A, T_j=25^\circ C$	-	4.9	-	nC
Gate-Source Charge	Q_{gs}		-	0.72	-	
Gate-Drain Charge	Q_{gd}		-	0.48	-	
Reverse Recovery Charge	Q_{rr}	$I_F=-5A, di/dt=100A/\mu s, V_{GS}=0V, V_R=-15V, T_j=25^\circ C$	-	8.7	-	nC
Reverse Recovery Time	t_{rr}		-	13.5	-	ns
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=-10V, V_{DS}=-15V, I_D=-5A, R_{GEN}=3\Omega, T_j=25^\circ C$	-	4.3	-	ns
Turn-on Rise Time	t_r		-	2.3	-	
Turn-off Delay Time	$t_{D(off)}$		-	9.6	-	
Turn-off Fall Time	t_f		-	9	-	

Note:

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
2. The value of $R_{\theta JA}$ is measured with the device mounted on the 40mm*40mm*1.1mm single layer FR-4 PCB board with 1 in² pad of 2oz. Copper, in the still air environment with $T_A=25^\circ C$. The maximum allowed junction temperature of $150^\circ C$. The value in any given application depends on the user's specific board design.
3. Thermal resistance from junction to soldering point (on the exposed drain pad).



■ Typical Electrical and Thermal Characteristics Diagrams

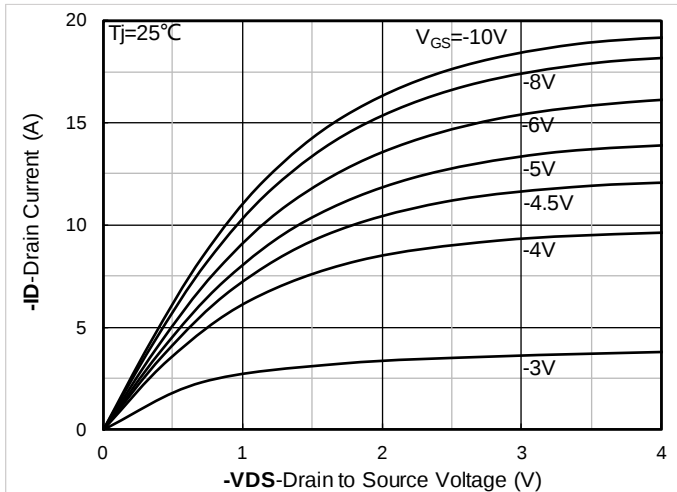


Figure 1. Output Characteristics; typical values

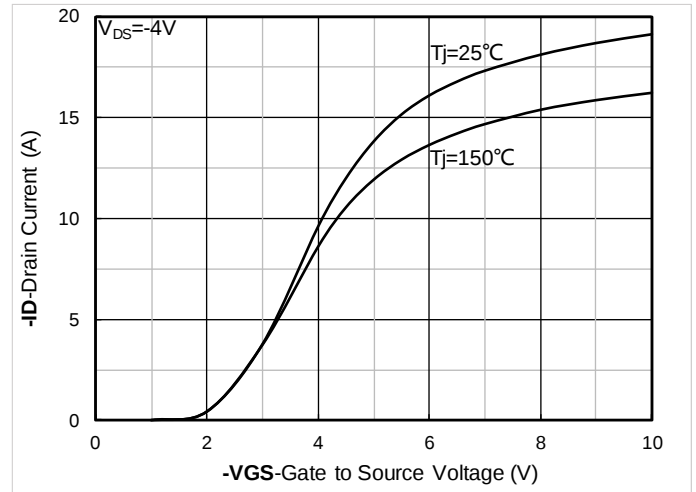


Figure 2. Transfer Characteristics; typical values

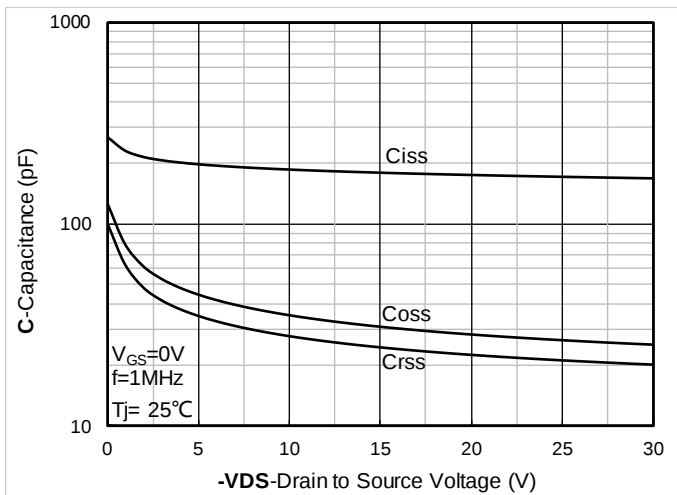


Figure 3. Capacitance Characteristics; typical values

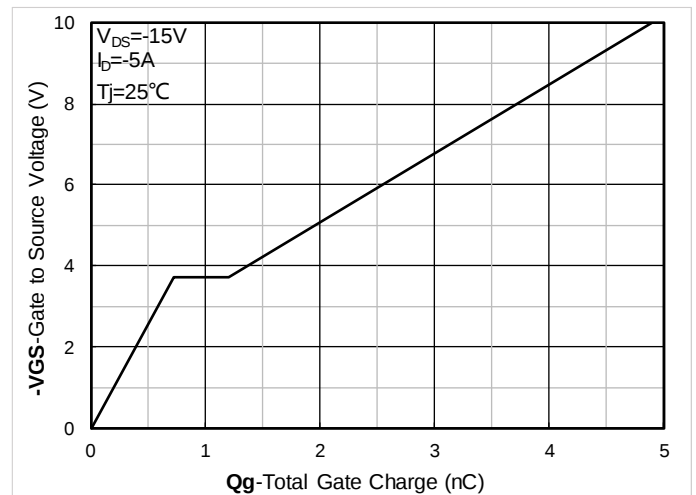


Figure 4. Gate Charge; typical values

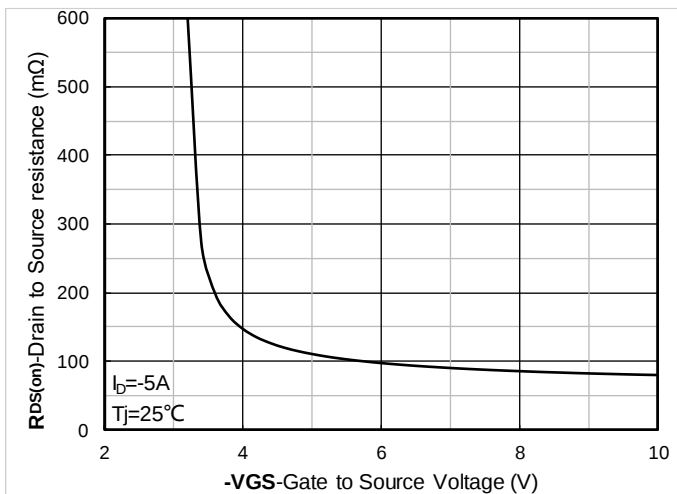


Figure 5. On-Resistance vs Gate to Source Voltage; typical values

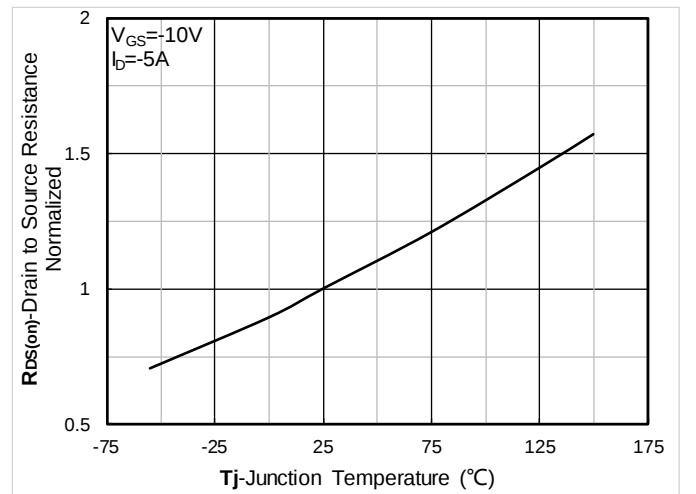


Figure 6. Normalized On-Resistance

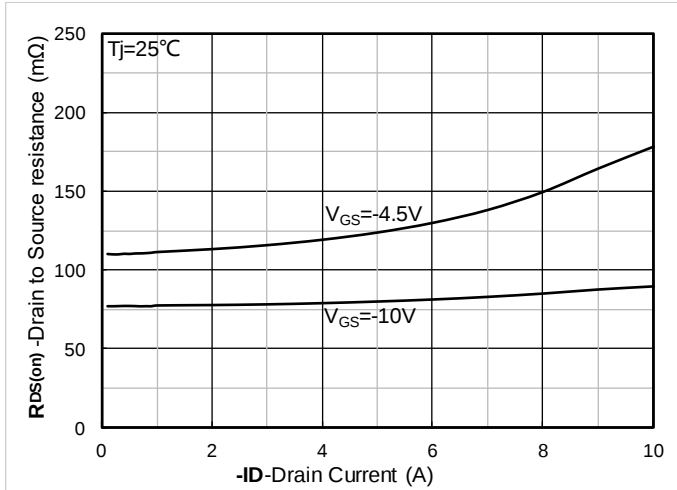


Figure 7. RDS(on) VS Drain Current; typical values

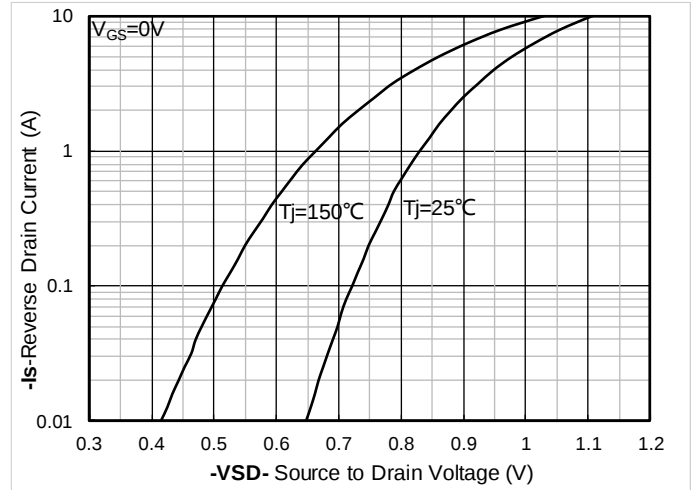


Figure 8. Forward characteristics of reverse diode; typical values

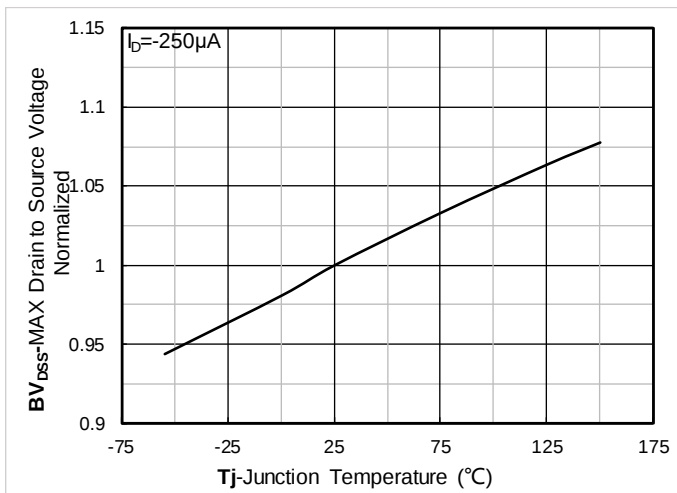


Figure 9. Normalized breakdown voltage

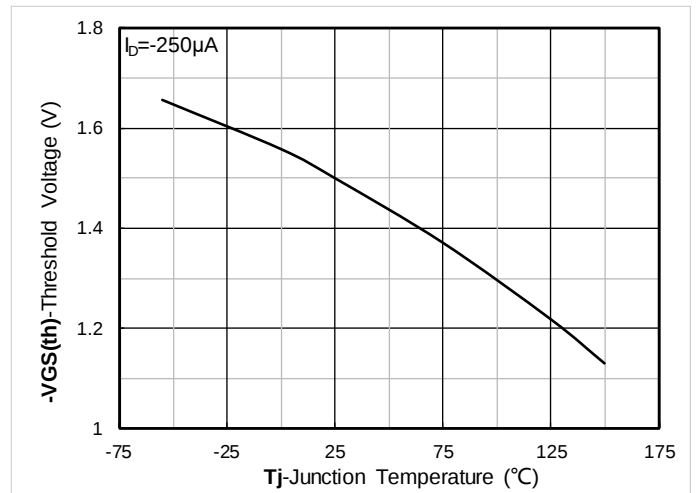


Figure 10. Gate Threshold voltage; typical values

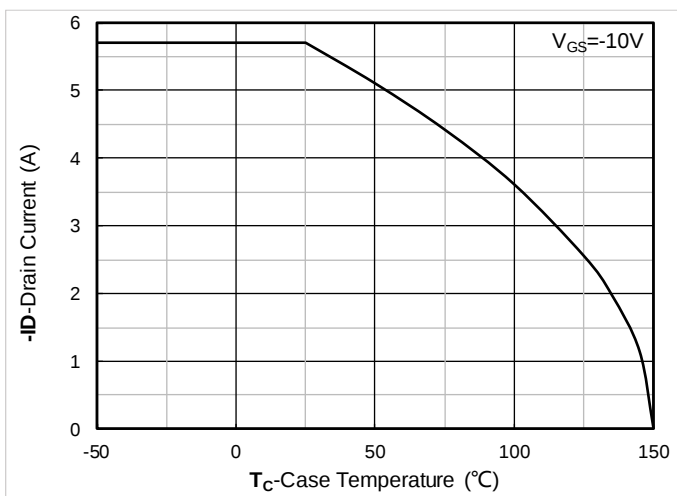


Figure 11. Current dissipation

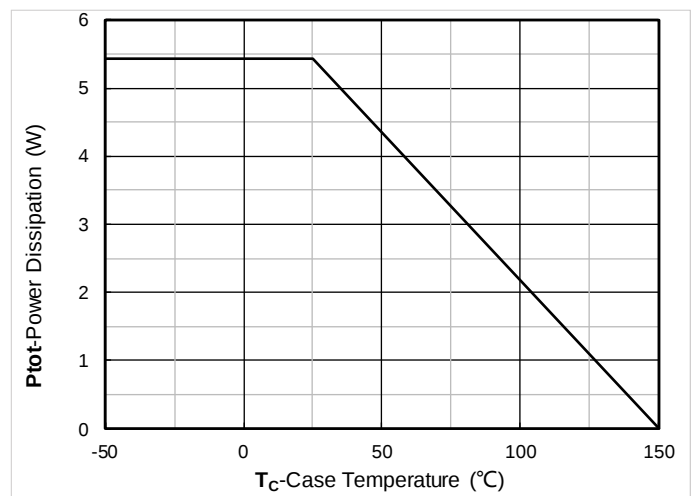


Figure 12. Power dissipation

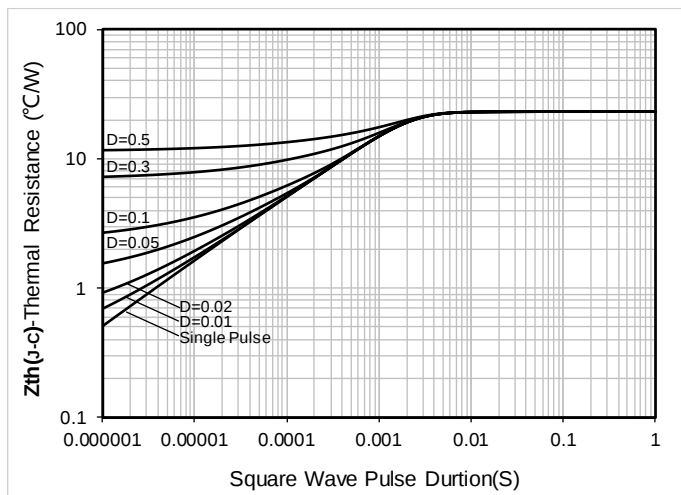


Figure 13. Maximum Transient Thermal Impedance

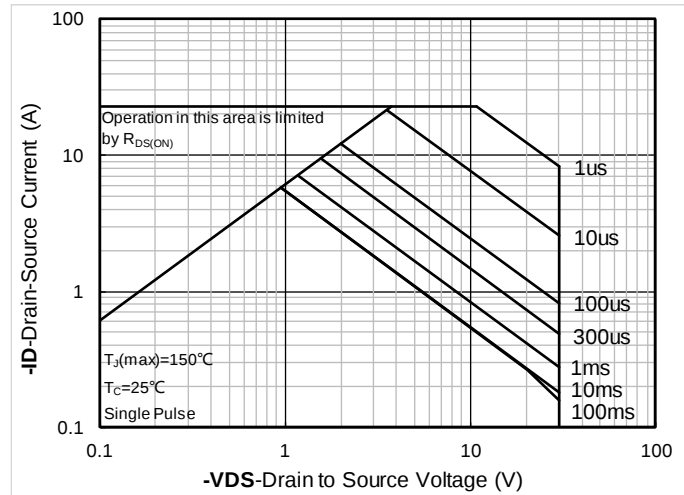
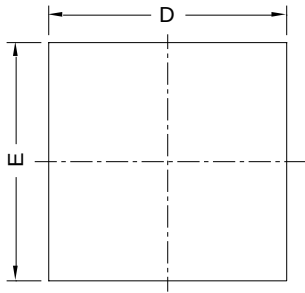


Figure 14. Safe Operation Area

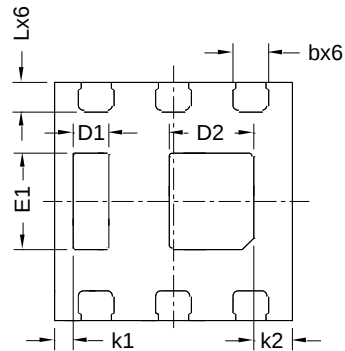


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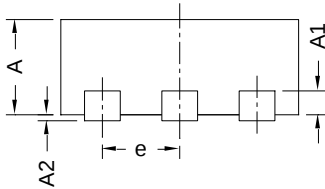
■ DFN2020-6L-E-0.80MM Package information



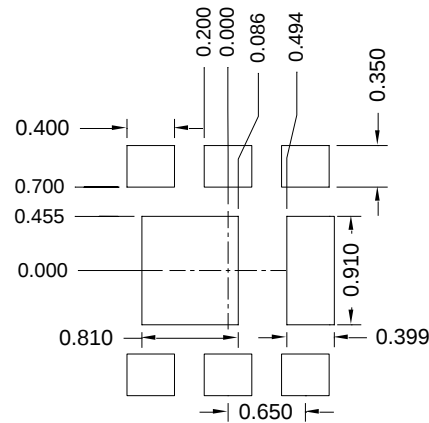
Top View
正面视图



Bottom View
背面视图



Side View
侧面视图



Suggested Solder Pad Layout
Top View

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
D	1.90	2.00	2.10
E	1.90	2.00	2.10
A	0.70	0.80	0.90
A1	0.20 BSC		
A2			0.10
D1	0.20	0.30	0.40
D2	0.61	0.71	0.81
E1	0.71	0.81	0.91
L	0.15	0.25	0.35
b	0.20	0.30	0.40
e	0.65 BSC		
k1	0.156 BSC		
k2	0.326 BSC		

Note:

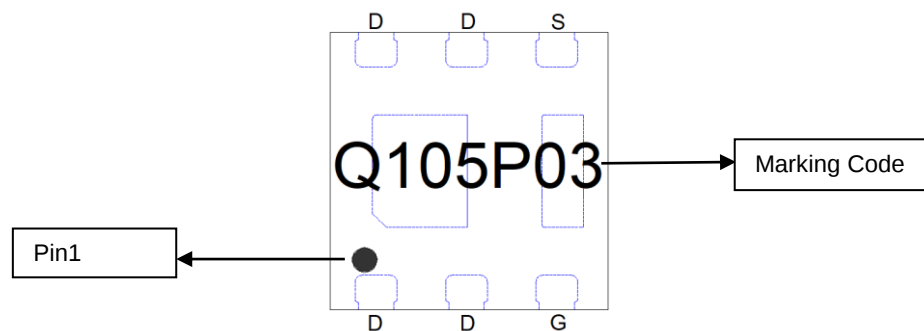
1. Controlling dimension: in millimeters.

2. General tolerance: $\pm 0.10\text{mm}$.

3. The pad layout is for reference purposes only.



■ Marking Information



Note:

1. All marking is at middle of the product body
2. All marking is in laser printing
3. Marking code body color: Black



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