

Description

Silicon Carbide (SiC) MOSFET use a completely new technology that provide superior switching performance and higher reliability compared to Silicon. In addition, the low ON resistance and compact chip size ensure low capacitance and gate charge. Consequently, system benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size.

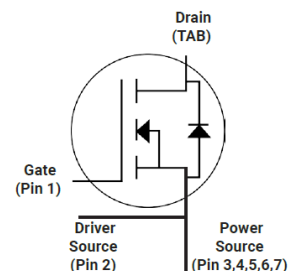
Features

- High Speed Switching with Low Capacitances
- AST Technology with 12V Gate Drive
- Lower Q_G and Device Capacitances(C_{oss} , C_{rss})
- Body Diode with Low V_F and Low Q_{RR}
- Faster and More Efficient Switching
- ROHS Compliant, Halogen free



Application

- Solar String Inveter and Central Inverter
- UPS
- Switch Mode Power Supplies
- Power Factor Correction Modules
- Battery Charging
- Auxiliary Power Supply
- High Voltage Coverter



Ordering Information

Part Number	Marking	Package	Packaging
ASC5N1700MT7PB	ASC5N1700MT7	TO-263-7	Tube

Absolute Maximum Ratings(Tc=25℃)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage	1700	V
I _D	Drain Current(continuous)at Tc=25℃	5	A
I _D	Drain Current(continuous)at Tc=100℃	3	A
I _{DM}	Drain Current (pulsed)	10	A
V _{GS}	Gate-Source Voltage	-5/+15	V
P _D	Power Dissipation T _C = 25℃	60	W
T _J , Tstg	Junction and Storage Temperature Range	-55 to +175	℃

Electrical Characteristics(T_J = 25℃ unless otherwise specified)
Typical Performance-Static

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
BV _{DS}	Drain-source Breakdown Voltage	I _D =100uA, V _{GS} =0V	1700			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =1700V, V _{GS} =0V, T _J =25℃			100	uA
I _{GSS}	Gate-body Leakage Current	V _{DS} =0V ; V _{GS} =-5 to 15V			100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D =1mA	1.5	2	3	V
V _{GS(on)}	Recommended turn-on Voltage	Static		12		V
V _{GS(off)}	Recommended turn-off Voltage			-3		V
R _{DS(on)}	Static Drain-source On Resistance	V _{GS} =12V, I _D =2A		750	1000	mΩ
		V _{GS} =12V, I _D =2A T _J =175℃		1350		mΩ

Typical Performance-Dynamic

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
C_{iss}	Input Capacitance	$V_{DS}=1000V, f=1MHz, V_{GS}=0V$ $V_{AC}=25mV$		200		pF
C_{oss}	Output Capacitance			6		pF
C_{rss}	Reverse Transfer Capacitance			1		pF
g_{fs}	Transconductance	$V_{DS}=10V, I_D=2A$		1		S
E_{OSS}	C_{OSS} Stored Energy	$V_{DS}=1000V, f=1MHz$		3		μJ
E_{ON}	Turn-On Energy (Body Diode)	$V_{DS}=1200V, V_{GS}=-3/12V$, $I_D=2A, L=1mH, T_J=175^\circ C$ $R_{ext}=25\Omega$		27		μJ
E_{OFF}	Turn-Off Energy (Body Diode)			8.4		μJ
Q_g	Total Gate Charge	$V_{DS}=1200V, V_{GS}=-3V/12V$, $I_D=2A$		8		nC
Q_{gs}	Gate-source Charge			1.5		nC
Q_{gd}	Gate-Drain Charge			3		nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=1200V, V_{GS}=-3V/12V$, $I_D=2A, L=1mH$ $R_{ext}=25\Omega$		11		ns
t_r	Rise Time			7		ns
$t_{d(off)}$	Turn-off Delay Time			9		ns
t_f	Fall Time			6		ns

Typical Performance-Reverse Diode($T_J = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_{FSD}	Forward Voltage	$V_{GS}=0V, I_F=1A, T_J=25^\circ C$		3.5	6	V
		$V_{GS}=0V, I_F=1A, T_J=175^\circ C$		3	6	V
I_S	Continuous Diode Forward Current	$V_{GS}=0V, T_C=25^\circ C$		5		A

Thermal Characteristics

Symbol	Parameter	Value.	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.5	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	40	$^\circ C/W$

The values are based on the junction-to case thermal impedance which is measured with the device mounted to a large heat sink assuming maximum junction temperature of $T_J(max)=175^\circ C$

Electrical Characteristics

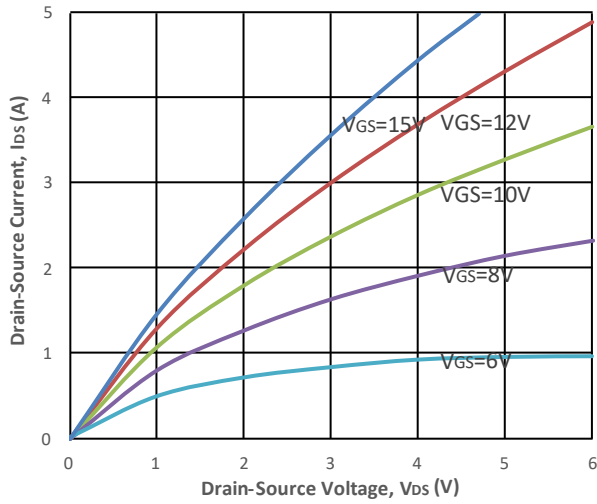
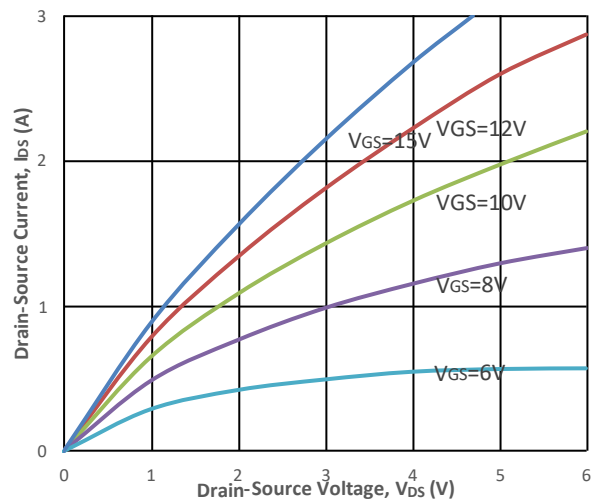
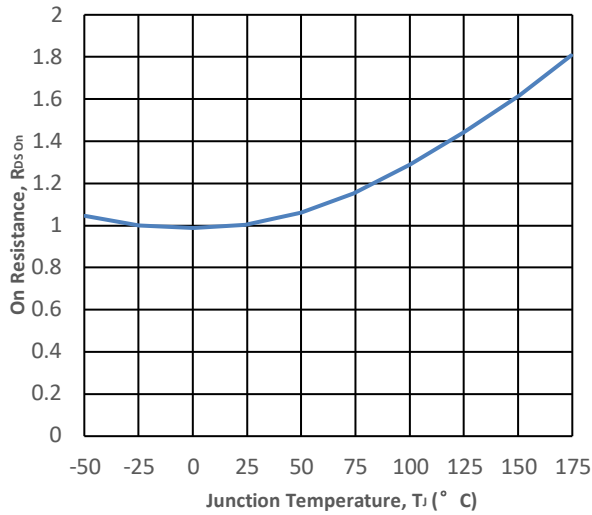
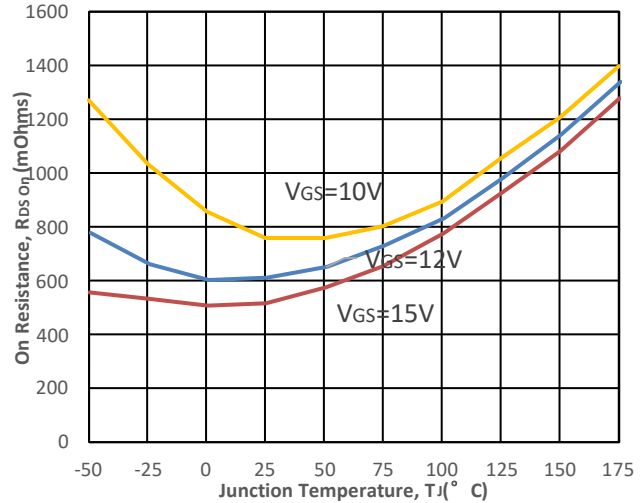
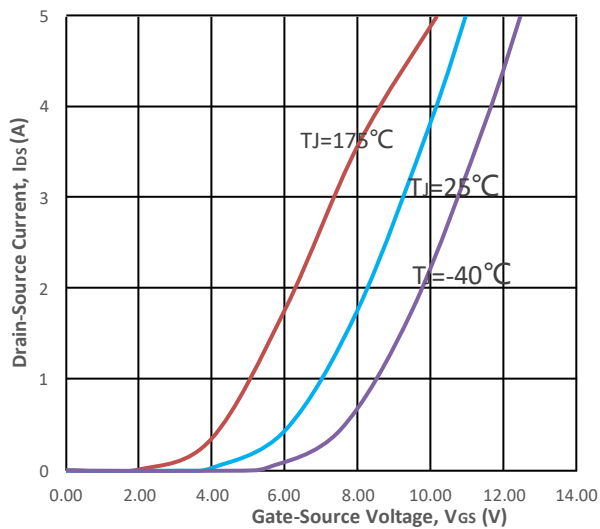
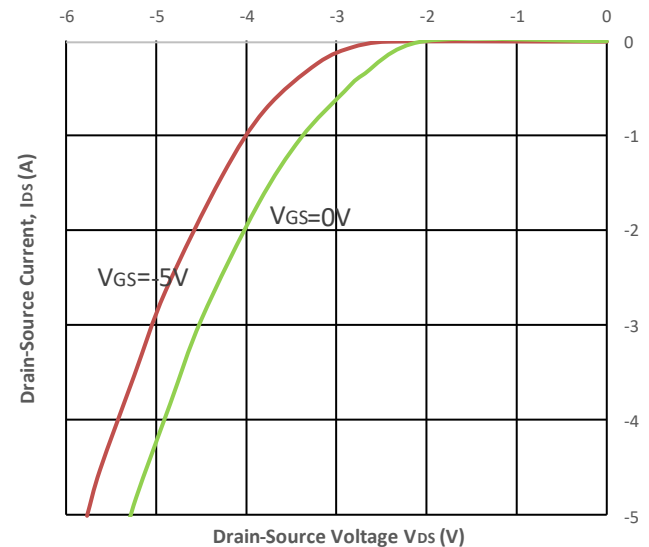
Fig1. Output characteristics ($T_J = 25^\circ\text{C}$)

Fig2. Output characteristics ($T_J = 175^\circ\text{C}$)

Fig3. Normalized On-Resistance vs. Temperature

Fig4. On-Resistance vs. Temperature

Fig5. Transfer Characteristic

Fig6. Body Diode Characteristic at 25°C


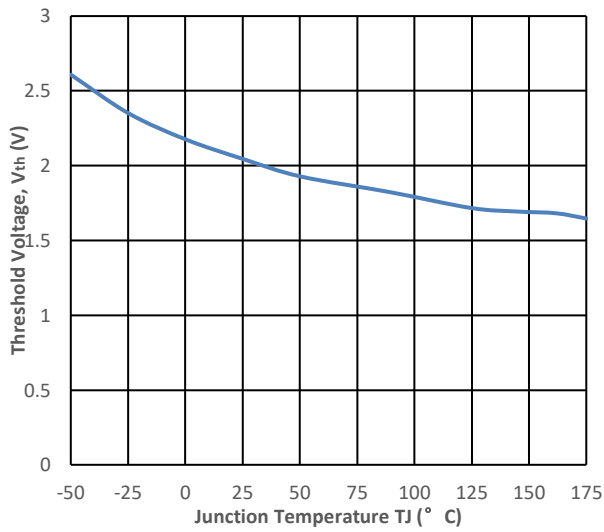
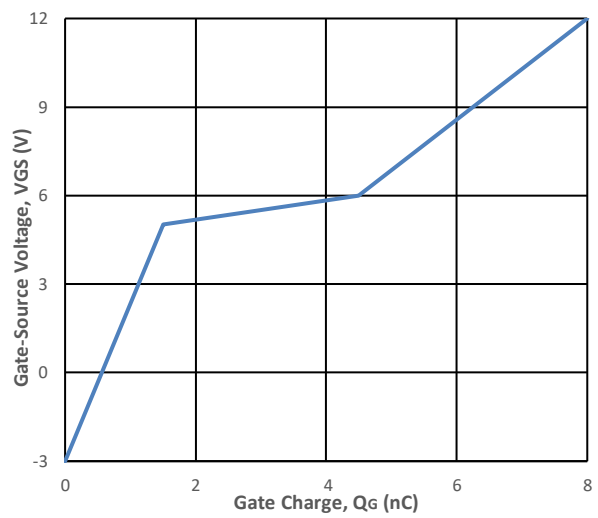
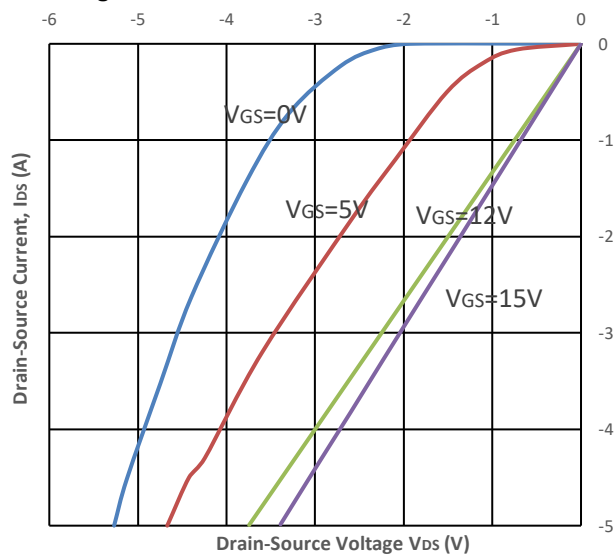
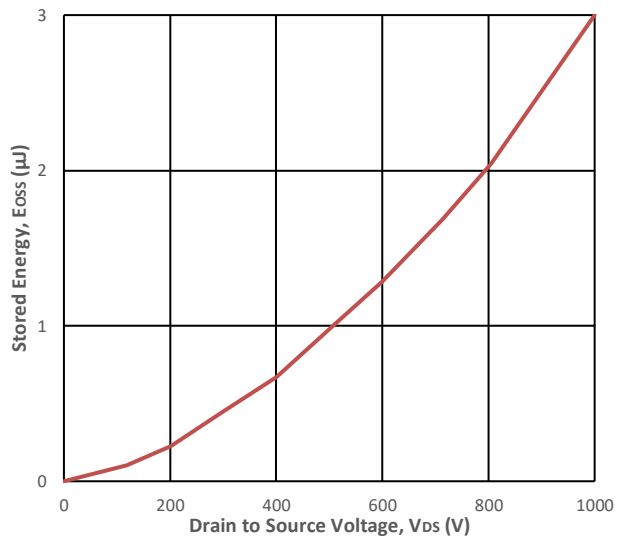
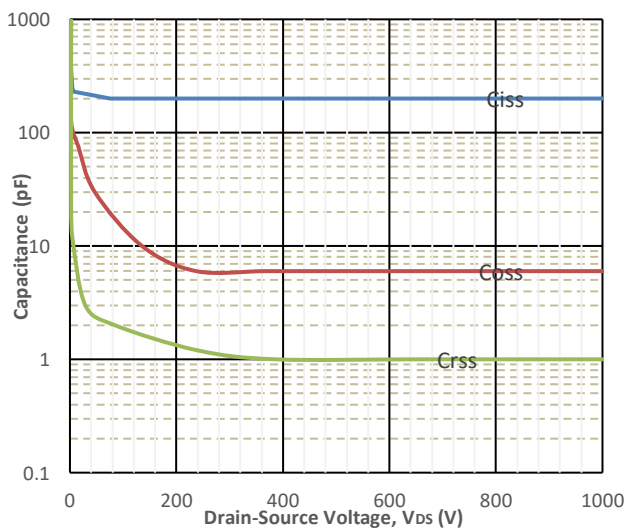
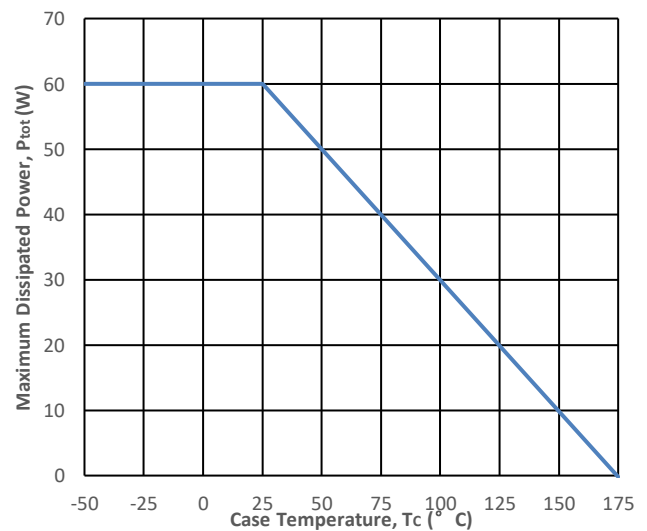
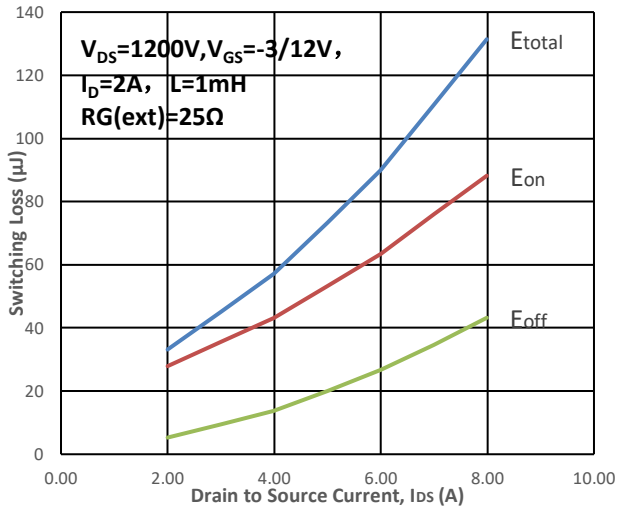
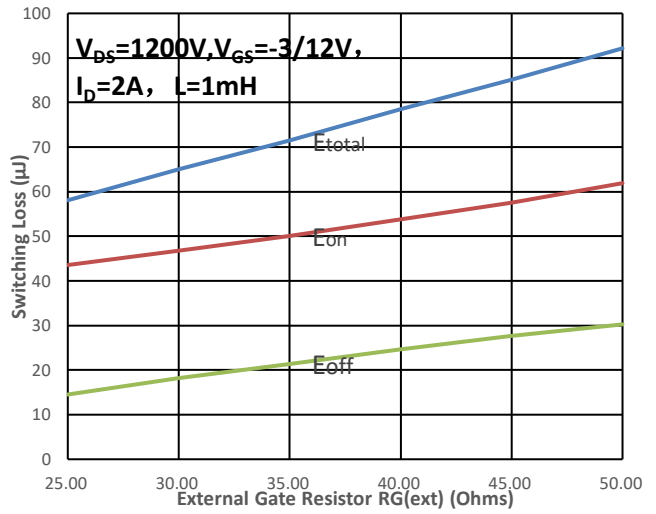
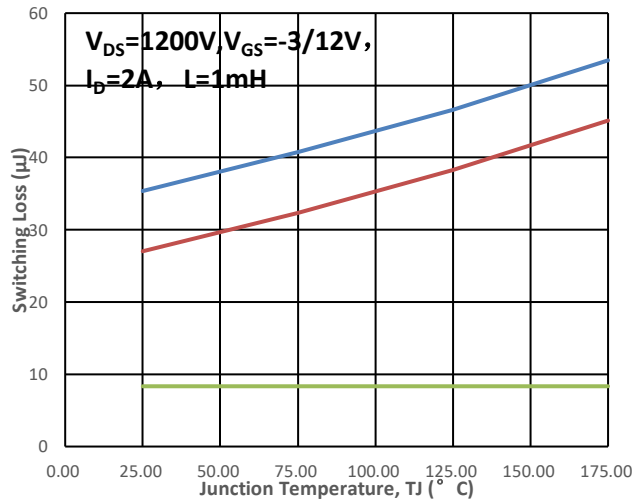
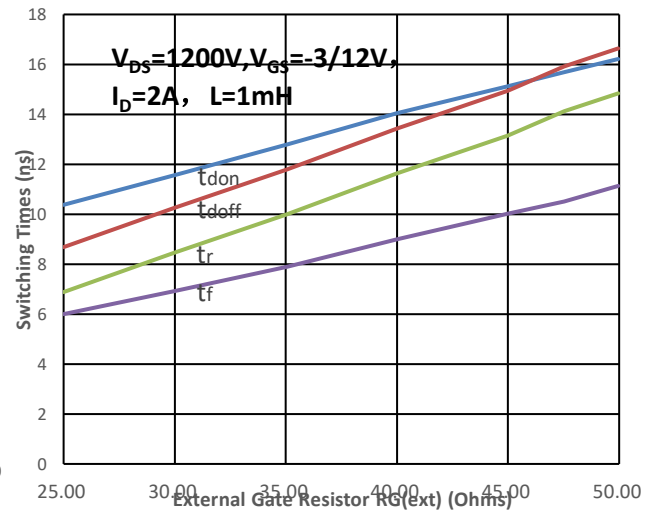
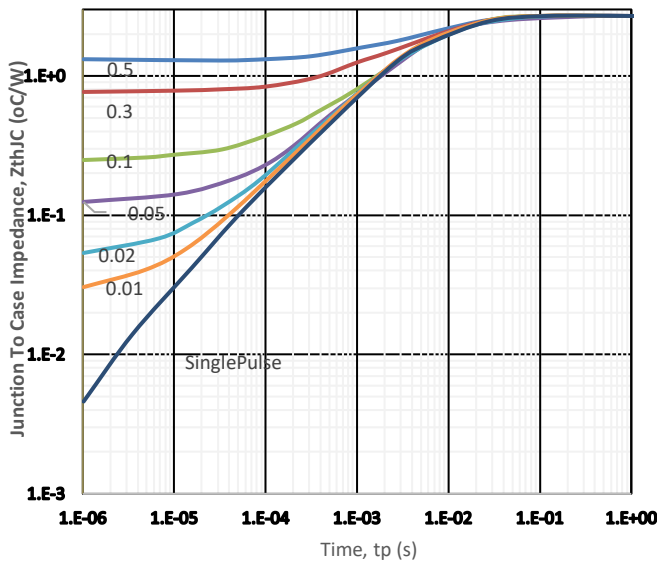
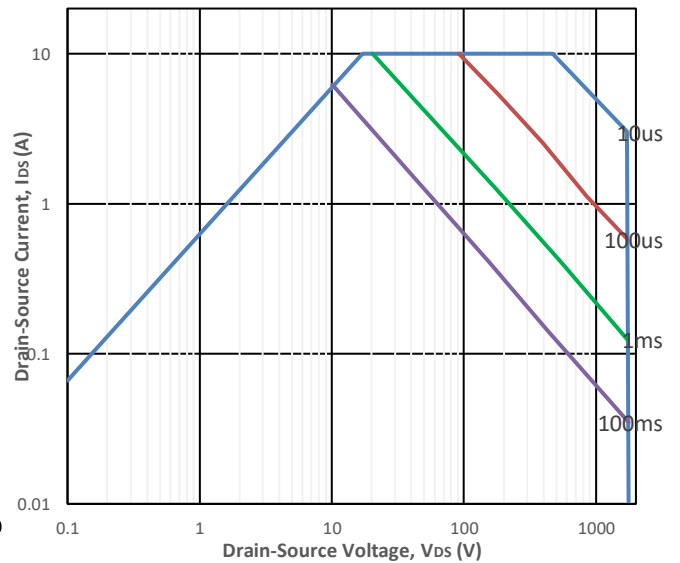
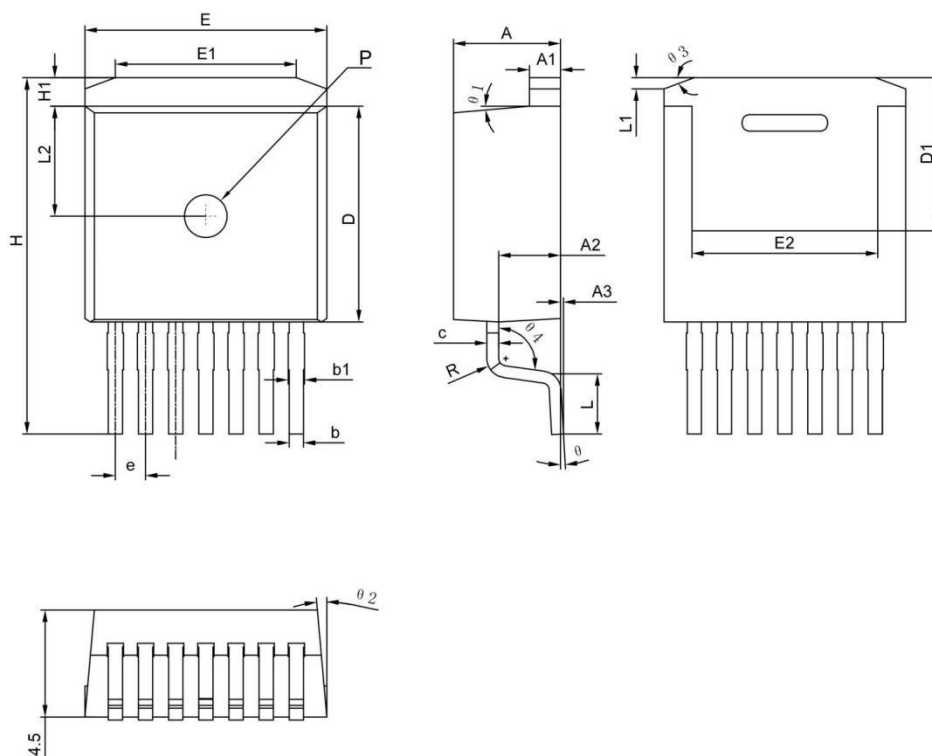
Fig7. Threshold Voltage vs. Temperature

Fig8. Gate Charge Characteristics

Fig9. 3rd Quadrant Characteristic at 25 °C

Fig10. Output Capacitor Stored Energy

Fig11. Capacitances vs. Drain-Source

Fig12. Max Power Dissipation Derating Vs Tc


Fig13. Switching Energy vs. Drain Current

Fig14. Switching Energy vs. $R_{G(ext)}$

Fig15. Switching Energy vs. Temperature

Fig16. Switching Times vs. $R_{G(ext)}$

Fig17. Transient Thermal Impedance

Fig18. Safe Operating Area


Package Drawing:

Dimensions: (UNIT: mm)

SYMBOL	MIN	TYPE	MAX
A	4.40	4.50	4.60
A1	1.25	1.30	1.40
A2	2.45	2.60	2.70
A3	0.05	0.13	0.20
b	0.50	0.60	0.70
b1	0.60	0.70	0.85
c	0.45	0.50	0.60
D	8.88	9.08	9.28
D1	6.25	6.45	6.65
E	9.88	10.18	10.28
E1	6.67	7.07	7.47
E2	7.67	7.82	7.97
e	1.17	1.27	1.37
H	14.75	15.00	15.25
H1	1.10	1.20	1.30
L	2.35	2.55	2.75
L1	0.37	0.57	0.77
L2	4.48	4.63	4.78
θ	0°	3°	5°
$\theta 1$	3°	5°	7°
$\theta 2$	3°	5°	7°
$\theta 3$	15°	20°	25°
R	0.75	0.80	0.85
P	1.70	1.80	1.90

Notes:

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Contact Info:

- Tel: +86 755-89300160
- Web: www.astsic.com
- Add: No.1 Rongtian Rd., Kendzi Street, Pingshan District, Shenzhen, China

Offical Account:



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