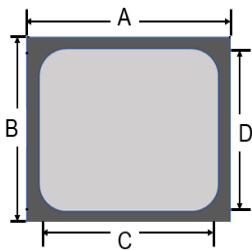




SPD10C120KM4 1200V 10A SiC Schottky Diode

DESCRIPTION: SM provides leading edge technology for the SiC Schottky barrier diodes with the advanced thin-wafer technology. The new family of 1200V products showing improved efficiency over all load conditions, attributed to its lower figure of merit ($Q_c \times V_f$).

DIE INFORMATION



FEATURES

- 1200V Schottky Barrier Diode
- Zero Reverse Recovery/Zero Forward Recovery
- 100% UIS screened
- Temperature Independent Switching Behavior

TYPICAL APPLICATIONS

- Switch mode power supply
- Power factor correction
- Solar inverter
- Uninterruptible power supply

MAJOR RATINGS AND CHARACTERISTICS

Symbol	Parameter	Value	Unit	Test Conditions
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V	
V_R	DC Peak Blocking Voltage	1200	V	
I_F	Continuous Forward Current	10	A	$T_C=150^{\circ}\text{C}$
I_{FRM}	Repetitive Peak Forward Surge Current	40	A	$T_C=25^{\circ}\text{C}$, $t_p = 10$ ms, Half Sine Wave
I_{FSM}	Non-Repetitive Peak Forward Surge Current	80*	A	$T_C=25^{\circ}\text{C}$, $t_p = 10$ ms, Half Sine Wave
$\int i^2 dt$	i^2t value	32	A^2s	$T_C=25^{\circ}\text{C}$, $t_p=10$ ms, Half Sine Wave
T_j, T_{stg}	Operating Junction and Storage Temperature	-40 ~ +175	$^{\circ}\text{C}$	

**ELECTRICAL CHARACTERISTICS (25 °C unless otherwise noted)**

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions
V _{DC}	DC blocking voltage	1200			V	I _R = 250μA, T _J = 25 °C
V _F	Forward Voltage		1.50 2.20	1.75	V	I _F = 10A, T _J = 25 °C I _F = 10A, T _J = 175 °C
I _R	Reverse Current		2 15	80	μA	V _R = 1200V, T _J = 25 °C V _R = 1200V, T _J = 175 °C
Q _C	Total Capacitive Charge		37.1		nC	V _R = 800V
C	Total Capacitance		0.666 0.035 0.026		nF	V _R = 0V, T _J = 25 °C, f = 1 MHz V _R = 400V, T _J = 25 °C, f = 1 MHz V _R = 800V, T _J = 25 °C, f = 1 MHz
E _C	Capacitance Stored Energy		8.32		μJ	V _R = 800V

MECHANICAL PARAMETERS

PARAMETERS	TYP	UNIT
Die Size	1.947×1.945	mm
Anode Pad Opening	1.603×1.601	mm
Thickness	100 ± 10	μm
Wafer Size	150	mm
Anode Metalization Al	4	um
Cathode Metalization	Ti/Ni/Ag	/
Frontside Passivation	Polyimide	/



Typical Characteristics

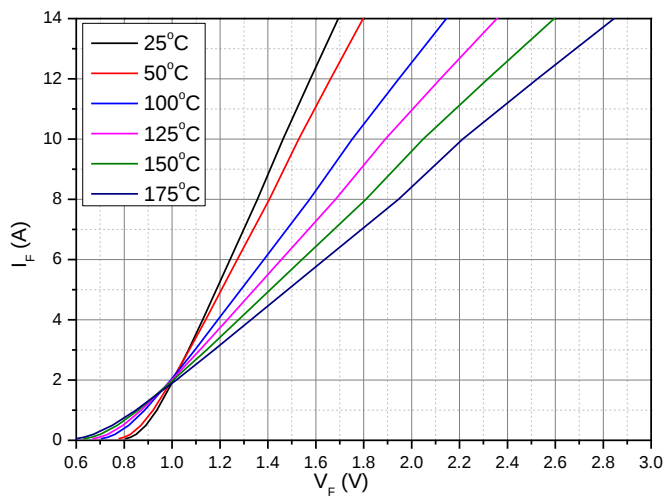


Figure 1. Forward Characteristics

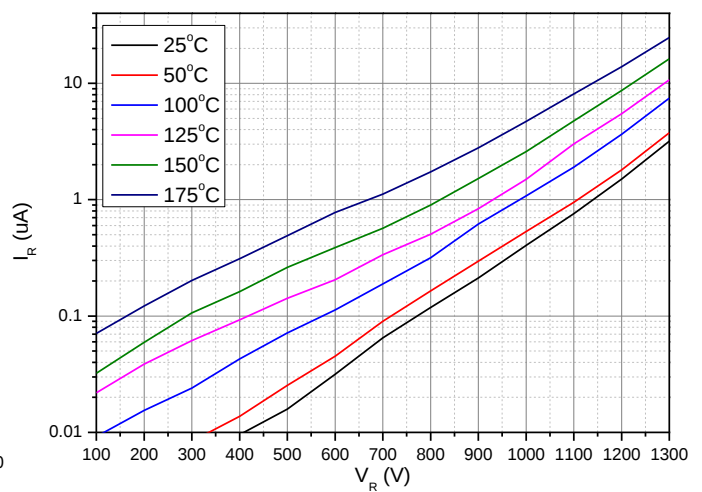


Figure 2. Reverse Characteristics

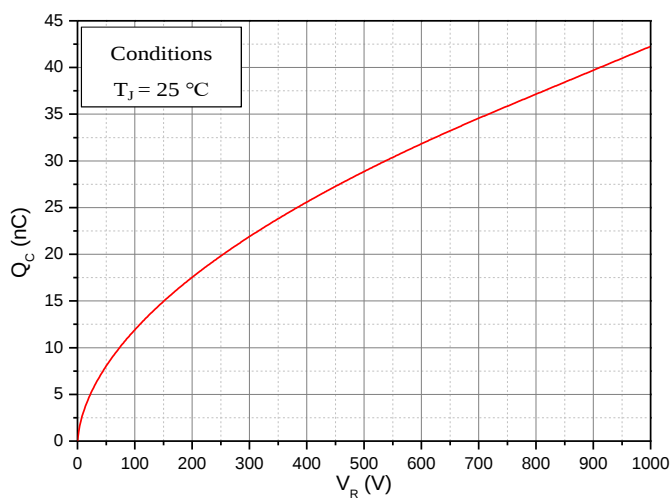


Figure 3. Total Capacitance Charge vs. Reverse Voltage

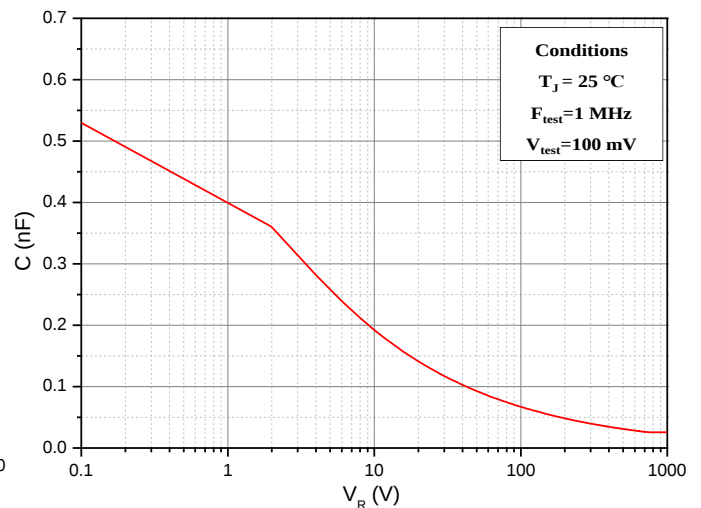
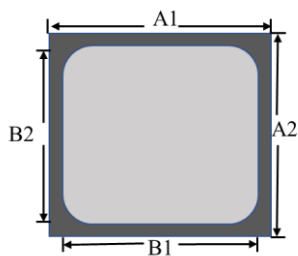


Figure 4. Capacitance vs. Reverse Voltage

Chip Dimensions



symbol	Dimens on	
	mm	inch
A 1 (die size)	1.947	0.077
A 2 (die size)	1.945	0.077
B1 (anode pad opening)	1.603	0.063
B2 (anode pad opening)	1.601	0.063



Notes

- Some data are test results of the die packaged by TO220.
 - This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.
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