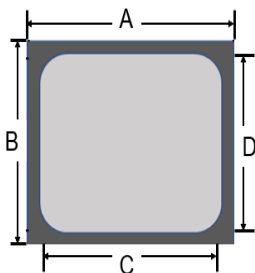




## SPD10C65KM4 650V 10A SiC Schottky Diode

**DESCRIPTION:** SM provides leading edge technology for the SiC Schottky barrier diodes with the advanced thin-wafer technology. The new family of 650V products showing improved efficiency over all load conditions, attributed to its lower figure of merit ( $Q_c \times V_f$ ).

### DIE INFORMATION



### FEATURES

- 650V Schottky Barrier Diode
- Zero Reverse Recovery/Zero Forward Recovery
- 100% UIS screened
- Temperature Independent Switching Behavior

### TYPICAL APPLICATIONS

- Switch mode power supply
- Power factor correction
- Solar inverter
- Uninterruptible power supply

### MAJOR RATINGS AND CHARACTERISTICS

| Symbol        | Parameter                                 | Value | Unit                 | Test Conditions                                        |
|---------------|-------------------------------------------|-------|----------------------|--------------------------------------------------------|
| $V_{RRM}$     | Repetitive Peak Reverse Voltage           | 650   | V                    |                                                        |
| $V_R$         | DC Peak Blocking Voltage                  | 650   | V                    |                                                        |
| $I_F$         | Continuous Forward Current                | 10    | A                    | $T_C=150^\circ\text{C}$                                |
| $I_{FRM}$     | Repetitive Peak Forward Surge Current     | 32    | A                    | $T_C=25^\circ\text{C}$ , $t_p = 10$ ms, Half Sine Wave |
| $I_{FSM}$     | Non-Repetitive Peak Forward Surge Current | 58    | A                    | $T_C=25^\circ\text{C}$ , $t_p = 10$ ms, Half Sine Wave |
| EAS           | Minimum Repetitive Pulse Avalanche Energy | 10    | mJ                   | $L=5\text{mH}$                                         |
| $\int i^2 dt$ | $i^2 t$ value                             | 16.8  | $\text{A}^2\text{s}$ | $T_C=25^\circ\text{C}$ , $t_p=10$ ms, Half Sine Wave   |



|                |                                            |            |    |  |
|----------------|--------------------------------------------|------------|----|--|
| $T_j, T_{stg}$ | Operating Junction and Storage Temperature | -40 ~ +175 | °C |  |
|----------------|--------------------------------------------|------------|----|--|

**ELECTRICAL CHARACTERISTICS (25 °C unless otherwise noted)**

| Symbol   | Parameter                 | Min. | Typ.                    | Max. | Unit    | Test Conditions                                                                                                                                              |
|----------|---------------------------|------|-------------------------|------|---------|--------------------------------------------------------------------------------------------------------------------------------------------------------------|
| $V_{DC}$ | DC blocking voltage       | 650  |                         |      | V       | $I_R = 250\mu A, T_J = 25\text{ °C}$                                                                                                                         |
| $V_F$    | Forward Voltage           |      | 1.30<br>1.50            | 1.60 | V       | $I_F = 10A, T_J = 25\text{ °C}$<br>$I_F = 10A, T_J = 175\text{ °C}$                                                                                          |
| $I_R$    | Reverse Current           |      | 8.0<br>30               | 50   | $\mu A$ | $V_R = 650V, T_J = 25\text{ °C}$<br>$V_R = 650V, T_J = 175\text{ °C}$                                                                                        |
| Qc       | Total Capacitive Charge   |      | 22.4                    |      | nC      | $V_R = 400V$                                                                                                                                                 |
| C        | Total Capacitance         |      | 0.711<br>0.041<br>0.037 |      | nF      | $V_R = 0V, T_J = 25\text{ °C}, f = 1\text{ MHz}$<br>$V_R = 200V, T_J = 25\text{ °C}, f = 1\text{ MHz}$<br>$V_R = 400V, T_J = 25\text{ °C}, f = 1\text{ MHz}$ |
| $E_C$    | Capacitance Stored Energy |      | 2.96                    |      | $\mu J$ | $V_R = 400V$                                                                                                                                                 |

**MECHANICAL PARAMETERS**

| PARAMETERS            | TYP         | UNIT    |
|-----------------------|-------------|---------|
| Die Size              | 1.545×1.532 | mm      |
| Anode Pad Opening     | 1.29×1.277  | mm      |
| Thickness             | 100         | $\mu m$ |
| Wafer Size            | 150         | mm      |
| Anode Metalization Al | 4           | $\mu m$ |
| Cathode Metalization  | Ti/Ni/Ag    | /       |
| Frontside Passivation | Polyimide   | /       |



## Typical Characteristics

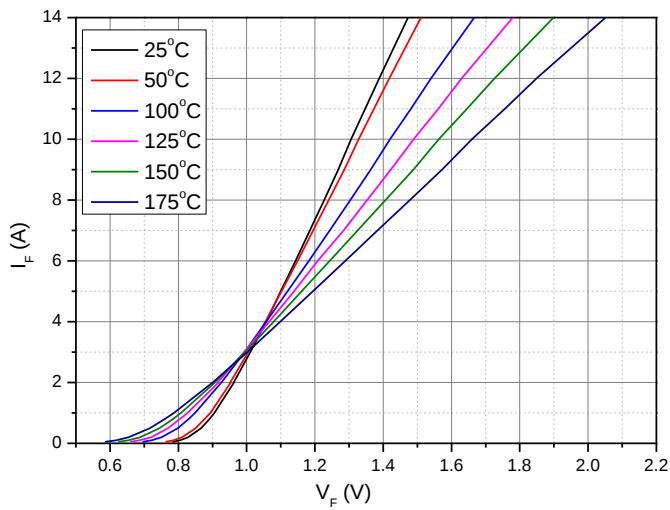


Figure 1. Forward Characteristics

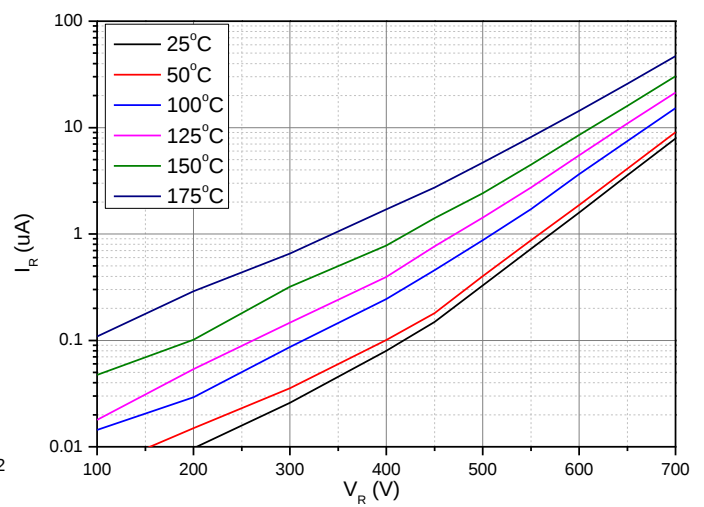


Figure 2. Reverse Characteristics

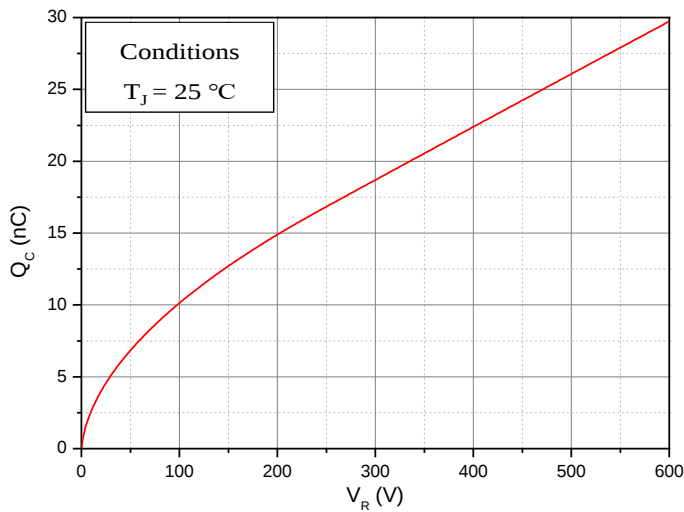


Figure 3. Total Capacitance Charge vs. Reverse Voltage

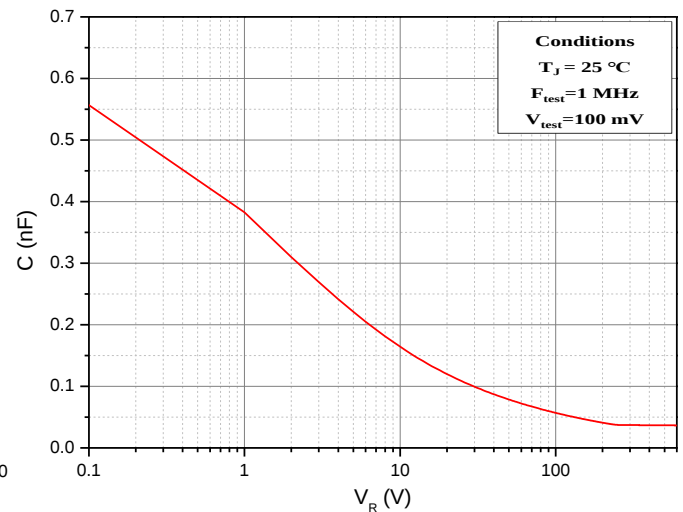
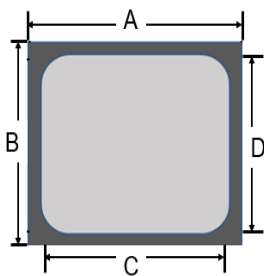


Figure 4. Capacitance vs. Reverse Voltage

## Chip Dimensions



| Symbol                | Dimension |       |
|-----------------------|-----------|-------|
|                       | mm        | inch  |
| A (die size)          | 1.545     | 0.061 |
| B(die size)           | 1.532     | 0.060 |
| C (anode pad opening) | 1.29      | 0.051 |
| D (anode pad opening) | 1.277     | 0.050 |



## Notes

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- Some data are test results of the die packaged by TO220.

**This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.**

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