

DATASHEET

SISD0750ED120i20_A01

ED-Type phase leg IGBT module



$V_{CE} = 1200 \text{ V}$

$I_C = 2 \times 750 \text{ A}$

- *i*20 ultra-low loss fine pattern Trench IGBT chipset
- Baseplate isolation with reinforced Al_2O_3 ceramic
- Cu baseplate for low thermal resistance
- Improved Package with reduced internal impedance and terminal heating

Maximum ratings¹

PARAMETER	SYMBOL	CONDITIONS	MIN	MAX	UNIT
Collector-emitter voltage	V_{CES}	$V_{GE} = 0 \text{ V}$, $T_{vj} = 25 \text{ °C}$		1200	V
DC collector current	I_C	$T_C = 110 \text{ °C}$, $T_{vj} = 175 \text{ °C}$		750	A
Peak collector current	I_{CM}	$t_p = 1 \text{ ms}$		1500	A
Gate-emitter voltage	V_{GES}		-20	20	V
Total power dissipation	P_{tot}	$T_C = 25 \text{ °C}$, $T_{vj} = 175 \text{ °C}$, per switch		3750	W
DC forward current	I_F			750	A
Peak forward current	I_{FRM}	$t_p = 1 \text{ ms}$		1500	A
Surge current	I_{FSM} I^2t	$V_R = 0 \text{ V}$, $T_{vj} = 150 \text{ °C}$, $t_p = 10 \text{ ms}$, half-sinewave		2750 37500	A A ² s
Isolation voltage	V_{isol}	1 min, $f = 50 \text{ Hz}$		3400	V
Junction operating temperature	$T_{vj(op)}$		-40	175 ²	°C
Case temperature	T_C		-40	125 ³	°C
Storage temperature	T_{stg}		-40	125	°C
Mounting torques ⁴	M_S	Base-heatsink, M5 screws	3	6	Nm
	M_{t1}	Main terminals, M6 screws	3	6	Nm



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¹ Maximum rated values indicate limits beyond which damage to the device may occur per IEC 60747;

² $T_{vj(op)} > 150 \text{ °C}$ allowed for overload conditions, in maximum for 60s and less than 20% of operation time;

³ For UL1557 compliance T_{Cmax} must be limited to 125°C;

⁴ For details, please refer to the mounting instructions.

IGBT⁵

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Collector(-emitter) breakdown voltage	V _{(BR)CES}	V _{GE} = 0 V, I _C = 10 mA, T _{vj} = 25°C		1200			V
Collector-emitter saturation voltage ⁶	V _{CEsat}	I _C = 750 A, V _{GE} = 15 V	T _{vj} = 25 °C		1.65	2	V
			T _{vj} = 125 °C		1.95		V
			T _{vj} = 175 °C		2.1		V
Collector cut-off current	I _{CES}	V _{CE} = 1200 V, V _{GE} = 0 V	T _{vj} = 25 °C			1	mA
			T _{vj} = 125 °C		1		mA
			T _{vj} = 175 °C		20		mA
Gate leakage current	I _{GES}	V _{CE} = 0 V, V _{GE} = ±20 V		-0.5		0.5	μA
Gate-emitter threshold voltage	V _{GE(th)}	I _C = 37.5 mA, V _{CE} = V _{GE} , T _{vj} = 25°C		5	6.2	7.5	V
Gate charge	Q _G	I _C = 750 A, V _{CE} = 600 V, V _{GE} = -15 V .. 15 V			5.3		μC
Input capacitance	C _{ies}	V _{CE} = 25 V, V _{GE} = 0 V, f = 100 kHz, T _{vj} = 25 °C			44		nF
Output capacitance	C _{oes}				3.9		nF
Reverse tansfer capacitance	C _{res}				2.1		nF
Internal gate resistor	R _{Gint}	Per switch			1.2		Ω
Turn-on delay time	t _{d(on)}	V _{CC} = 600 V, I _C = 750 A, R _G = 0.47 Ω, V _{GE} = ± 15 V, L _S = 30 nH, inductive load	T _{vj} = 25 °C		120		ns
			T _{vj} = 125 °C		145		ns
			T _{vj} = 175 °C		155		ns
Rise time	t _r		T _{vj} = 25 °C		70		ns
			T _{vj} = 125 °C		80		ns
			T _{vj} = 175 °C		80		ns
Turn-off delay time	t _{d(off)}	V _{CC} = 600 V, I _C = 750 A, R _G = 1.5 Ω, V _{GE} = ± 15 V, L _S = 30 nH, inductive load	T _{vj} = 25 °C		550		ns
			T _{vj} = 125 °C		650		ns
			T _{vj} = 175 °C		690		ns
Fall time	t _f		T _{vj} = 25 °C		165		ns
			T _{vj} = 125 °C		285		ns
			T _{vj} = 175 °C		325		ns
Turn-on switching energy	E _{on}	V _{CC} = 600 V, I _C = 750 A, R _G = 0.47 Ω, V _{GE} = ± 15 V, L _S = 30 nH, inductive load	T _{vj} = 25 °C		33		mJ
			T _{vj} = 125 °C		82		mJ
			T _{vj} = 175 °C		115		mJ
Turn-off switching energy	E _{off}	V _{CC} = 600 V, I _C = 750 A, R _G = 1.5 Ω, V _{GE} = ± 15 V, L _S = 30 nH, inductive load	T _{vj} = 25 °C		82		mJ
			T _{vj} = 125 °C		110		mJ
			T _{vj} = 175 °C		120		mJ
Short circuit current	I _{SC}	t _{pcs} ≤ 10 μs, V _{GE} = 15 V, T _{vj} = 175 °C, V _{CC} = 800 V, V _{CEM} Chip ≤ 1200 V			2000		A

⁵ Characteristic values according to IEC 60747-9

⁶ Collector-emitter saturation voltage is given at chip-level



Diode⁷

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Forward voltage ⁸	V _F	I _F = 750 A	T _{vj} = 25 °C		2	2.5	V
			T _{vj} = 125 °C		2.15		V
			T _{vj} = 175 °C		2.15		V
Peak reverse recovery current	I _{RM}	V _R = 600 V, I _F = 750 A, di/dt = 9100 A/μs (175°C), R _G = 0.47 Ω, V _{GE} = ± 15 V, L _S = 30 nH, inductive load	T _{vj} = 25 °C		515		A
			T _{vj} = 125 °C		590		A
			T _{vj} = 175 °C		610		A
Recovery charge	Q _{rr}		T _{vj} = 25 °C		60		μC
			T _{vj} = 125 °C		110		μC
			T _{vj} = 175 °C		145		μC
Reverse recovery time	t _{rr}		T _{vj} = 25 °C		240		ns
			T _{vj} = 125 °C		760		ns
			T _{vj} = 175 °C		980		ns
Reverse recovery energy	E _{rec}	T _{vj} = 25 °C		30		mJ	
		T _{vj} = 125 °C		45		mJ	
		T _{vj} = 175 °C		55		mJ	

NTC Thermistor

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Rated resistance	R_{25}	$T_C = 25 \text{ °C}$		5		k Ω
R100	R_{100}	$T_C = 100 \text{ °C}$	468		518	Ω
B-value B	$B_{25/50}$	$R_2 = R_{25} \exp [B_{25/50}(1/T_2 - 1/(298.15\text{K}))]$		3375		K
B-value B	$B_{25/100}$	$R_2 = R_{25} \exp [B_{25/100}(1/T_2 - 1/(298.15\text{K}))]$		3433		K

⁷ Characteristic values according to IEC 60747-2

⁸ Forward voltage is given at chip-level



Package properties⁹

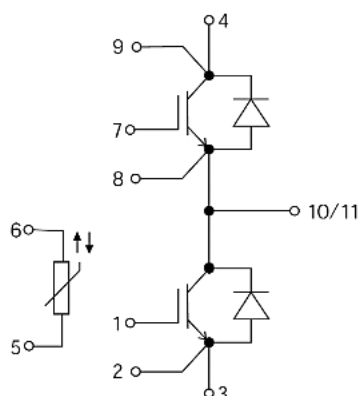
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
IGBT thermal resistance junction to case IGBT	$R_{th(j-c)IGBT}$	Per switch				0.040	K/W
Diode thermal resistance junction to case	$R_{th(j-c)Diode}$					0.067	K/W
IGBT thermal resistance case to heatsink IGBT	$R_{th(c-s)IGBT}$	IGBT per switch			0.030		K/W
Diode thermal resistance case to heatsink	$R_{th(c-s)Diode}$	diode per switch			0.036		K/W
Comparative tracking index	CTI			200			
Module stray inductance	$L_{s\ CE}$	Per switch			16		nH
Resistance, terminal chip	$R_{CC'+EE'}$	Per switch	$T_{vj} = 25\ ^\circ C$		0.7		m Ω
			$T_{vj} = 125\ ^\circ C$		1		m Ω
			$T_{vj} = 175\ ^\circ C$		1.1		m Ω
Maximum RMS DC-Terminal current	$I_{Term\ RMS}$		$T_{Terminal} = 90^\circ C$		580		A
			$T_{Terminal} = 105^\circ C$		565		A

Mechanical properties

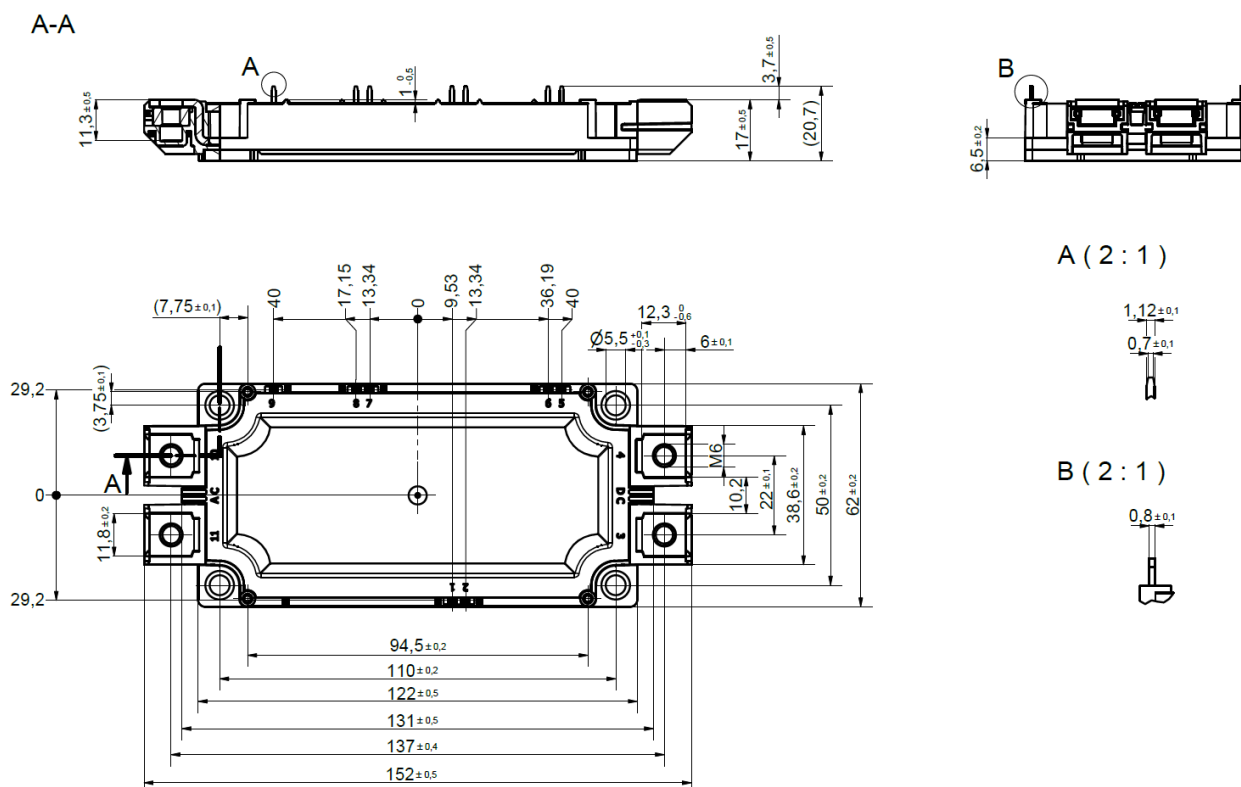
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNIT
Dimensions	L x W x H	Typical		152 x 62 x 17			mm ³
Clearance distance in air	d_a	According to IEC 60664-1 and EN 50124-1	Terminal to base:	12.5			mm
			Terminal to terminal:	10			mm
Surface creepage distance	d_s	According to IEC 60664-1 and EN 50124-1 IEC 60664-1EN 50124-1	Terminal to base:	15			mm
			Terminal to terminal:	13			mm
Mass	m				350		g

⁹ Package and mechanical properties according to IEC 60747-15

Outline drawing



Outline drawing

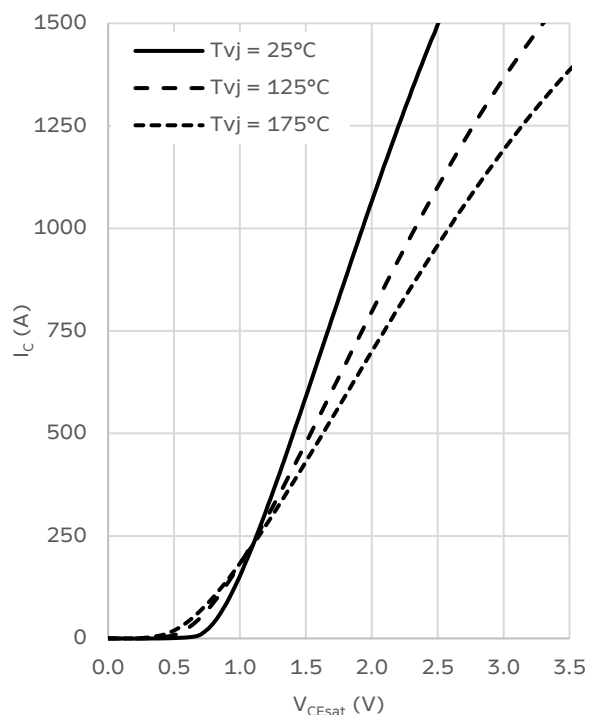


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Characteristics

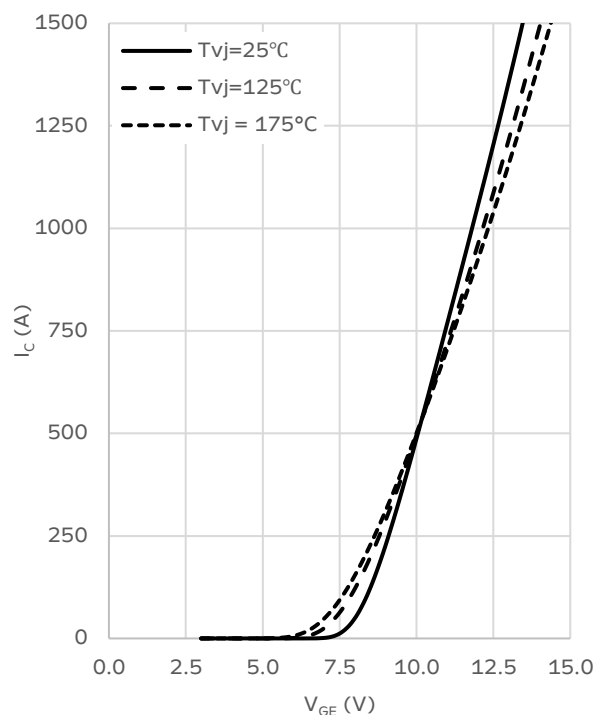
IGBT on-state characteristics (typical)

$I_C = f(V_{CE})$
 $V_{GE} = 15 \text{ V}$



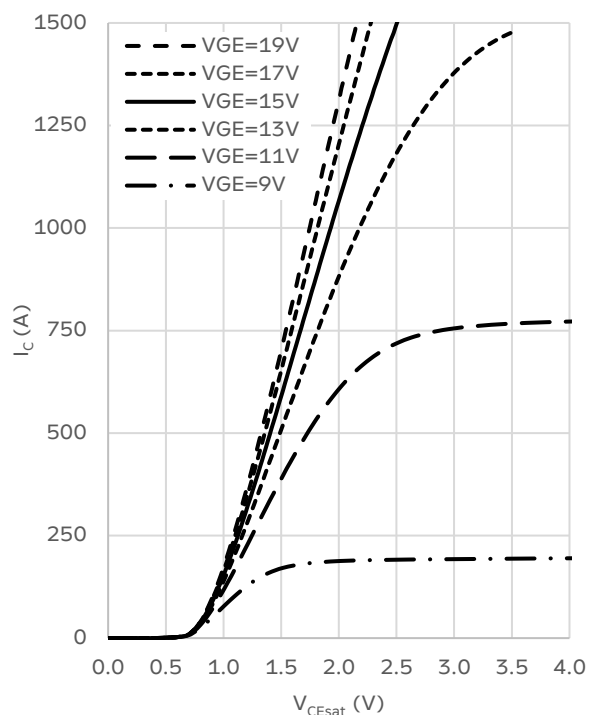
IGBT transfer characteristics (typical)

$I_C = f(V_{GE})$
 $V_{CE} = 20 \text{ V}$



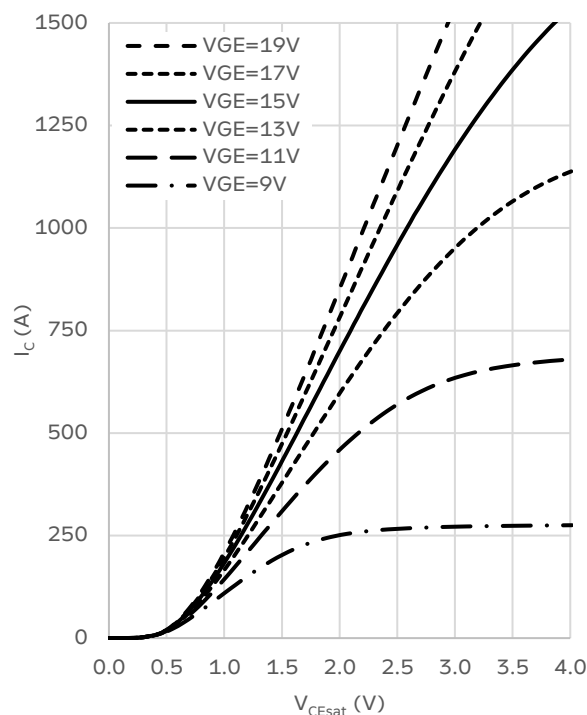
IGBT output characteristics (typical)

$I_C = f(V_{CE})$
 $T_{vj} = 25^\circ\text{C}$



IGBT output characteristics (typical)

$I_C = f(V_{CE})$
 $T_{vj} = 175^\circ\text{C}$

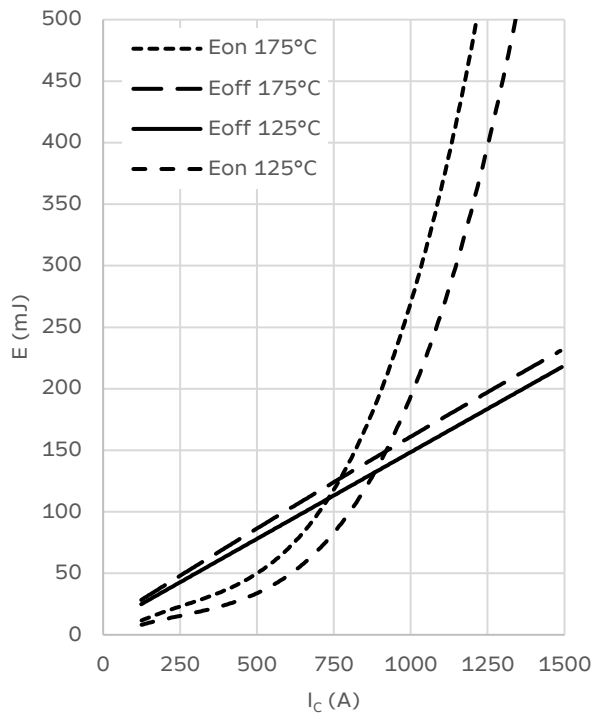




IGBT switching losses (typical)

$$E = f(I_{CE})$$

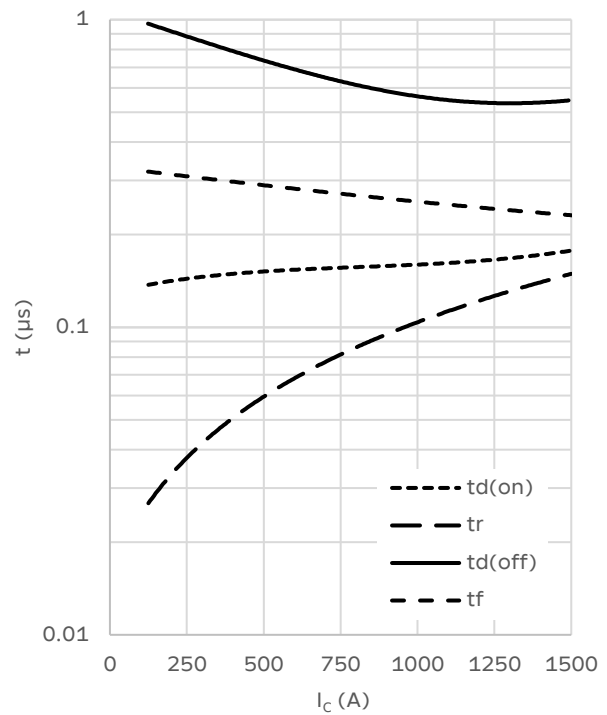
$V_{CE} = 600 \text{ V}$, $R_{Gon} = 0.47 \text{ } \Omega$, $R_{Goff} = 1.5 \text{ } \Omega$, $V_{GE} = -15/+15 \text{ V}$



IGBT switching times (typical)

$$t = f(I_{CE}), T_{vj} = 175 \text{ } ^\circ\text{C}$$

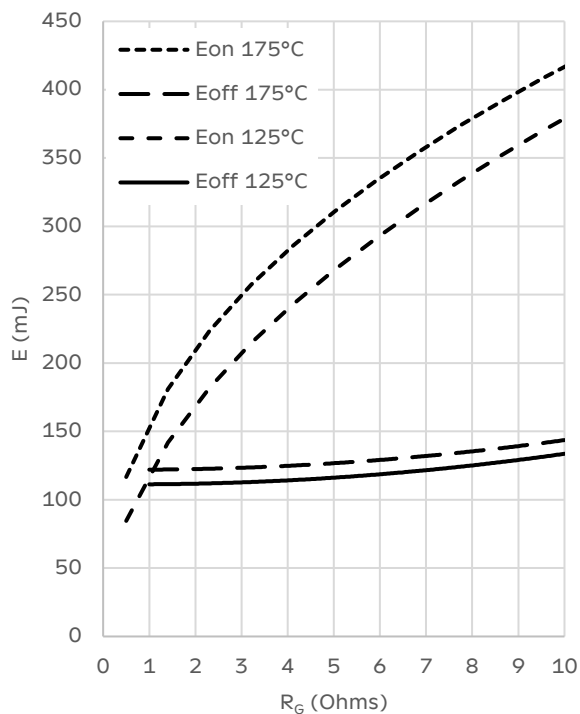
$V_{CE} = 600 \text{ V}$, $R_{Gon} = 0.47 \text{ } \Omega$, $R_{Goff} = 1.5 \text{ } \Omega$, $V_{GE} = -15/+15 \text{ V}$



IGBT switching losses (typical)

$$E = f(R_G)$$

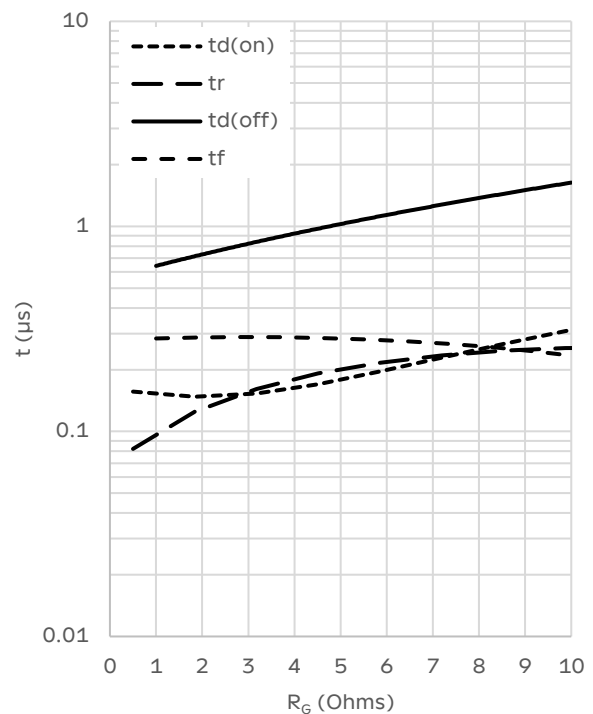
$V_{CE} = 600 \text{ V}$, $I_C = 750 \text{ A}$, $V_{GE} = -15/+15 \text{ V}$



IGBT switching times (typical)

$$t = f(R_G), T_{vj} = 175 \text{ } ^\circ\text{C}$$

$V_{CE} = 600 \text{ V}$, $I_C = 750 \text{ A}$, $V_{GE} = -15/+15 \text{ V}$

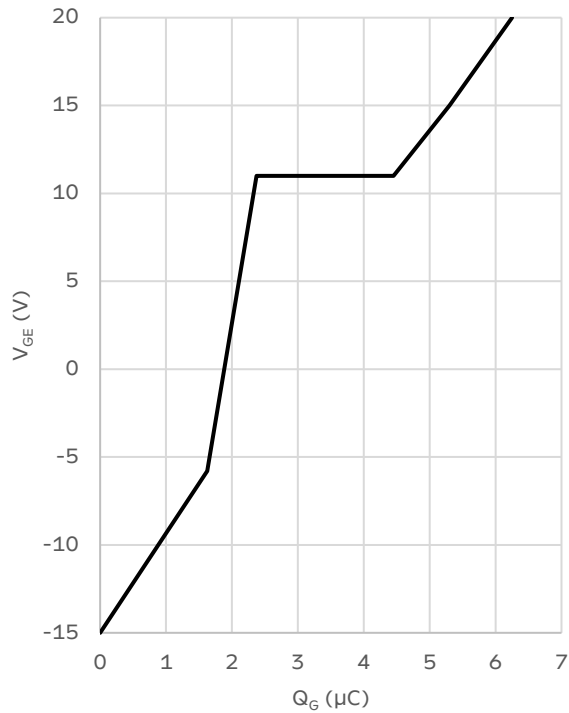




IGBT gate charge (typical)

$V_{GE} = f(Q_G)$, $T_{vj} = 25\text{ °C}$

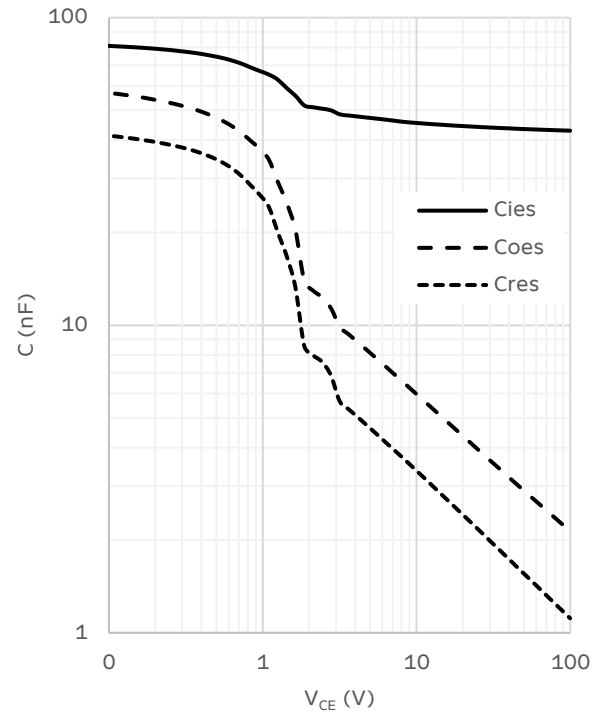
$V_{CE} = 600\text{ V}$, $I_C = 750\text{ A}$



Capacitance characteristics (typical)

$C = f(V_{CE})$, $T_{vj} = 25\text{ °C}$

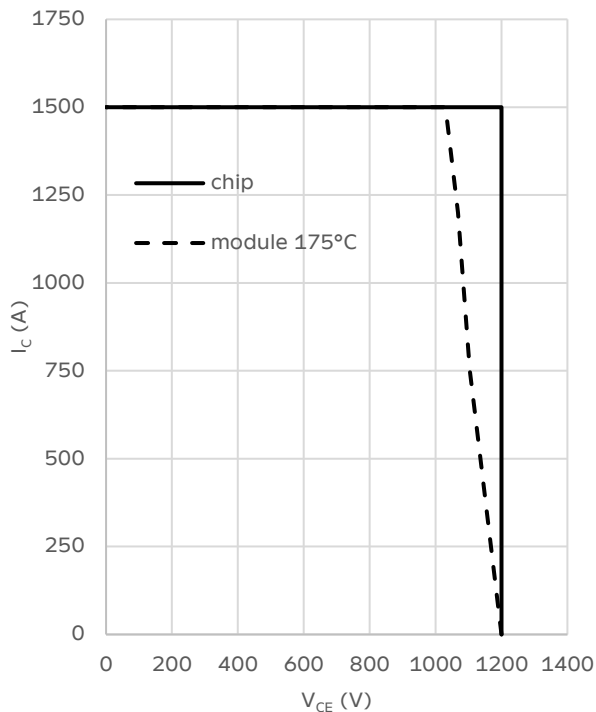
$f = 100\text{ kHz}$, $V_{GE} = 0\text{ V}$



IGBT RBSOA

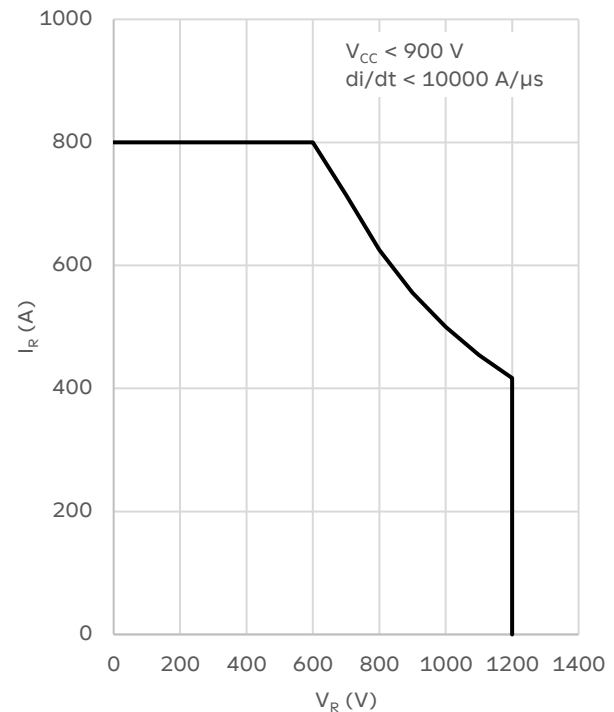
$I_C = f(V_{CEm})$

$R_{Goff} = 1.5\text{ }\Omega$, $V_{GE} = \pm 15\text{ V}$



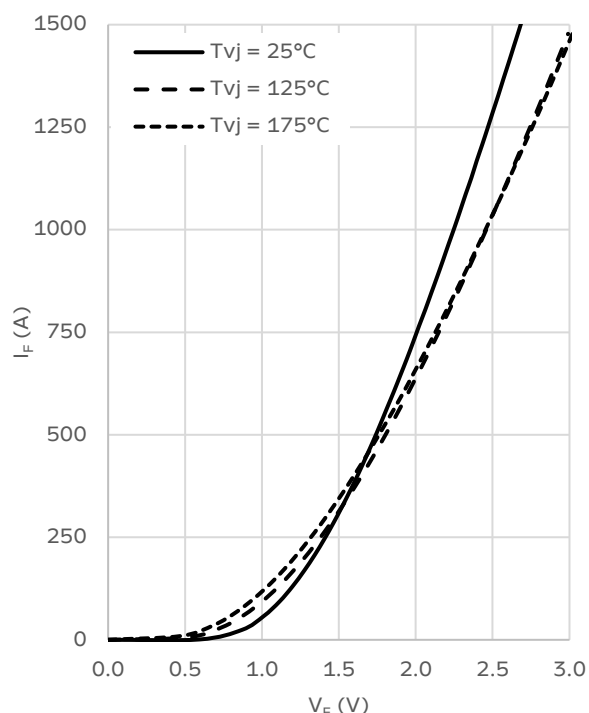
Diode SOA

$T_{vj} \leq 175\text{ °C}$



Diode forward characteristic (typical)

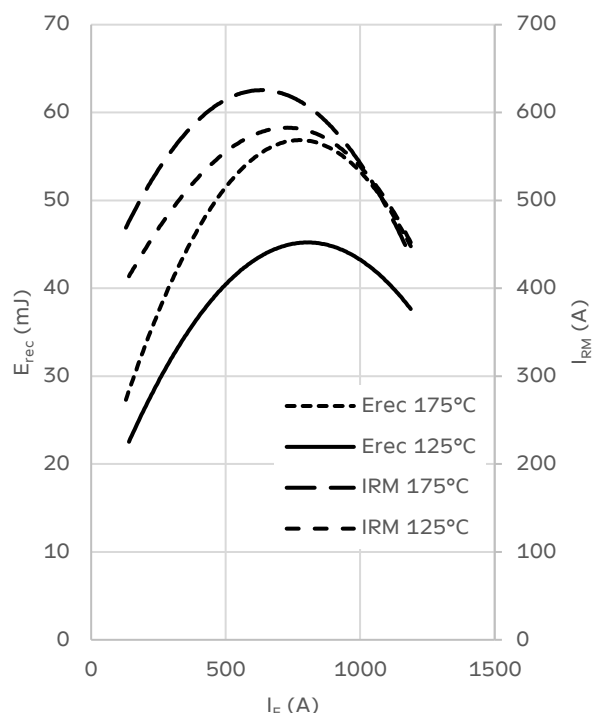
$$I_F = f(V_F)$$



Diode switching characteristics (typical)

$$E_{rec} = f(I_F), I_{RM} = f(I_F)$$

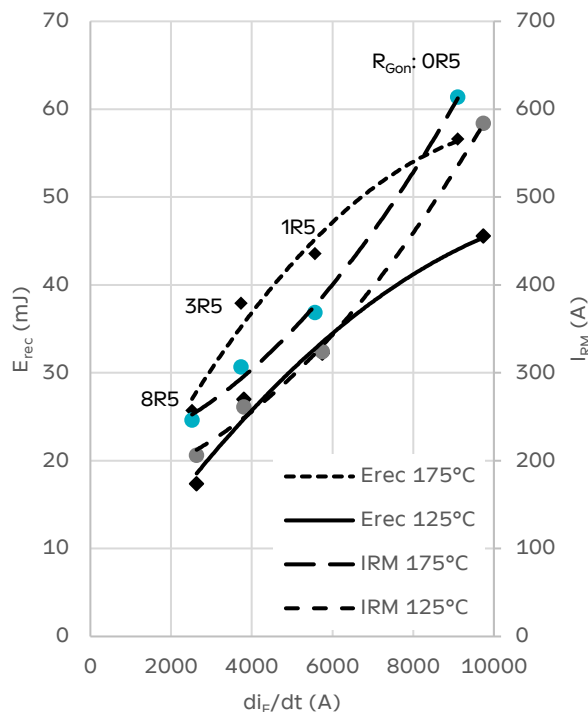
$$V_{DC} = 600 \text{ V}, R_{Gon} = 0.47 \Omega \text{ (IGBT)}, V_{GE} = -15/+15 \text{ V (IGBT)}$$



Diode switching characteristics (typical)

$$E_{rec} = f(di/dt), I_{RM} = f(di/dt)$$

$$V_{DC} = 600 \text{ V}, I_F = 750 \text{ A}, V_{GE} = -15/+15 \text{ V (IGBT)}$$



Thermal impedance (typical)

$$Z_{th(j-c)} = f(t)$$

