Features

- CRM(CQ) Super_Junction technology
- Much lower Ron*A performance for On-state efficiency
- Better efficiency due to very low FOM
- Qualified for industrial grade applications according to JEDEC

Product Summary

V_{DS}	650V
$R_{DS(on),typ}$	112mΩ
I_D	21A

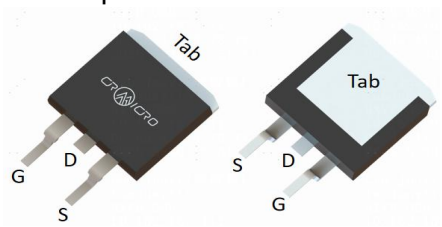
Applications

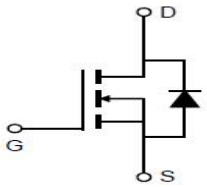
- LED/LCD/PDP TV and monitor Lighting
- Solar/Renewable/UPS -Micro Inverter System
- Charger
- Power Supply

100% DVDS Tested
100% Avalanche Tested

RoHS HF

Top view Bottom view





Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRJS140N65G4Z	CRJS140N65G4Z	TO-263	Tape&Reel	N/A	N/A	1000pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Continuous drain current [1)] T_C = 25℃ T_C = 100℃	I_D	 21 13	A
Pulsed drain current [2)] (T_C = 25℃, t_p limited by $T_{j,max}$)	$I_{D,pulse}$	51	A
Avalanche energy, single pulse (L=30mH)	E_{AS}	135	mJ
MOSFET dv/dt ruggedness	dv/dt	120	V/ns
Gate-Source voltage	V_{GS}	±30	V
Power dissipation (T_C = 25℃)	P_{tot}	140	W
Continuous diode forward current(T_C = 25℃)	I_S	21	A
Diode pulse current [2)] (T_C = 25℃)	$I_{S,pulse}$	51	A
Recovery diode dv/dt [3)]	dv/dt	70	V/ns
Operating junction and storage temperature	T_j , T_{stg}	-55...+150	℃

1) Limited by $T_{j,max}$. Maximum Duty Cycle D = 0.50
2) Pulse width t_p limited by $T_{j,max}$
3) Identical low side and high side switch with identical R_g

Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case	R_{thJC}	-	0.64	0.89	℃/W	
Thermal resistance, junction – ambient	R_{thJA}	-	-	97	℃/W	

Electrical Characteristic (at T_j = 25℃, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	V_{GS}=0V,I_D=1mA
Gate threshold voltage	$V_{GS(th)}$	3	-	5	V	V_{DS}=V_{GS},I_D=250μA
Zero gate voltage drain current	I_{DSS}	-	-	1	μA	V_{DS}=650V,V_{GS}=0V
Gate-source leakage current	I_{GSS}	-	-	±100	nA	V_{GS}=±30V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	- -	112 260	140 -	mΩ	V_{GS}=10V,I_D=7.8A, T_j=25℃ T_j=150℃
Transconductance	g_{fs}	-	18.5	-	S	V_{DS}=20V,I_D=7.8A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	1540	-	pF	V_{GS}=0V, V_{DS}=400V, f=250KHz
Output Capacitance	C_{oss}	-	29	-		
Reverse Transfer Capacitance	C_{rss}	-	2	-		
Gate Total Charge	Q_g	-	34	-	nC	V_{GS}=10V, V_{DS}=400V, I_D=7.8A
Gate-Source charge	Q_{gs}	-	12	-		
Gate-Drain charge	Q_{gd}	-	13	-		
Gate plateau voltage	$V_{plateau}$	-	6.5	-	V	
Turn-on delay time	$t_{d(on)}$	-	18	-	ns	V_{GS}=13V, I_D=7.8A, V_{DS}=400V, R_g=10.2Ω
Rise time	t_r	-	22	-		
Turn-off delay time	$t_{d(off)}$	-	79	-		
Fall time	t_f	-	26	-		
Gate resistance	$R_{g,int}$	-	6.6	-	Ω	f=1MHz

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	0.7	0.85	1	V	V_{GS}=0V,I_{SD}=7.8A
Body Diode Reverse Recovery Time	t_{rr}	-	280	-	ns	I_{SD}=7.8A di_F/dt=100A/μs V_{DS}=400V
Body Diode Reverse Recovery Charge	Q_{rr}	-	3.1	-	μC	
Body Diode Reverse Recovery Peak Current	I_{rrm}	-	24.5	-	A	

Typical Performance Characteristics

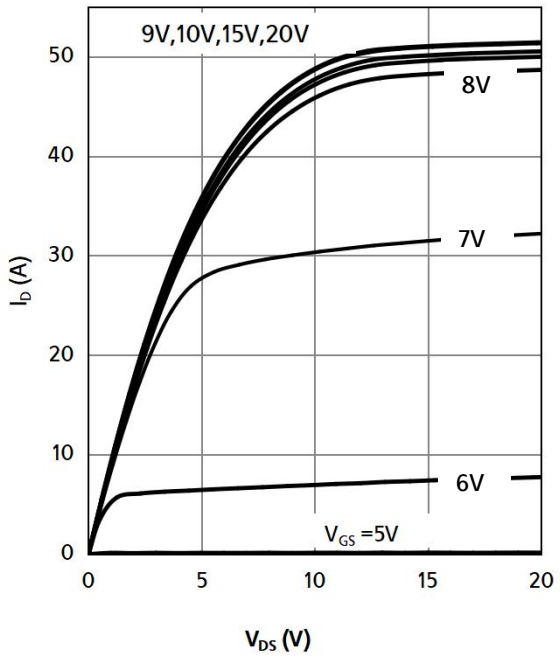
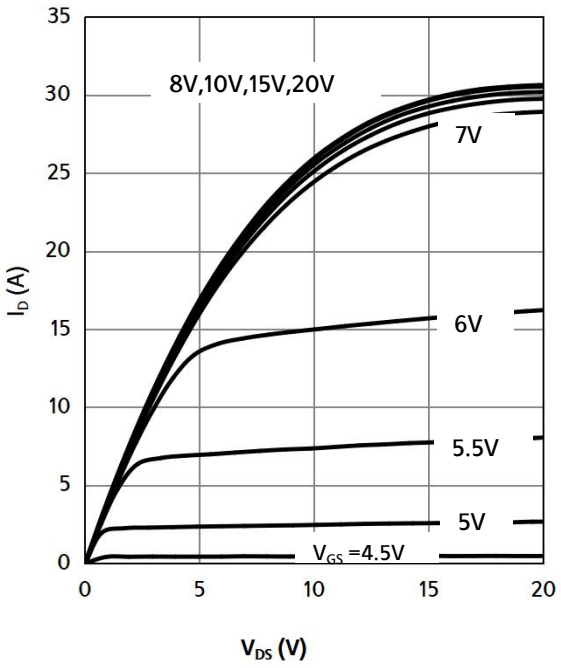
Fig 1. Output Characteristics (T_j=25℃)	Fig 2. Output Characteristics (T_j=150℃)
	
I_D=f(V_{DS});T_j=25℃;parameter: V_{GS}	I_D=f(V_{DS});T_j=150℃;parameter: V_{GS}

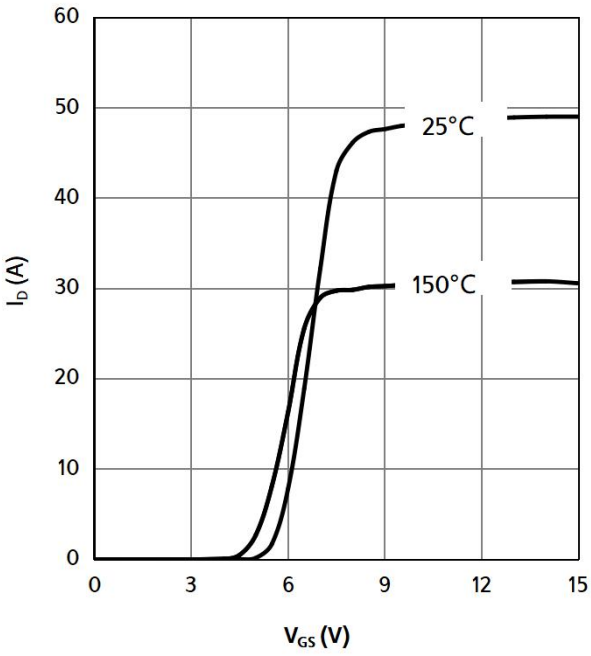
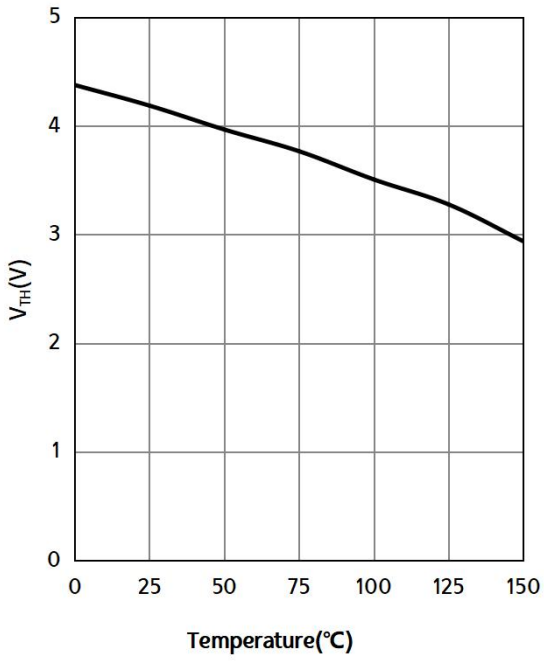
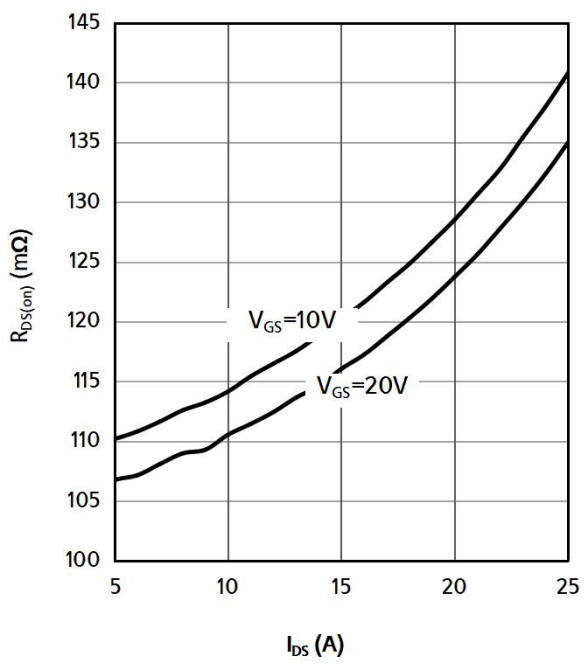
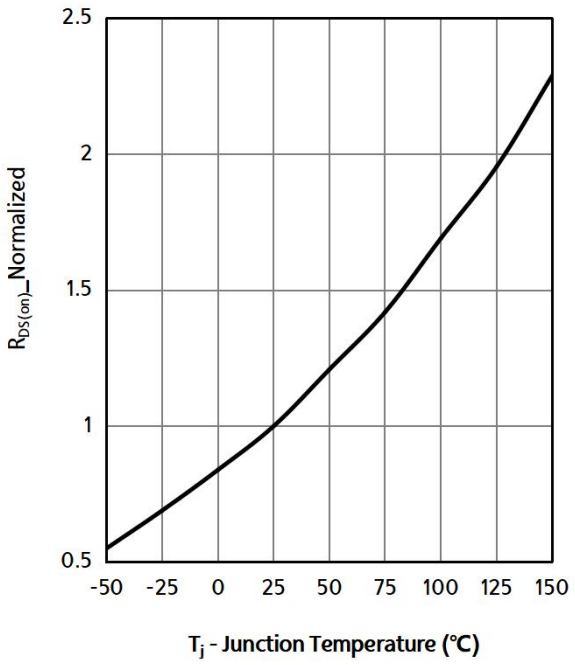
Fig 3: Transfer Characteristics	Fig 4: V_{TH} vs. T_j Temperature Characteristics
	
I_D=f(V_{GS});V_{DS}=20V;parameter: T_j	V_{TH}=f(T_j);I_D=250μA

Fig 5: $R_{DS(on)}$ vs. I_{DS} Characteristics(T_j=25℃)



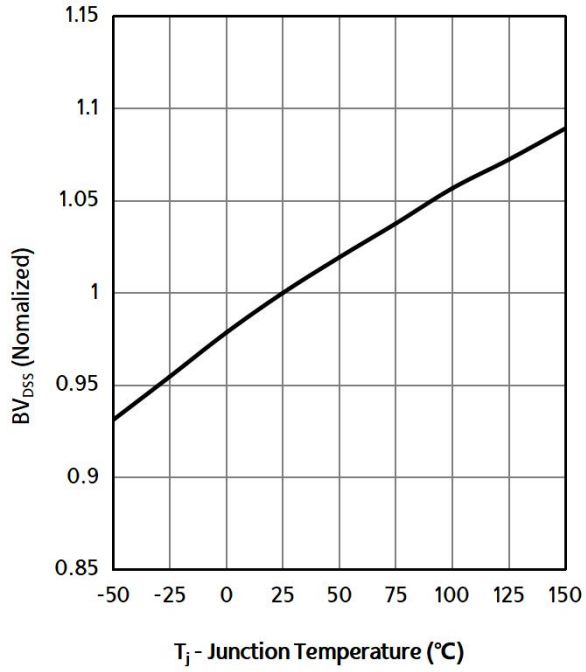
$R_{DS(on)}$=f(I_D); T_j=25℃;parameter: V_{GS}

Fig 6: $R_{DS(on)}$ vs. Temperature



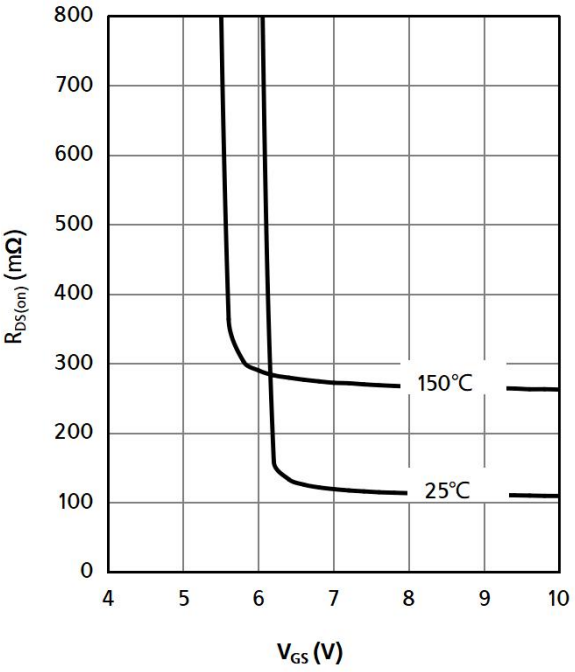
$R_{DS(on)}$=f(T_j); I_D=7.8A; V_{GS}=10V

Fig 7: BV_{DSS} vs. Temperature



BV_{DSS}=f(T_j)

Fig 8: $R_{DS(on)}$ vs. Gate Voltage



$R_{DS(on)}$=f(V_{GS}); I_D=7.8A;parameter: T_j

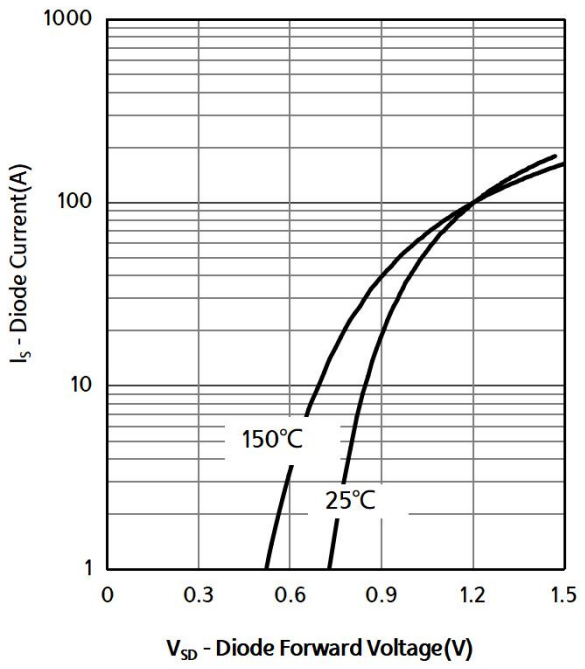
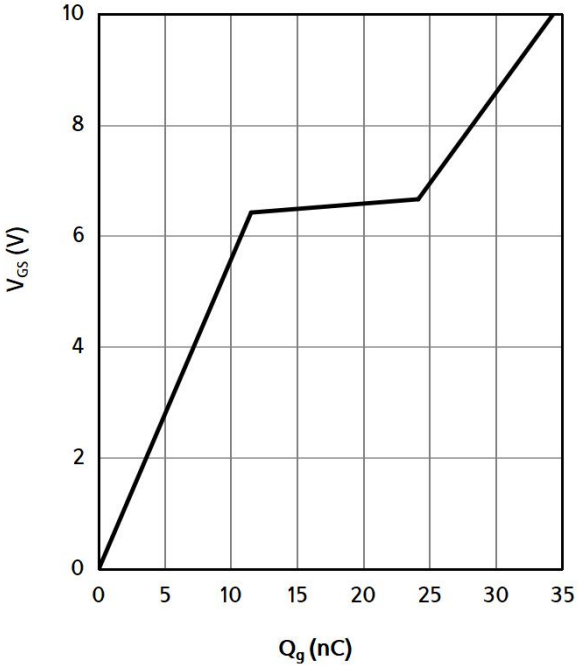
Fig 9: Body-diode Forward Characteristics	Fig 10: Gate Charge Characteristics
	
I_F=f(V_{SD});parameter: T_j	V_{GS}=f(Q_g); V_{DD}=400V; I_D=7.8A

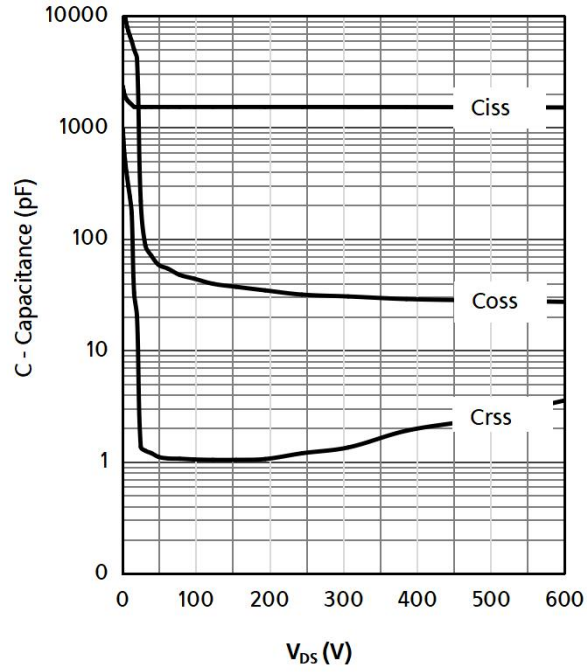
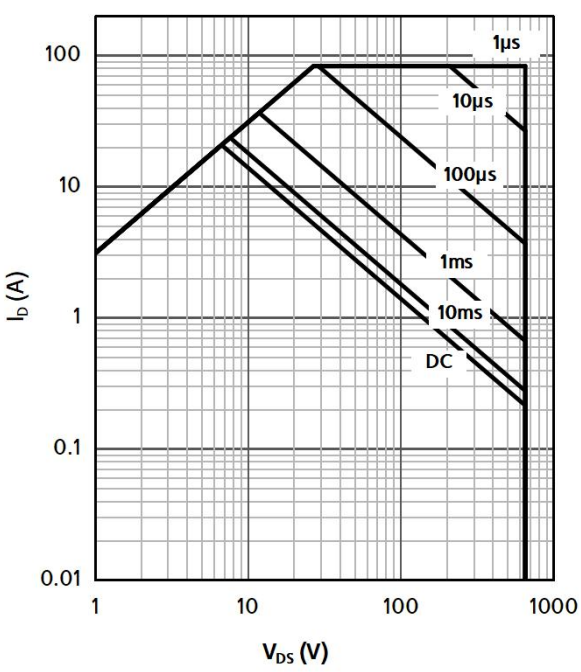
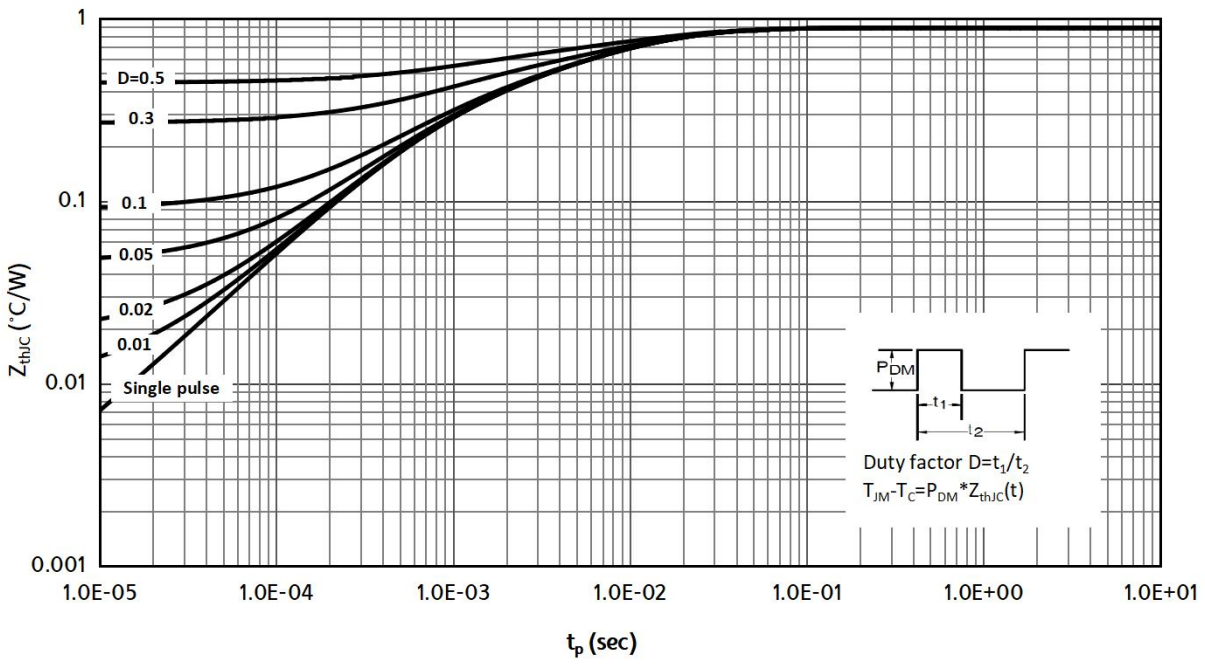
Fig 11: Capacitance Characteristics	Fig 12: Safe Operating Area
	
C=f(V_{DS}); V_{GS}=0V; f=250kHZ	I_D=f(V_{DS}); T_C=25°C;D=0;parameter: t_p

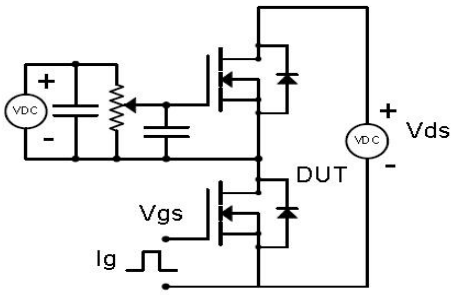
Fig 13: Max. Transient Thermal Impedance

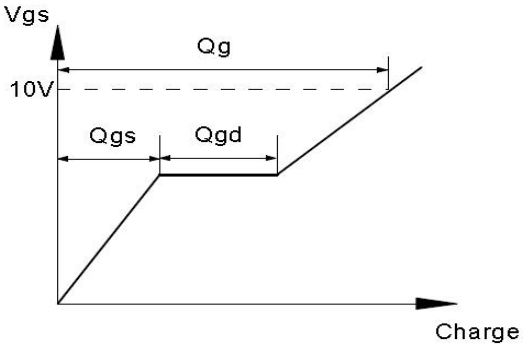


Z_{thJC}=f(t_p);parameter:D=t_1/t_2

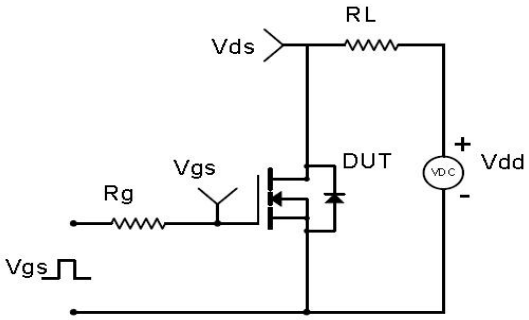
Test Circuit & Waveform

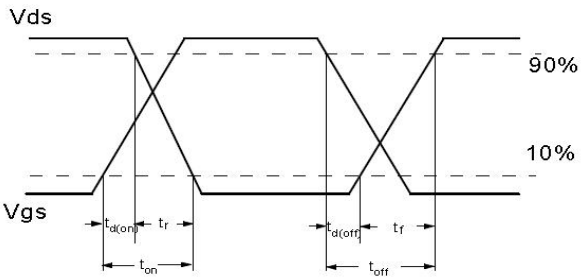
Gate Charge Test Circuit & Waveform



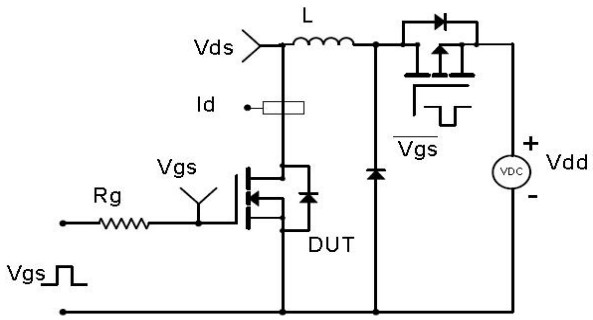


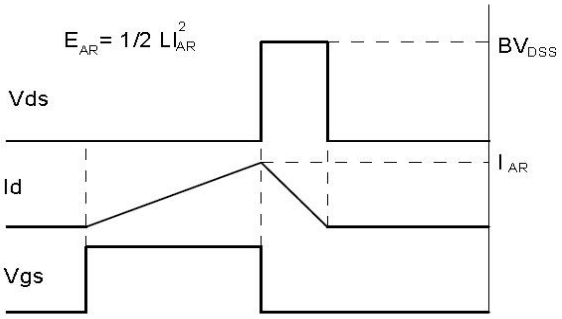
Resistive Switching Test Circuit & Waveforms



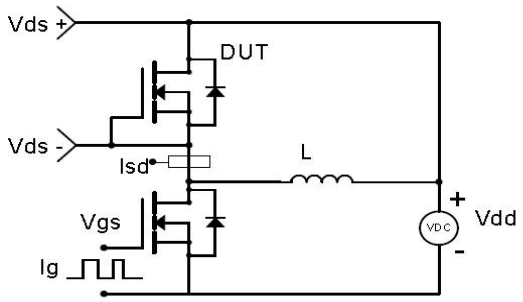


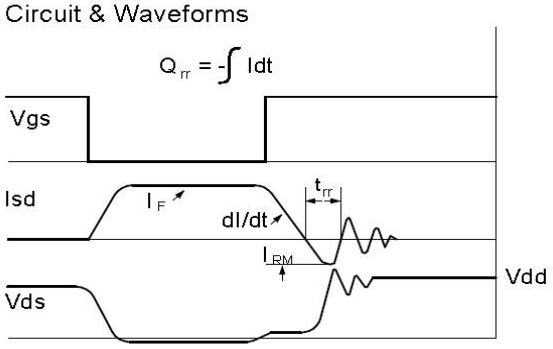
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



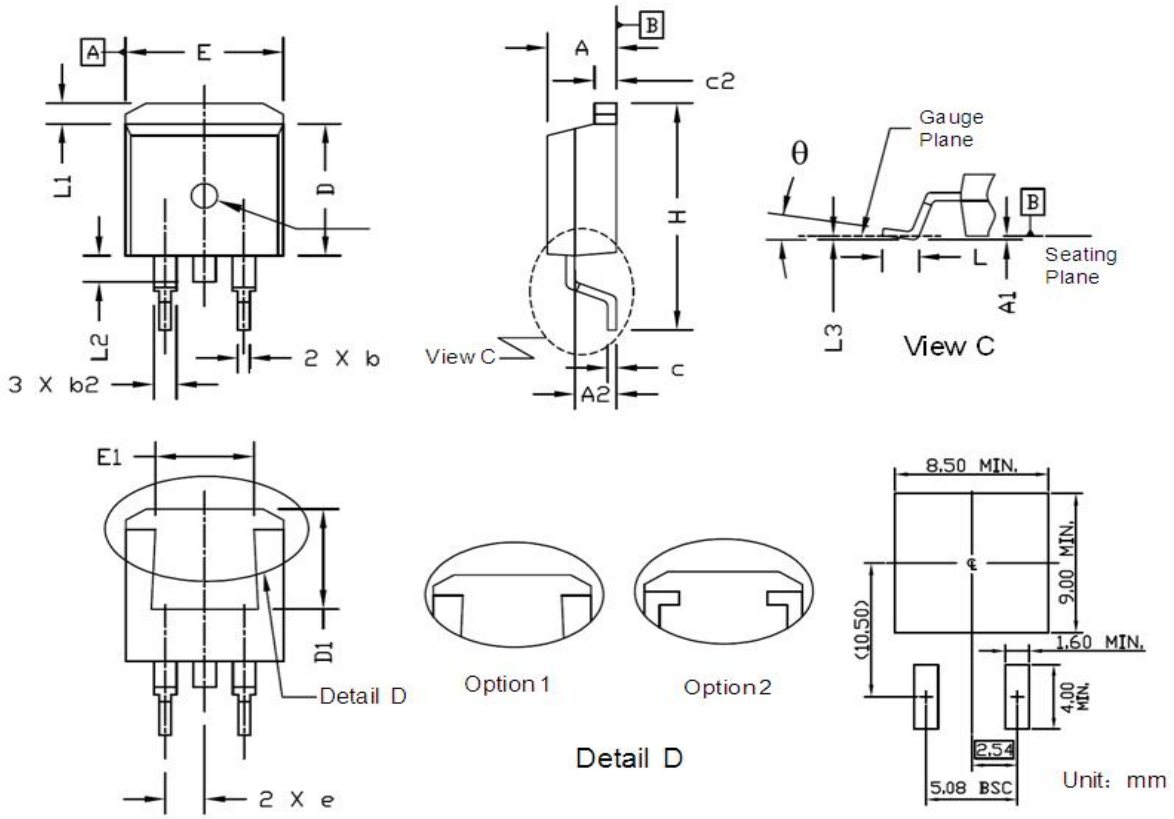


Diode Recovery Test Circuit & Waveforms





Package Outline: TO-263



Symbol		Dimensions In Millimeters		Dimensions In Inches	
		Min.	Max.	Min.	Max.
A		4.30	4.86	0.169	0.191
A1		0.00	0.25	0.000	0.010
A2		2.20	2.90	0.087	0.114
b		0.68	0.94	0.027	0.037
b2		1.14	1.78	0.045	0.070
c		0.33	0.65	0.013	0.026
c2		1.17	1.40	0.046	0.055
D		8.38	9.45	0.330	0.372
D1		6.90	8.17	0.272	0.322
e		2.54 BSC.		0.100 BSC.	
E		9.78	10.50	0.385	0.413
E1		6.50	8.60	0.256	0.339
H		14.61	15.88	0.575	0.625
L		1.78	2.79	0.070	0.110
L1		0.70	1.60	0.028	0.063
L2		1.00	1.78	0.039	0.070
L3		0.25 BSC.		0.010 BSC.	
θ	Option A	-8°	0°	-8°	0°
	Option B	0°	8°	0°	8°

Marking



NOTE:
NXBBAAAAY

N	—WB code (Usually omitted)
X	—Assembly location code
BB	—Fab code
AAAA	—Lot code
Y	—Bin code (Usually omitted)

Revision History

Revison	Major changes
1.0	First version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.