JJMICROELECTRONICS

-100V,-46A, 18mΩ P-channel Power SGT MOSFET

JMPL1025PK

Features

- Excellent $R_{DS(ON)}$ and Low Gate Charge
- 100% UIS Tested
- 100% ΔVds Tested
- Halogen-free; RoHS-compliant

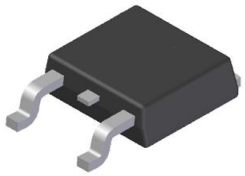
Applications

- Load Switch
- PWM Application
- Power Management

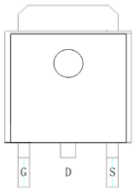
Product Summary

Parameters	Value	Unit
V_{DSS}	-100	V
$V_{GS(th)_Typ}$	-1.8	V
I_D(@V_{GS}=-10V)	-46	A
$R_{DS(ON)_Typ}$(@V_{GS}=-10V)	18	mΩ
$R_{DS(ON)_Typ}$(@V_{GS}=-4.5V)	24	mΩ

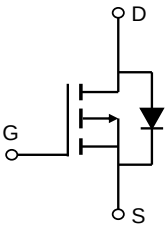
RoHS



TO-252-3L Top View



Pin Assignment



Schematic Diagram

Ordering Information

Device	Marking	MSL	Form	Package	Reel(pcs)	Per Carton (pcs)
JMPL1025PK-13	PL1025P	3	Tape&Reel	TO-252-3L	2500	25000

Absolute Maximum Ratings (@ T_C = 25°C unless otherwise specified)

Symbol	Parameter		Value	Unit
V_{DS}	Drain-to-Source Voltage		-100	V
V_{GS}	Gate-to-Source Voltage		±20	V
I_D	Continuous Drain Current	T_C = 25°C	-46	A
		T_C = 100°C	-29	
I_{DM}	Pulsed Drain Current (1)		Refer to Fig.4	A
E_{AS}	Single Pulsed Avalanche Energy (2)		423	mJ
P_D	Power Dissipation	T_C = 25°C	100	W
		T_C = 100°C	40	
T_J, T_{STG}	Junction & Storage Temperature Range		-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient(3)	41	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.3	

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Electrical Characteristics (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	I_D = -250μA, V_{GS} = 0V	-100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = -80V, V_{GS} = 0V	-	-	-1.0	μA
I_{GSS}	Gate-Body Leakage Current	V_{DS} = 0V, V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = -250μA	-1.3	-1.8	-2.4	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance(4)	V_{GS} = -10V, I_D = -20A	-	18	24	mΩ
		V_{GS} = -4.5V, I_D = -15A	-	24	31	mΩ
Dynamic Characteristics						
R_g	Gate Resistance	f = 1MHz	-	3.4	-	Ω
C_{iss}	Input Capacitance	V_{GS} = 0V, V_{DS} = -50V, f = 1MHz	1838	3063	4595	pF
C_{oss}	Output Capacitance		327	545	818	pF
C_{rss}	Reverse Transfer Capacitance		20	33	66	pF
Q_g	Total Gate Charge	V_{GS} = 0 to -10V, V_{DS} = -50V, I_D = -20A	-	52	-	nC
Q_{gs}	Gate Source Charge		-	11	-	nC
Q_{gd}	Gate Drain("Miller") Charge		-	10	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On DelayTime	V_{GS} = -10V, V_{DD} = -50V, I_D= -20A, R_{GEN} = 3Ω	-	10	-	ns
t_r	Turn-On Rise Time		-	25	-	ns
$t_{d(off)}$	Turn-Off DelayTime		-	51	-	ns
t_f	Turn-Off Fall Time		-	16	-	ns
Body Diode Characteristics						
I_S	Maximum Continuous Body Diode Forward Current		-	-	-46	A
I_{SM}	Maximum Pulsed Body Diode Forward Current		-	-	-186	A
V_{SD}	Body Diode Forward Voltage	V_{GS} = 0V, I_S = -20A	-		-1.2	V
trr	Body Diode Reverse Recovery Time	I_F = -20A, di/dt = 100A/us	39	65	98	ns
Qrr	Body Diode Reverse Recovery Charge		-	194	-	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
2. E_{AS} condition: Starting T_J=25C, V_{DD}=-50V, V_{GS}=-10V, R_G=25ohm, L=3mH, I_{AS}=-16.8A, V_{DD}=0V during time in avalanche.
3. $R_{\theta JA}$ is measured with the device mounted on a 1inch^2 pad of 2oz copper FR4 PCB.
4. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%.

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Typical Performance Characteristics

Figure 1: Power De-rating

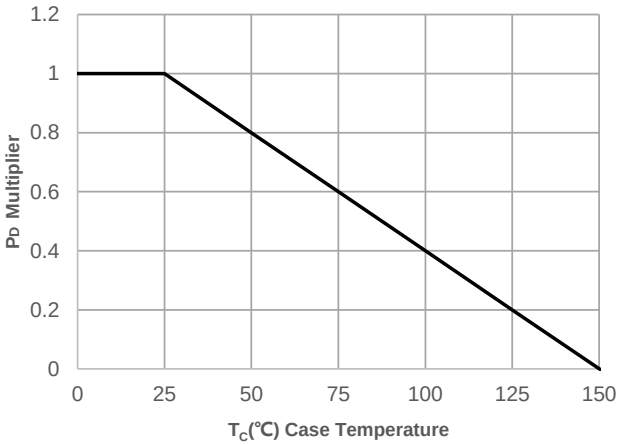


Figure 2: Current De-rating

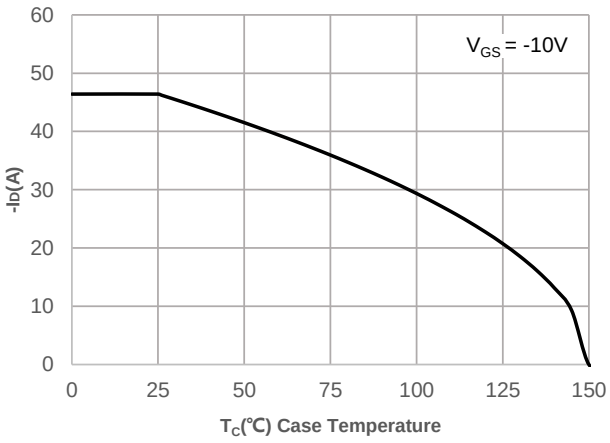


Figure 3: Normalized Maximum Transient Thermal Impedance

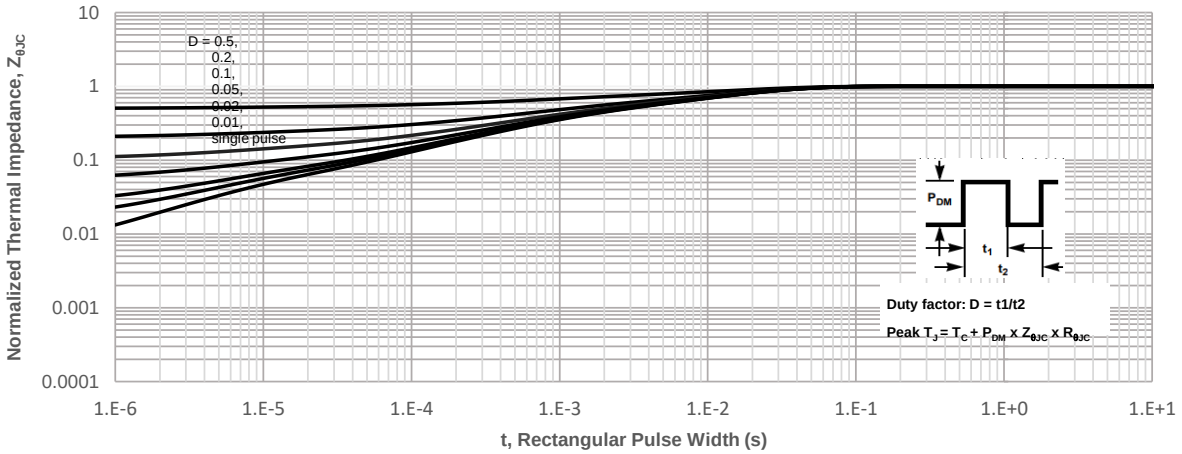
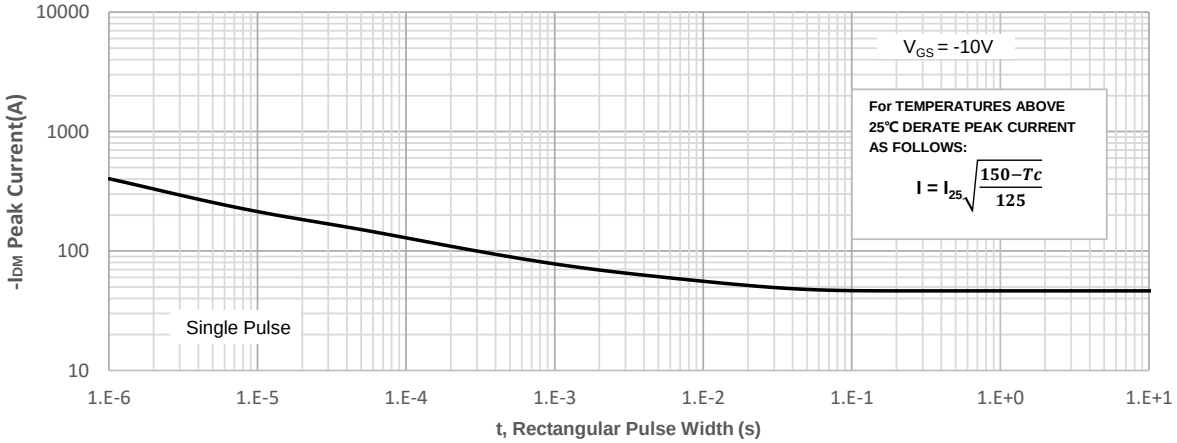


Figure 4: Peak Current Capacity



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Typical Performance Characteristics

Figure 5: Output Characteristics

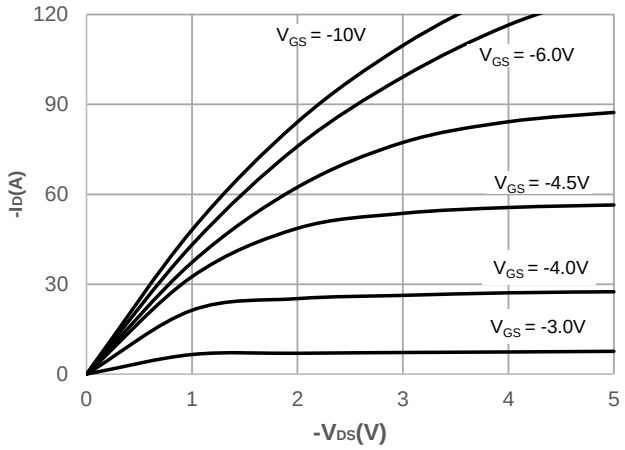


Figure 6: Typical Transfer Characteristics

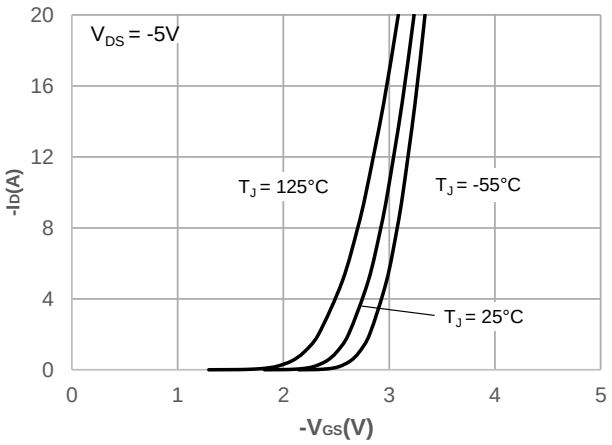


Figure 7: On-resistance vs. Drain Current

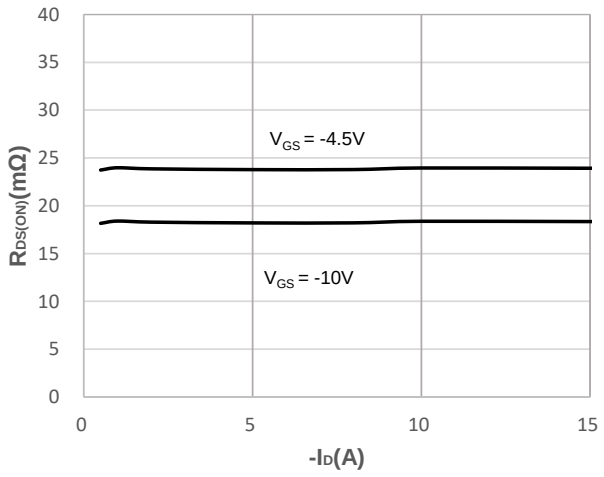


Figure 8: Body Diode Characteristics

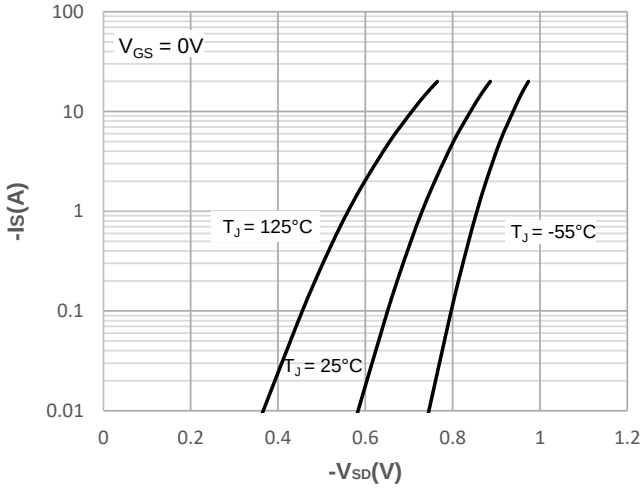


Figure 9: Gate Charge Characteristics

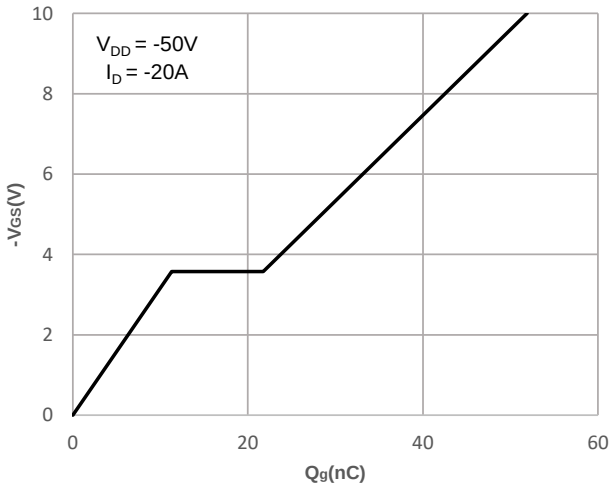
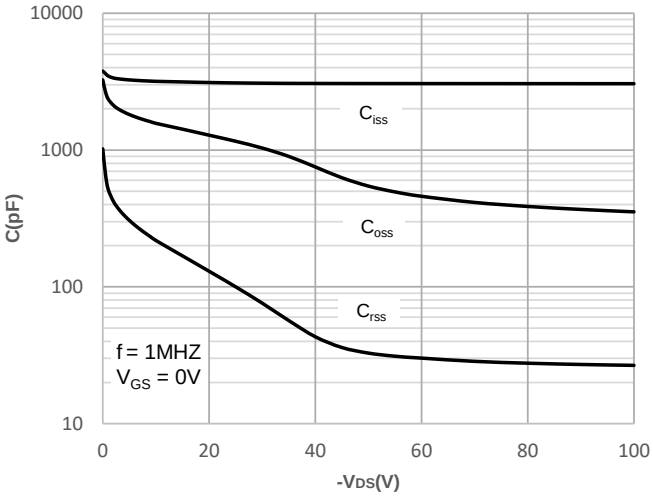


Figure 10: Capacitance Characteristics



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Typical Performance Characteristics

Figure 11: Normalized Breakdown voltage vs. Junction Temperature

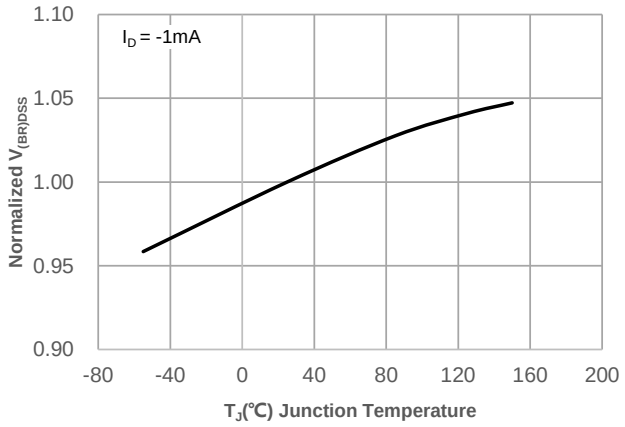


Figure 12: Normalized on Resistance vs. Junction Temperature

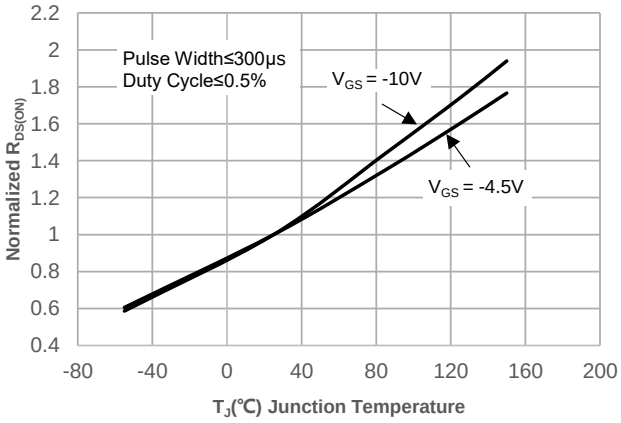


Figure 13: Normalized Threshold Voltage vs. Junction Temperature

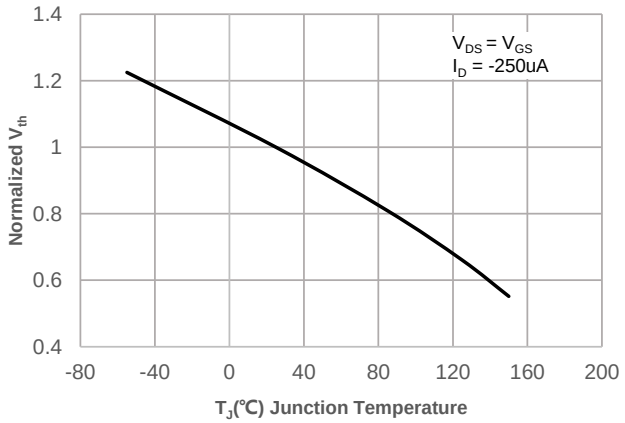


Figure 14: $R_{DS(ON)}$ vs. V_{GS}

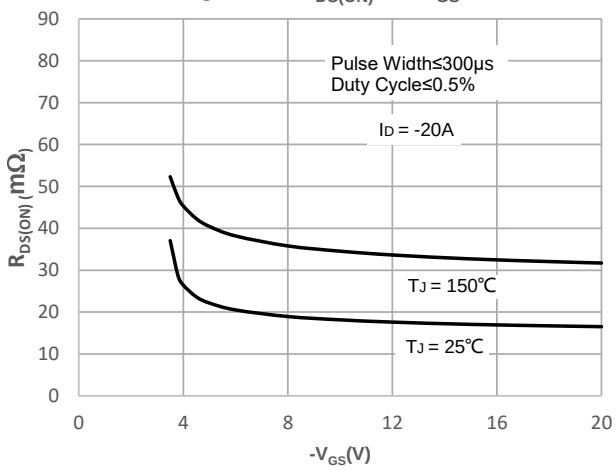
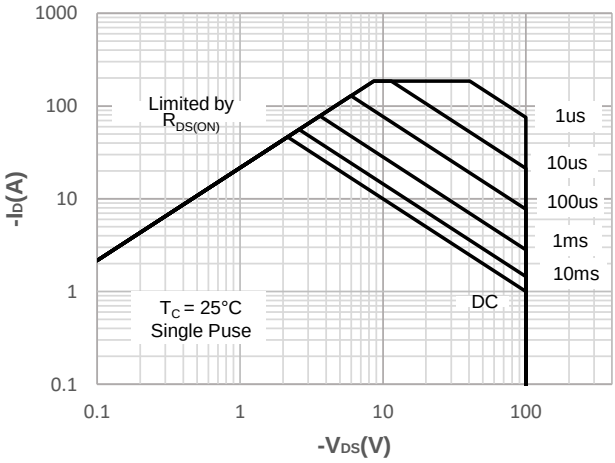


Figure 15: Maximum Safe Operating Area



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Test Circuit

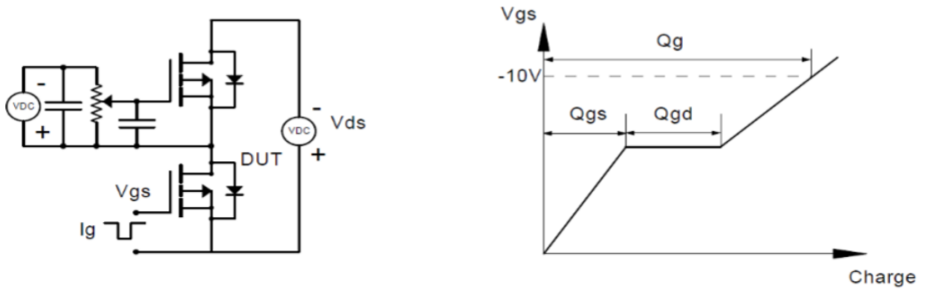


Figure 1: Gate Charge Test Circuit & Waveform

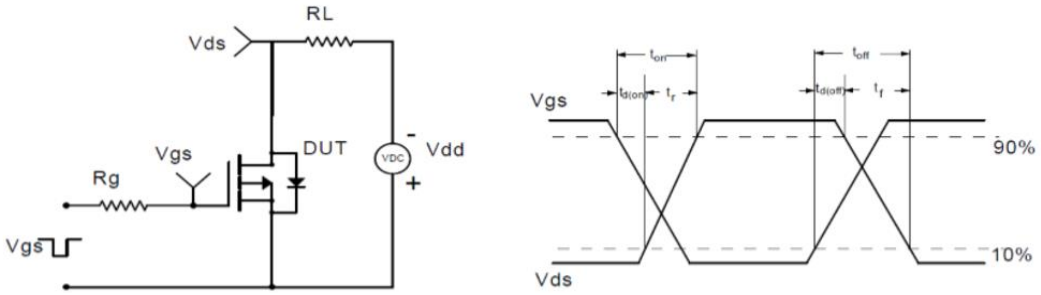


Figure 2: Resistive Switching Test Circuit & Waveform

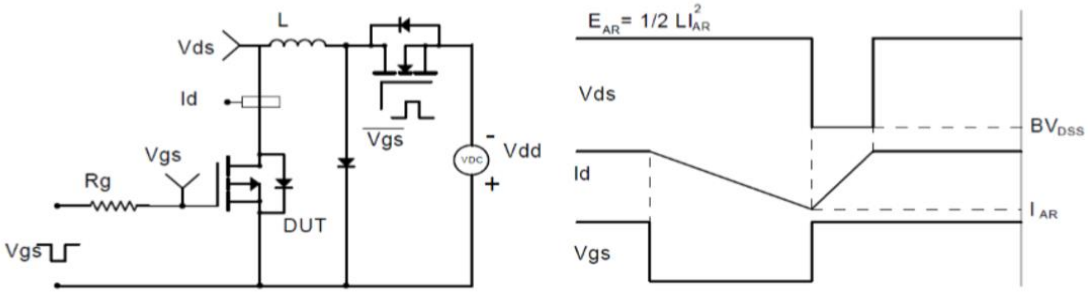


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

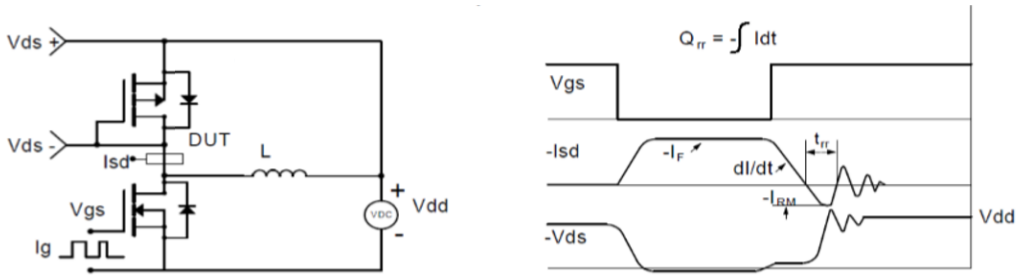
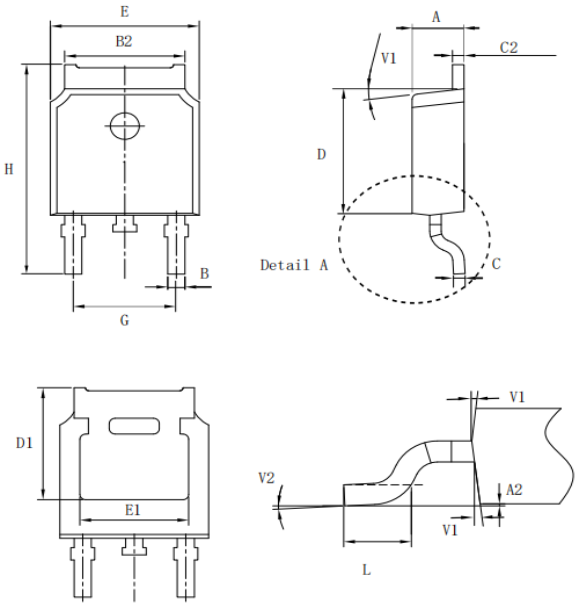


Figure 4: Diode Recovery Test Circuit & Waveform

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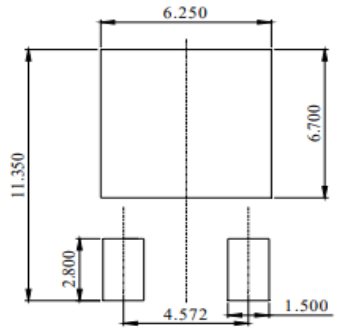
Package Mechanical Data(TO-252-3L)

Package Outline



DIM.	MILLIMETER		
	MIN.	TYP.	MAX.
A	2.275	2.3	2.325
A2	0	0.05	0.1
B	0.72	0.76	0.85
B2	5.234	5.334	5.434
C	-	0.508	-
C2	0.507	0.508	0.517
D	6.075	6.1	6.125
D1	-	5.399	-
E	6.575	6.6	6.625
E1	-	4.826	-
G	4.472	4.572	4.672
H	9.9	10.1	10.3
L	1.4	1.5	1.6
V1	6°	7°	8°
V2	1°	3°	5°

Recommended Soldering Footprint



DIMENSIONS:MILLIMETERS

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