N-Channel Super Junction Power MOSFET Ⅳ

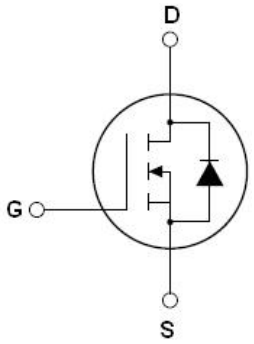
General Description

The series of devices use advanced trench gate super junction technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. This super junction MOSFET fits the industry's AC-DC SMPS requirements for PFC, AC/DC power conversion, and industrial power applications.

$V_{DS\ min@Tjmax}$	985	V
$R_{DS(ON)TYP}$	300	mΩ
ID	16	A
Qg	53	nC

Features

- Optimized body diode reverse recovery performance
- Low on-resistance and low conduction losses
- Small package
- Ultra Low Gate Charge cause lower driving requirements
- 100% Avalanche Tested
- ROHS compliant



Schematic diagram

Application

- Power factor correction（PFC）
- Switched mode power supplies(SMPS)
- Uninterruptible Power Supply（UPS）

Package Marking And Ordering Information

Device	Device Package	Marking
NCE90N340D	TO-263-2L	NCE90N340D



TO-263-2L

Table 1. Absolute Maximum Ratings (T_J=25℃)

Parameter	Symbol	Value	Unit
Drain-Source Voltage (VGS=0V）	V_{DS}	900	V
Gate-Source Voltage (VDS=0V) AC (f>1 Hz)	V_{GS}	±30	V
Gate-Source Voltage (VDS=0V) DC	V_{GS}	±20	V
Continuous Drain Current at Tc=25°C	$I_{D\ (DC)}$	16	A
Continuous Drain Current at Tc=100°C	$I_{D\ (DC)}$	11.2	A
Pulsed drain current **(Note 1)**	$I_{DM\ (pluse)}$	32	A
Maximum Power Dissipation(Tc=25°C)	P_D	234	W
Derate above 25°C		1.56	W/°C
Avalanche current**(Note 2)**	I_{AS}	2.0	A
Drain Source voltage slope, V_{DS} ≤660 V,	dv/dt	50	V/ns
Reverse diode dv/dt， V_{DS} ≤660 V,I_{SD}<I_D	dv/dt	15	V/ns
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55...+175	°C

Table 2. Thermal Characteristic

Parameter	Symbol	Value	Unit
Thermal Resistance，Junction-to-Case（Maximum）	R_{thJC}	0.64	°C /W
Thermal Resistance，Junction-to-Ambient （Maximum）	R_{thJA}	62	°C /W

Table 3. Electrical Characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	900			V
Zero Gate Voltage Drain Current(T_J=25℃)	I_{DSS}	V_{DS}=900V,V_{GS}=0V			1	μA
Zero Gate Voltage Drain Current(T_J=125℃)	I_{DSS}	V_{DS}=900V,V_{GS}=0V			100	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	3.0	3.5	4.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=5A		300	340	mΩ
Dynamic Characteristics						
Input Capacitance	C_{lss}	V_{DS}=50V,V_{GS}=0V, F=1.0MHz		2110		pF
Output Capacitance	C_{oss}			53		pF
Reverse Transfer Capacitance	C_{rss}			18		pF
Total Gate Charge	Q_g	V_{DS}=660V,I_D=8A, V_{GS}=10V		53		nC
Gate-Source Charge	Q_{gs}			10		nC
Gate-Drain Charge	Q_{gd}			21		nC
Gate plateau voltage	Vgp			5.3		V
Intrinsic gate resistance	R_G	f = 1 MHz D-S short		1.5		Ω
Switching times						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=660V,I_D=8A, R_G=20Ω,V_{GS}=10V		16		nS
Turn-on Rise Time	t_r			26		nS
Turn-Off Delay Time	$t_{d(off)}$			60		nS
Turn-Off Fall Time	t_f			23		nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I_{SD}	T_C=25°C			16	A
Pulsed Source-drain current(Body Diode)	I_{SDM}				32	A
Forward On Voltage	V_{SD}	Tj=25°C,I_{SD}=16A,V_{GS}=0V		0.9	1.2	V
Reverse Recovery Time	t_{rr}	Tj=25°C,I_F=8.0A, di/dt=100A/μs		240		nS
Reverse Recovery Charge	Q_{rr}			1.8		uC
Peak Reverse Recovery Current	I_{rrm}			24		A

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. Tj=25℃,VDD=50V,VG=10V, R_G=25Ω

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (curves)

Figure1. Safe operating area

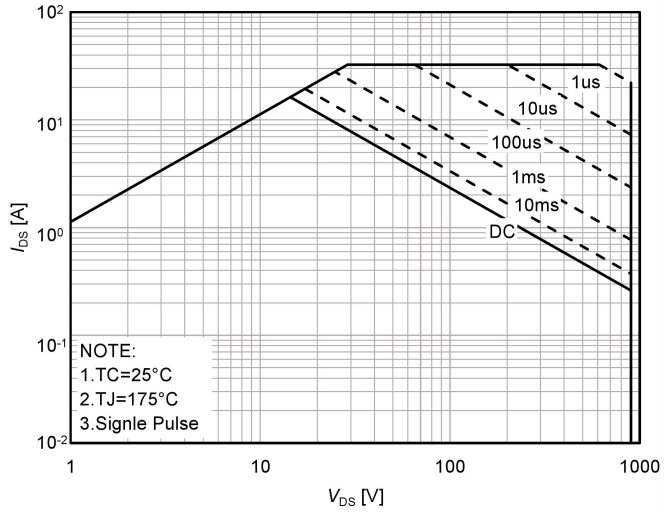


Figure2. Capacitance

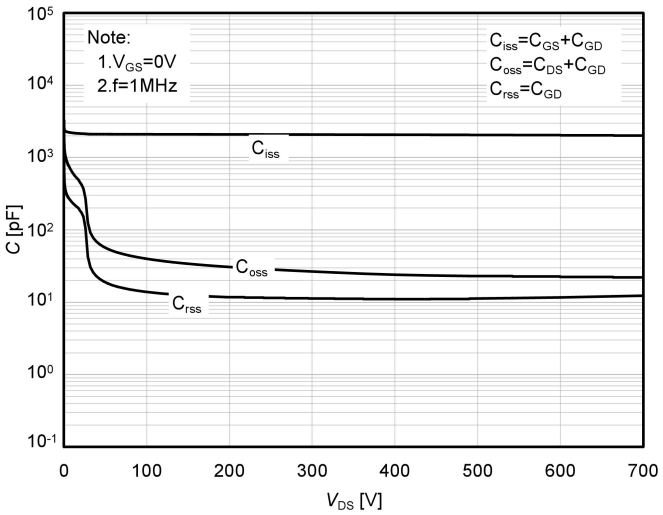


Figure3. Body Diode Forward Voltage

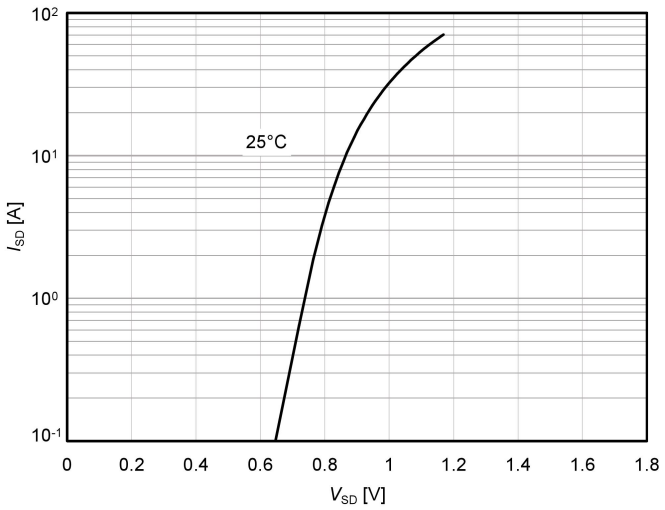


Figure4. Output characteristics

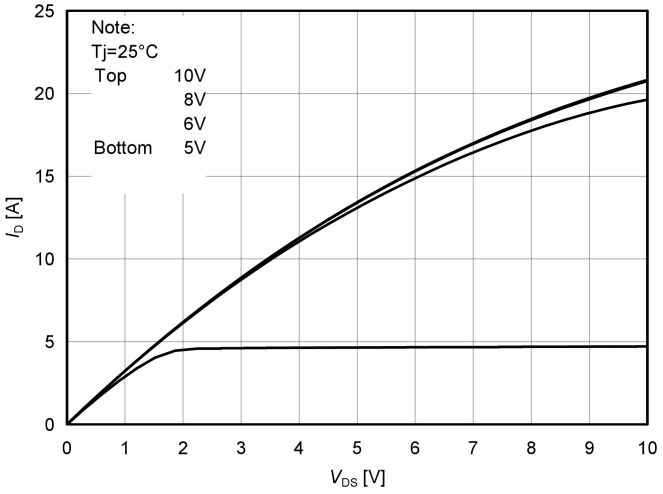


Figure5. $R_{DS(ON)}$ vs Junction Temperature

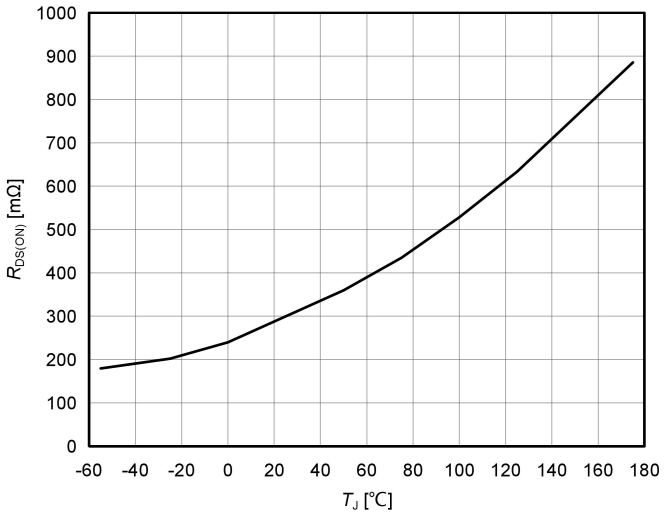


Figure6. $V_{BR(DSS)}$ vs Junction Temperature

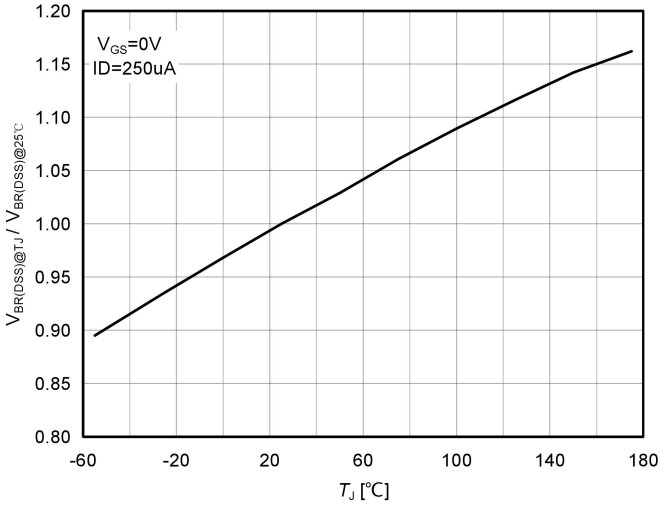


Figure7. Maximum I_D vs Junction Temperature

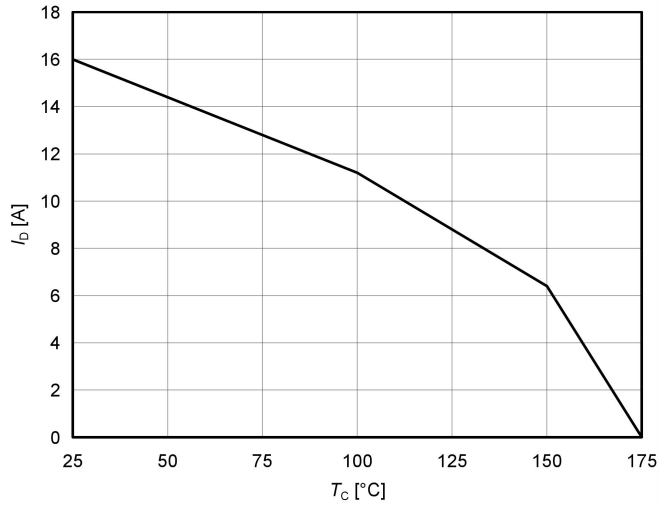


Figure8. Gate charge waveforms

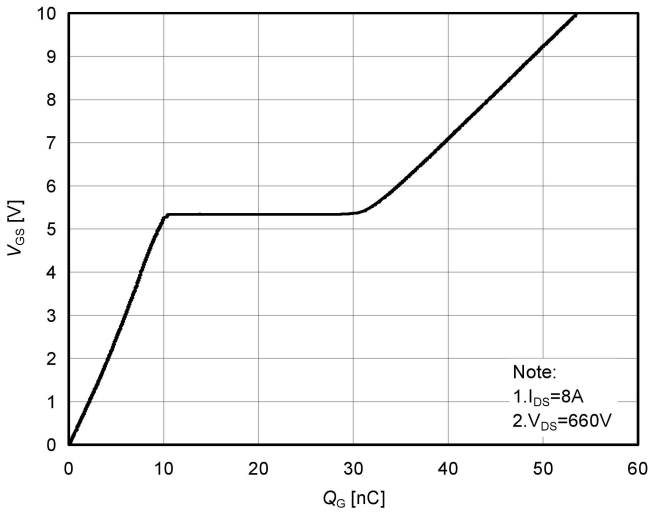


Figure9. Static drain-source on resistance

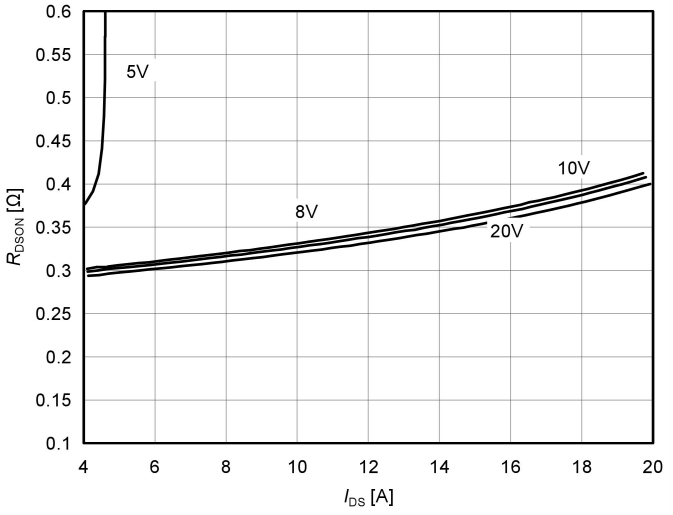


Figure10. Transfer characteristics

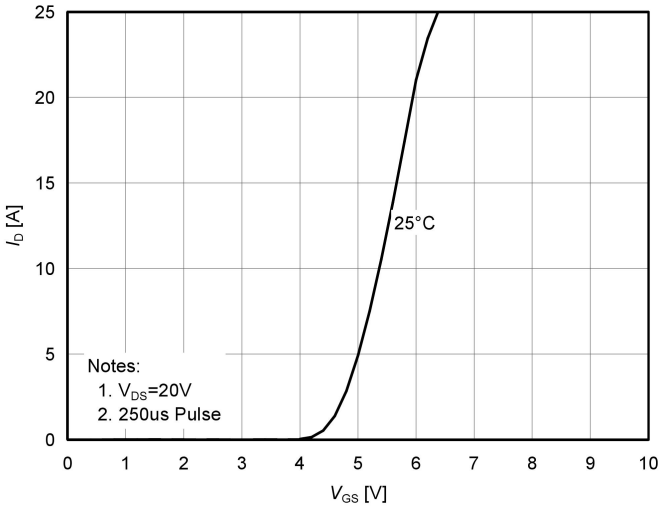
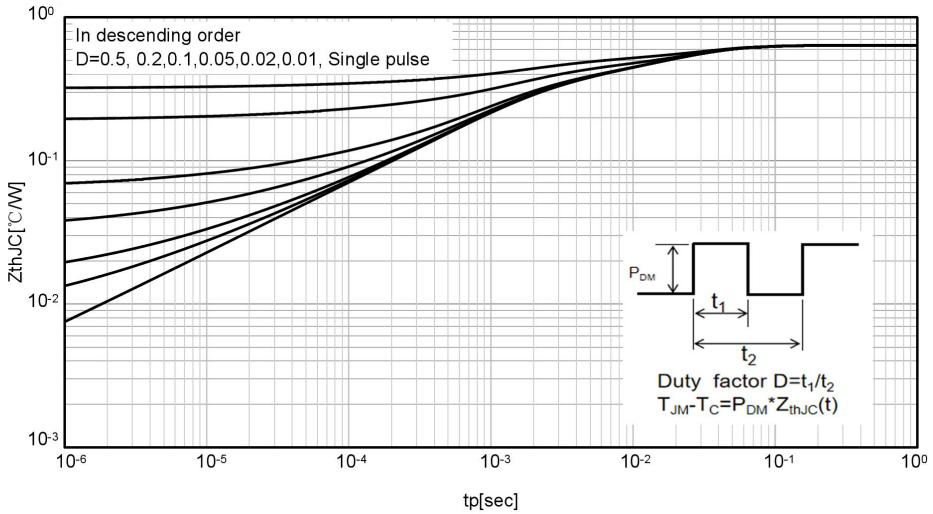
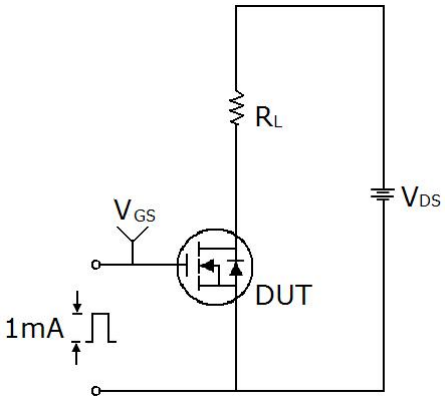


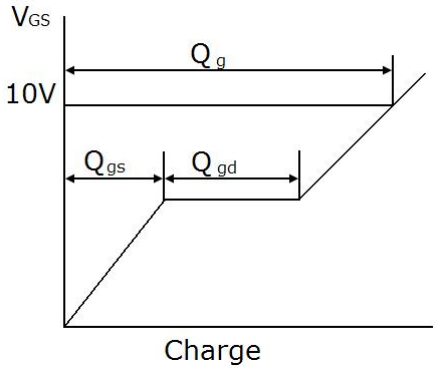
Figure11. Transient thermal impedance



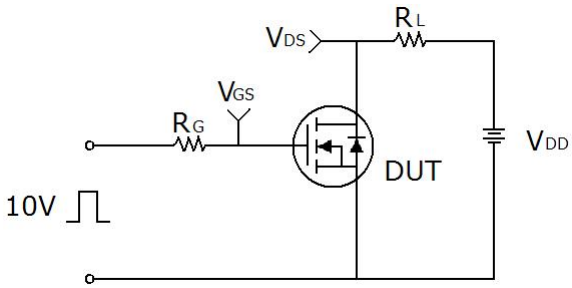
Test circuit

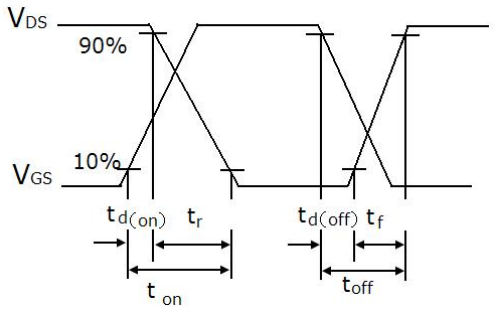
1）Gate charge test circuit & Waveform



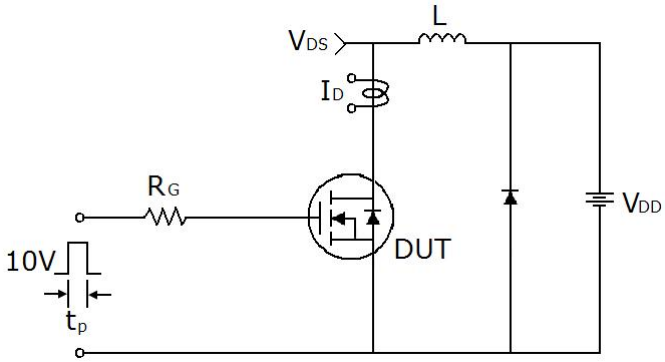


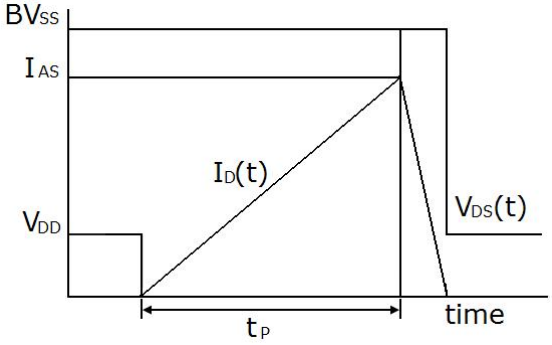
2）Switch Time Test Circuit：



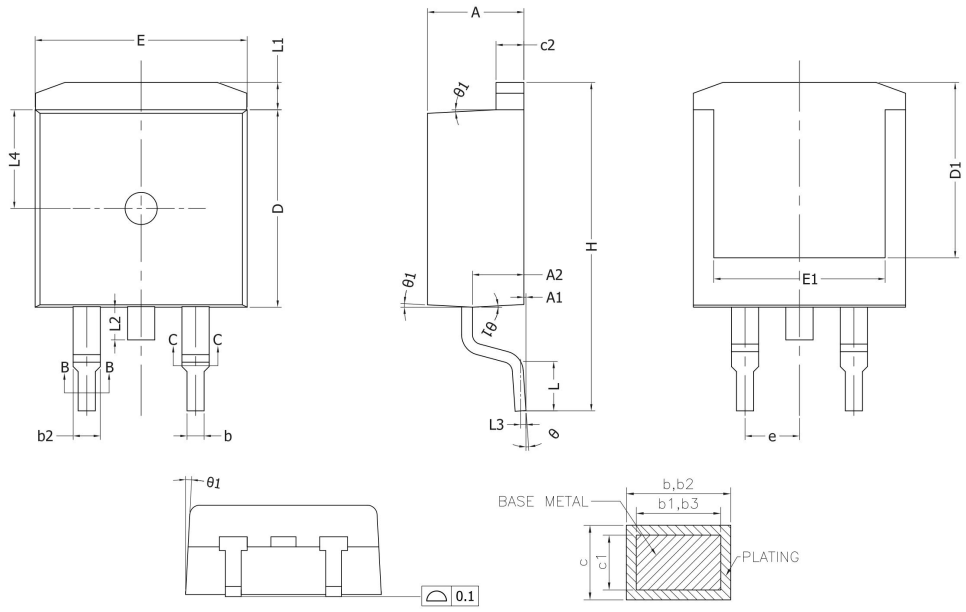


3）Unclamped Inductive Switching Test Circuit & Waveforms





TO-263-2L-P Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
A1	0.00	0.25	0.000	0.010
A2	2.20	2.60	0.087	0.102
b	0.76	0.89	0.030	0.035
b1	0.75	0.85	0.030	0.033
b2	1.23	1.37	0.048	0.054
b3	1.22	1.32	0.048	0.052
c	0.47	0.60	0.019	0.024
c1	0.46	0.56	0.018	0.022
c2	1.25	1.35	0.049	0.053
D	9.10	9.30	0.358	0.366
D1	8.00		0.315	
E	9.80	10.00	0.386	0.394
E1	7.80		0.307	
e	2.54BSC		0.100BSC	
H	14.90	15.70	0.587	0.618
L	2.00	2.60	0.079	0.102
L1	1.17	1.40	0.046	0.055
L2		1.75		0.069
L3	0.25BSC		0.101BSC	
L4	4.60REF		0.181REF	
θ	0°	8°	0°	8°
θ1	1°	5°	1°	5°

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