NCE N-Channel Super Trench III Power MOSFET

Description

The series of devices uses **Super Trench III** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g. This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

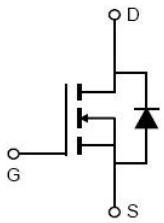
General Features

- V_{DS} =80V,I_D =315A
  $R_{DS(ON)}$=1.65mΩ , typical@ V_{GS}=10V
- Excellent gate charge x $R_{DS(on)}$ product(FOM)
- Very low on-resistance $R_{DS(on)}$
- 175 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!
100% ΔVds TESTED!

TO-247-3L





Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP020NH80T	NCEP020NH80T	TO-247-3L	-	-	-

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	315	A
Drain Current-Continuous(T_C=100℃)	I_D (100℃)	220	A
Pulsed Drain Current	I_{DM}	1260	A
Maximum Power Dissipation	P_D	370	W
Derating factor		2.47	W/℃
Single pulse avalanche energy (Note 1)	E_{AS}	2540	mJ
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 175	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	0.405	℃/W
Thermal Resistance,Junction-to-Ambient (Note 4)	$R_{\theta JA}$	40	℃/W

Electrical Characteristics (T_C=25℃unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V I_D=250μA	80		-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=80V,V_{GS}=0V	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250μA	2.5	3.5	4.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=20A	-	1.65	2.00	mΩ
Forward Transconductance	g_{FS}	V_{DS}=10V,I_D=20A	-	90	-	S
Dynamic Characteristics						
Input Capacitance	C_{lss}	V_{DS}=40V,V_{GS}=0V, F=1.0MHz	-	9256	-	PF
Output Capacitance	C_{oss}		-	4424	-	PF
Reverse Transfer Capacitance	C_{rss}		-	128	-	PF
Switching Characteristics (Note 2)						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}=40V,I_D=20A V_{GS}=10V,R_G=3Ω	-	27.2	-	nS
Turn-on Rise Time	t_r		-	21.3	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	56.6	-	nS
Turn-Off Fall Time	t_f		-	25	-	nS
Total Gate Charge	Q_g	V_{DS}=40V,I_D=20A, V_{GS}=10V	-	136	-	nC
Gate-Source Charge	Q_{gs}		-	37	-	nC
Gate-Drain Charge	Q_{gd}		-	30	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	V_{GS}=0V,I_S=20A	-	-	1.2	V
Diode Forward Current	I_S		-	-	315	A
Reverse Recovery Time	t_{rr}	T_J = 25°C, I_F = 20A di/dt = 100A/μs	-	115.2	-	nS
Reverse Recovery Charge	Qrr		-	333	-	nC

Notes:

1. EAS condition : Tj=25℃,V_{DD}=40V,V_G=10V,L=0.5mH,Rg=25Ω
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)}$=175°C. The SOA curve provides a single pulse rating.
4. The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25° C. The maximum allowed junction temperature of 175° C. The value in any given application depends on the user's specific board design.

Typical Electrical and Thermal Characteristics

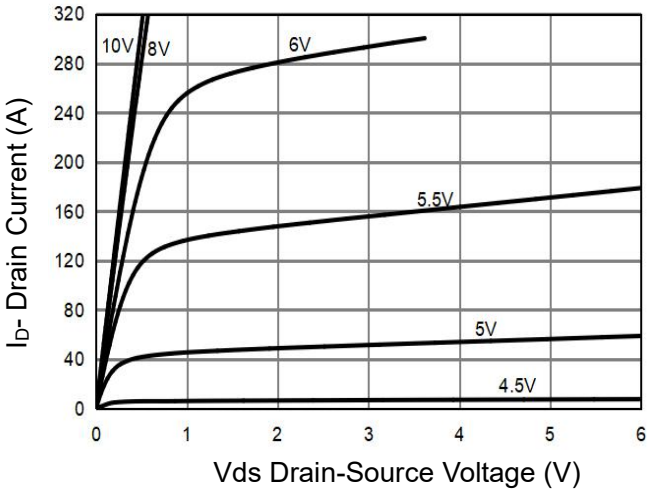


Figure 1 Output Characteristics

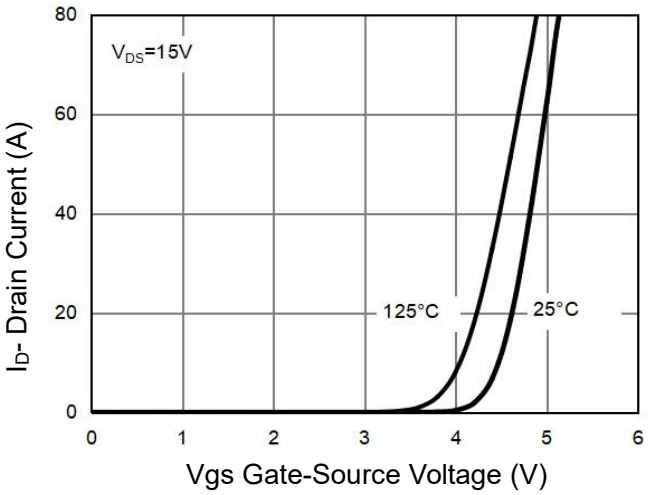


Figure 2 Transfer Characteristics

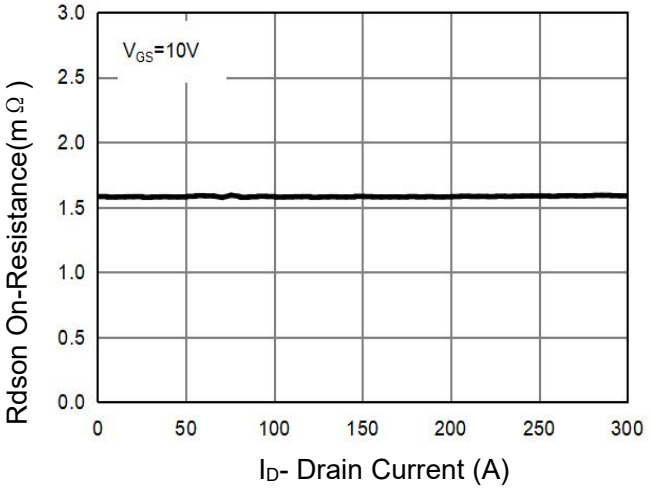


Figure 3 Rdson- Drain Current

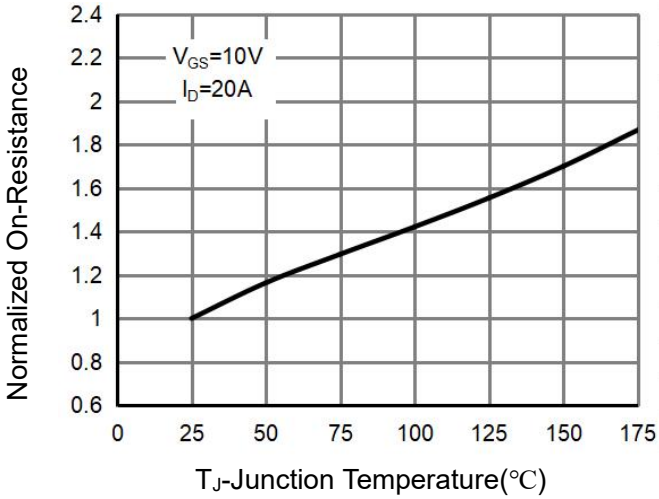


Figure 4 Rdson-Junction Temperature

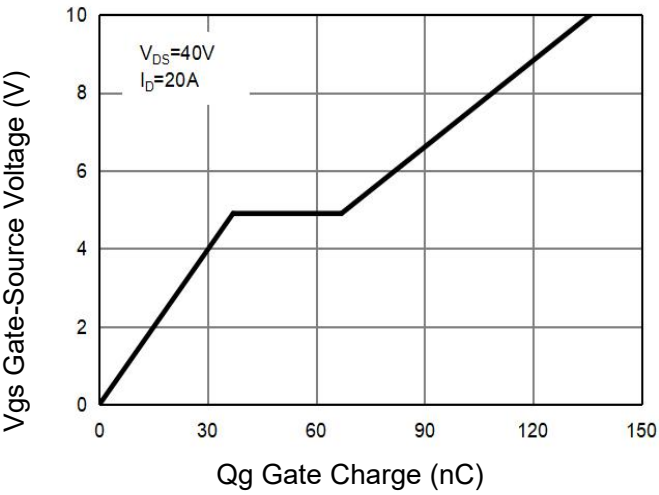


Figure 5 Gate Charge

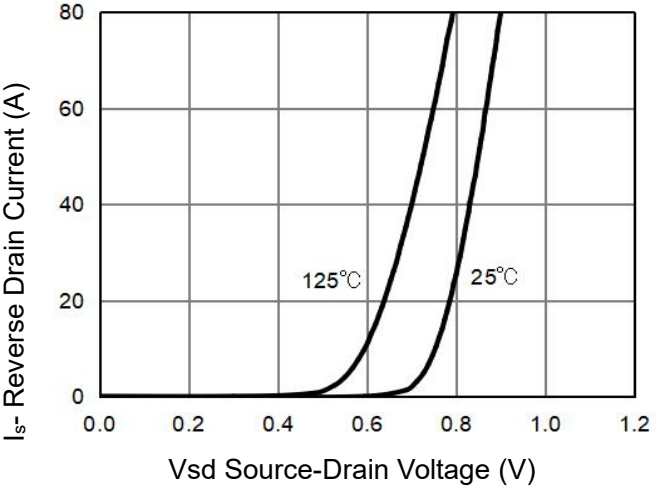


Figure 6 Source- Drain Diode Forward

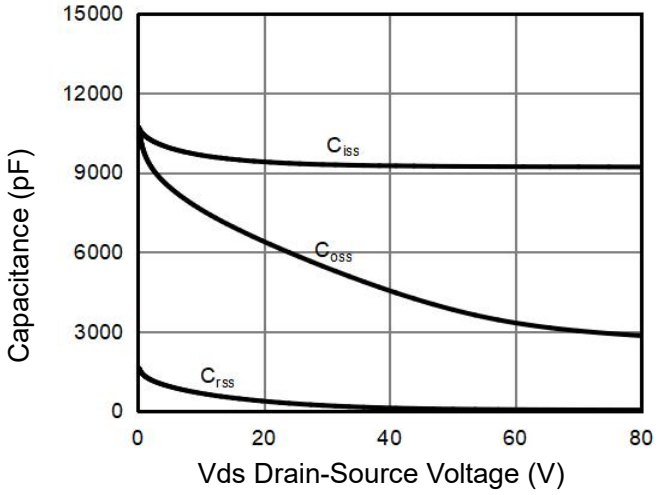


Figure 7 Capacitance vs Vds

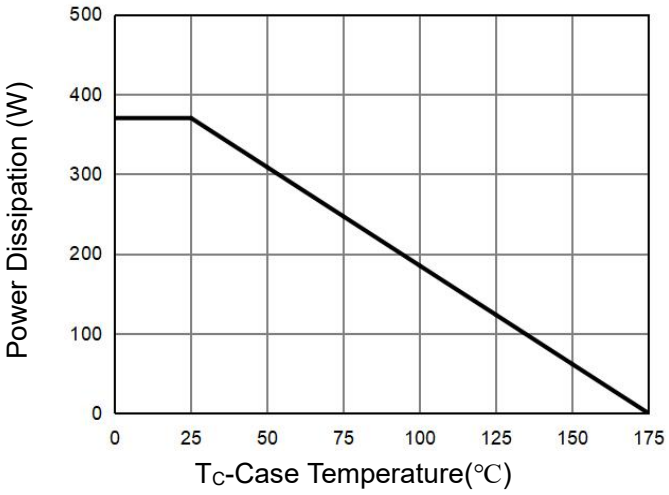


Figure 9 Power De-rating

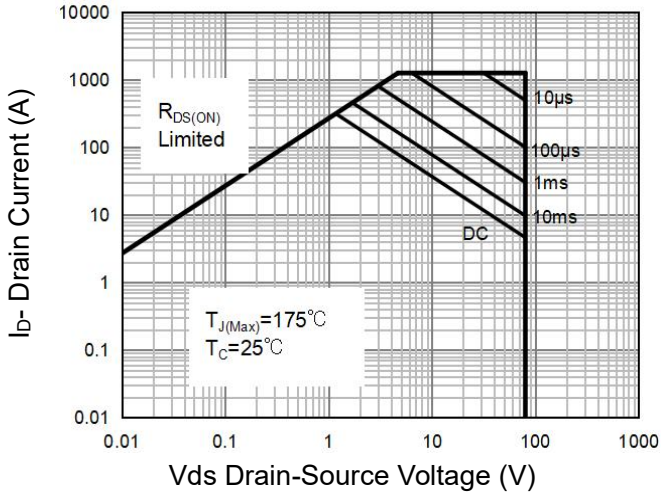


Figure 8 Safe Operation Area(Note 3)

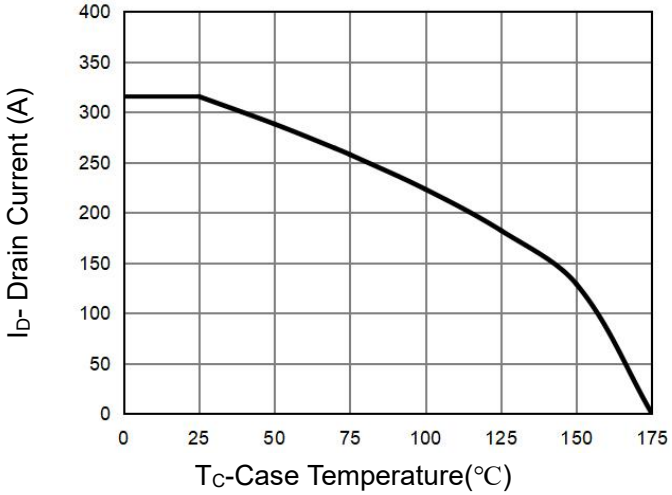


Figure 10 Current De-rating

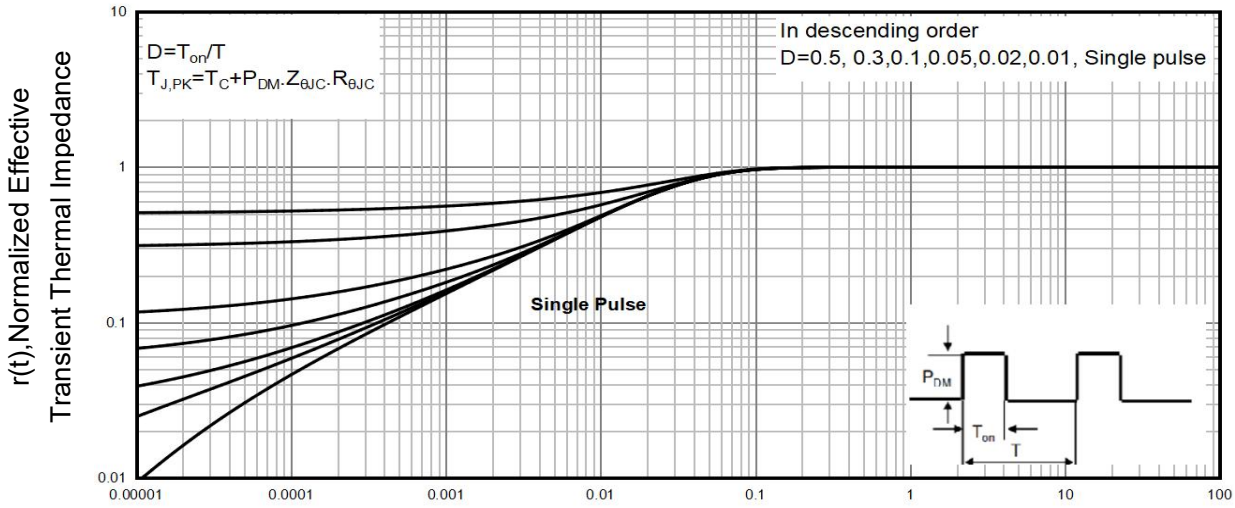
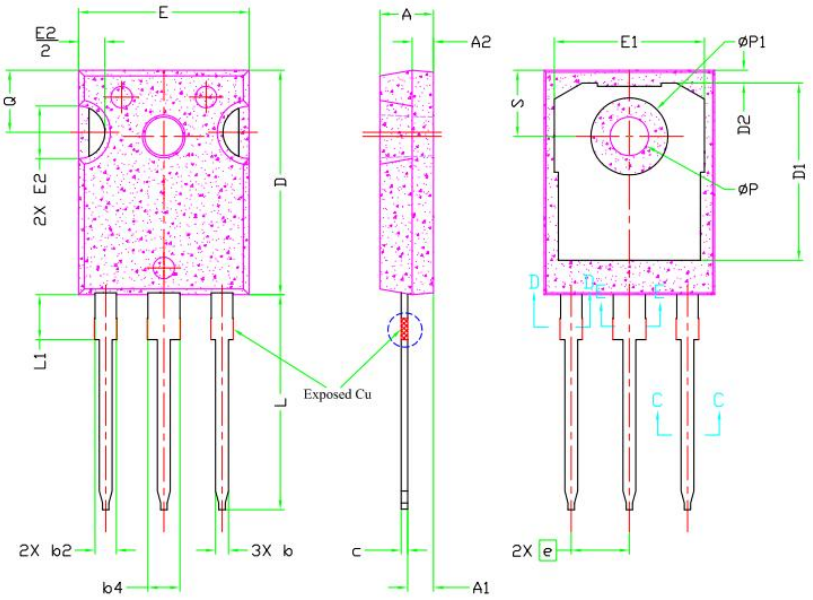
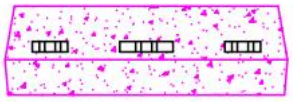
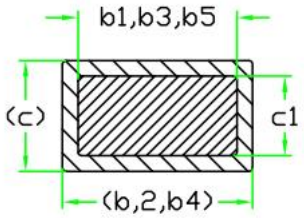


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-247-3L Package Information







Section C--C,D--D,E--E

SYMBOL	DIMENSIONS			NOTES
	MIN.	NOM.	MAX.	
A	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.50	2.00	2.49	
b	1.12	1.20	1.33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	6
b3	1.91	2.00	2.34	
b4	2.87	3.00	3.22	6, 8
b5	2.87	3.00	3.18	
c	0.55	0.60	0.69	6
c1	0.55	0.60	0.65	
D	20.80	20.95	21.10	4
D1	16.25	16.55	17.65	5
D2	0.51	1.19	1.35	
E	15.75	15.94	16.13	4
E1	13.46	14.02	14.16	5
E2	4.32	4.91	5.49	3
e	5.44BSC			
L	19.81	20.07	20.32	
L1	4.10	4.19	4.40	6
ØP	3.56	3.61	3.65	7
ØP1	7.19REF.			
Q	5.39	5.79	6.20	
S	6.04	6.17	6.30	

Revision History

Revision	Date	Subjects
V1.0	2025.12.09	Product data sheet

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